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Abstract of the Disclosure

A LCD vertical scrolling mechanism automatically tracks addresses of information scrolled on a LCD (30). A counter (12) is initialized to a value latched in a vector register when a frame signal is received. Subsequent BPCLK signals step the adder through a series of values. These values are relayed through two bus selectors (18, 22) to segment drivers (34) for the LCD. One of the bus selectors (22) is coupled to the counter in parallel with a subtracter (20). When a value from the counter exceeds a predetermined value equal to the MUX of the LCD, the subtracter takes the difference between the predetermined MUX value and the value received from the counter and directs the parallel bus selector to relay the difference to the RAM of a segment driver. An adder (16) is coupled to the other bus selector (18) and to the vector register (26). When the MCU (42) needs to fetch information from the segment drivers, the MCU relays a LCD address where the information is displayed, to the adder. The adder adds the address (a value) to the value latched in the vector register. The MCU directs the second bus selector to select the value determined in the adder and relay this address to the segment driver.

Figs. 1 and 2 accompanies this abstract.

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