A HIGH Q INDUCTOR REALIZATION FOR USE IN MMIC CIRCUITS

ABSTRACT

Methods and structures are disclosed which realize high Q inductors to provide on-chip noise matching of microwave monolithic integrated circuits, such as low noise amplifiers and output matching networks for power amplifiers. High Q inductors, of typically 6nH, are devised by using the inductance of package leads, bondwires, electronic board trace wires and transmission lines and other components in various configurations. These and other components are serially connected starting from a chip pad, representing a circuit node, to a circuit input on a that electronic board.

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(Fig. 5)