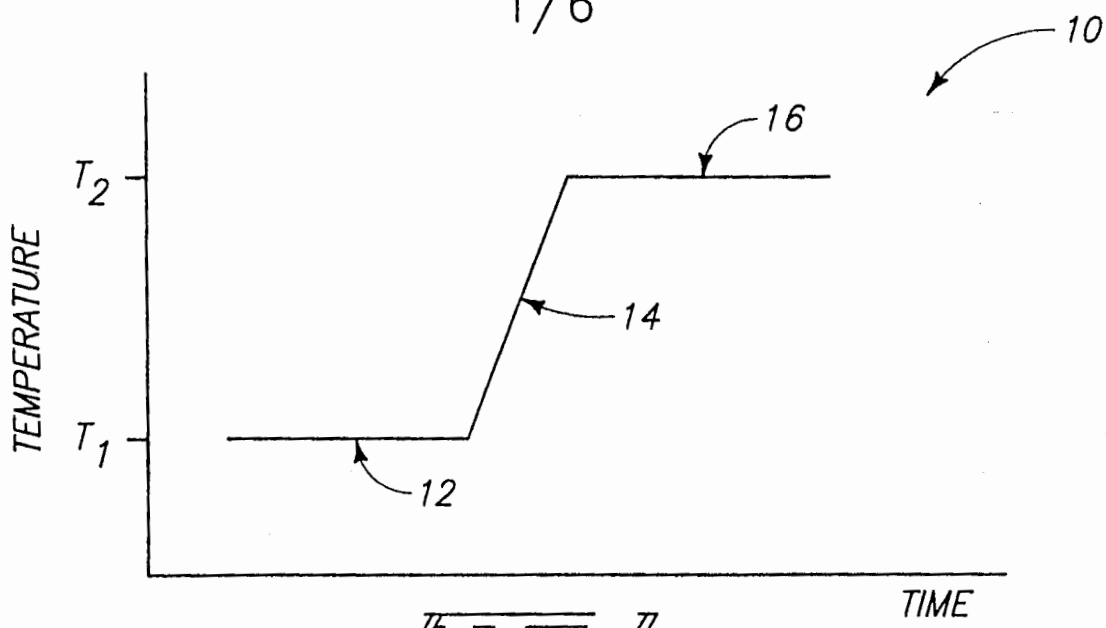


Fig 1
PRIOR ART

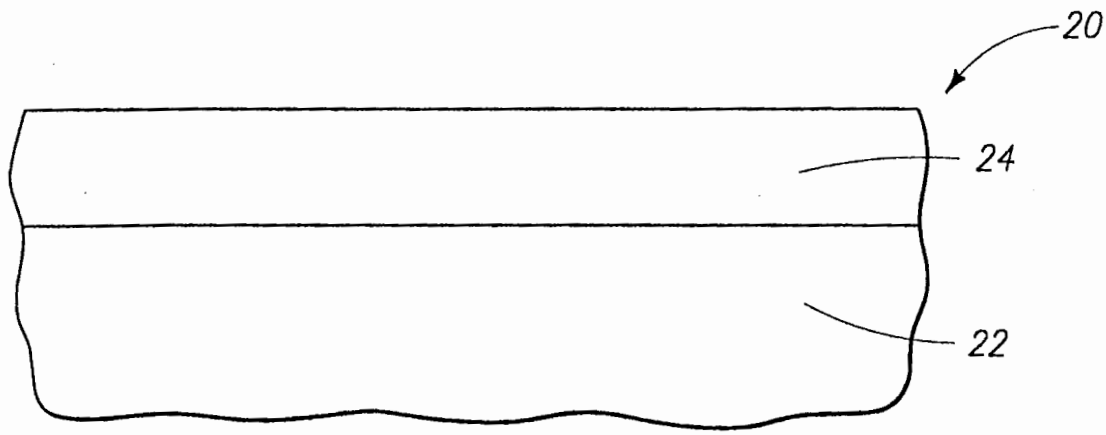


Fig 2
PRIOR ART

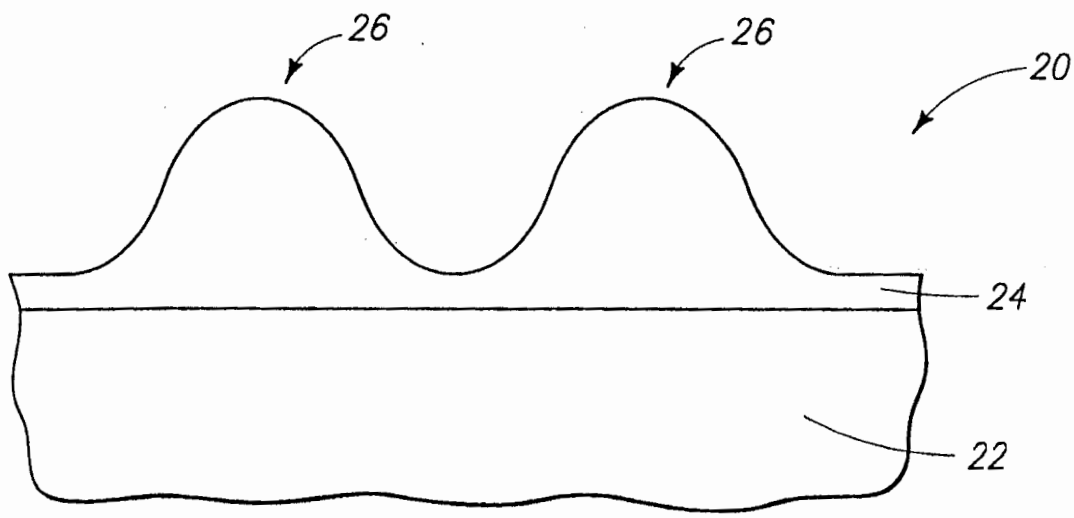


Fig 3
PRIOR ART

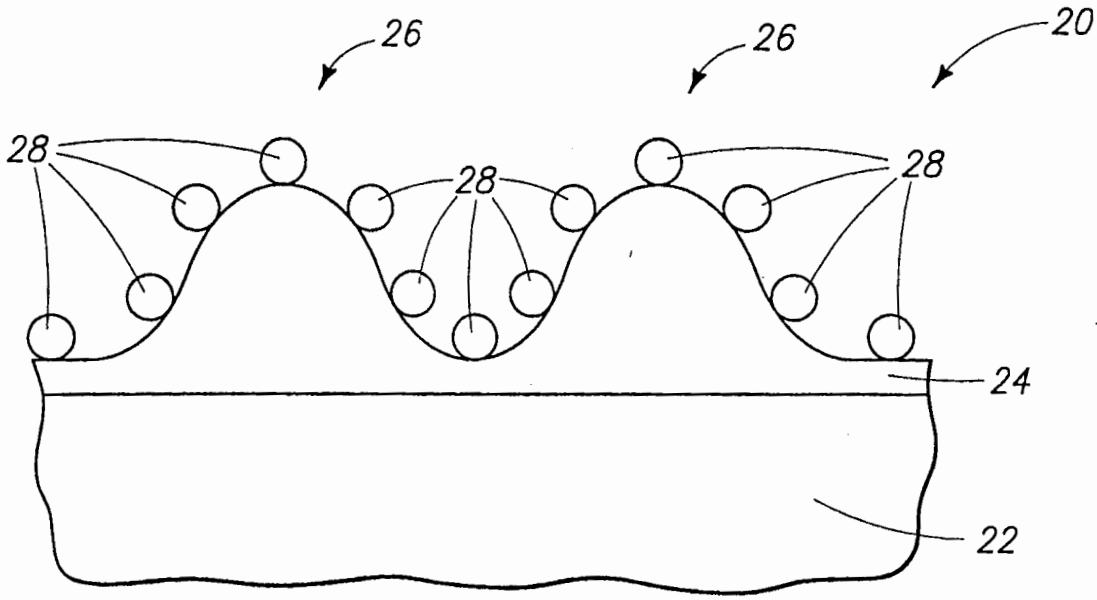


FIG 4
PRIOR ART

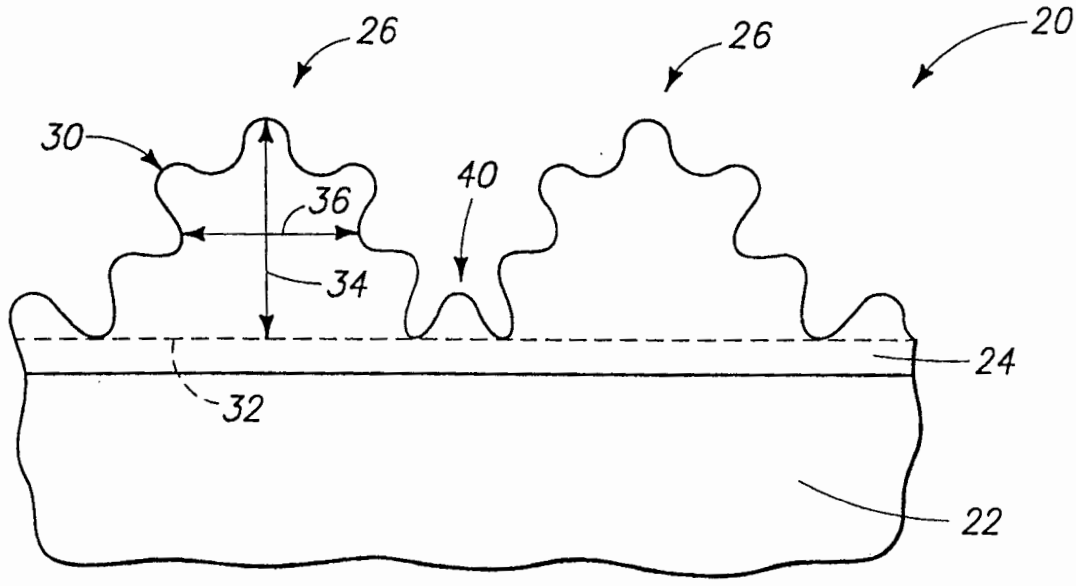


FIG 5
PRIOR ART

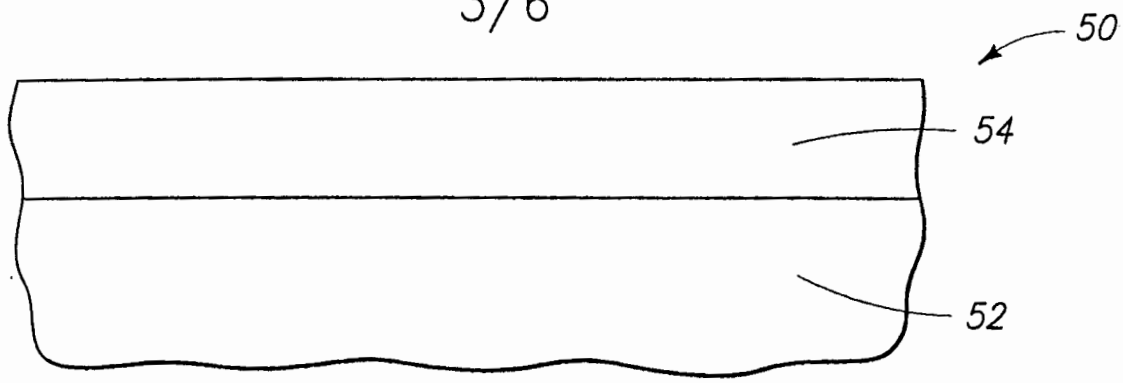


Fig 6

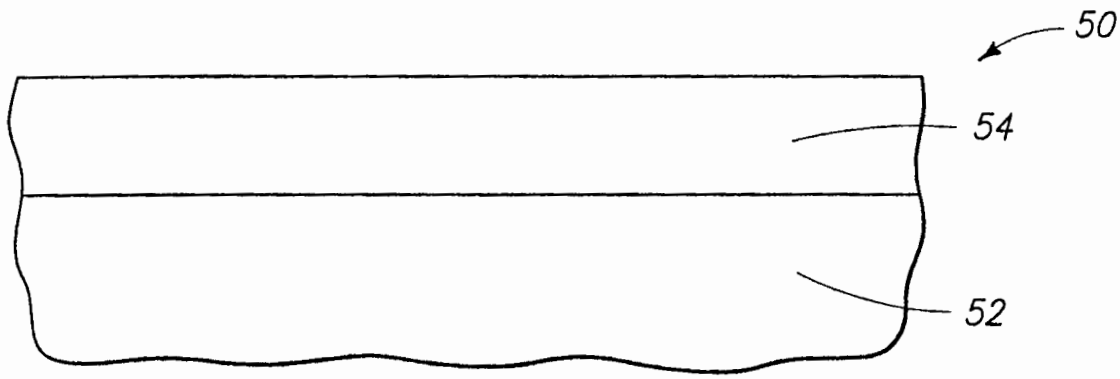


Fig 7

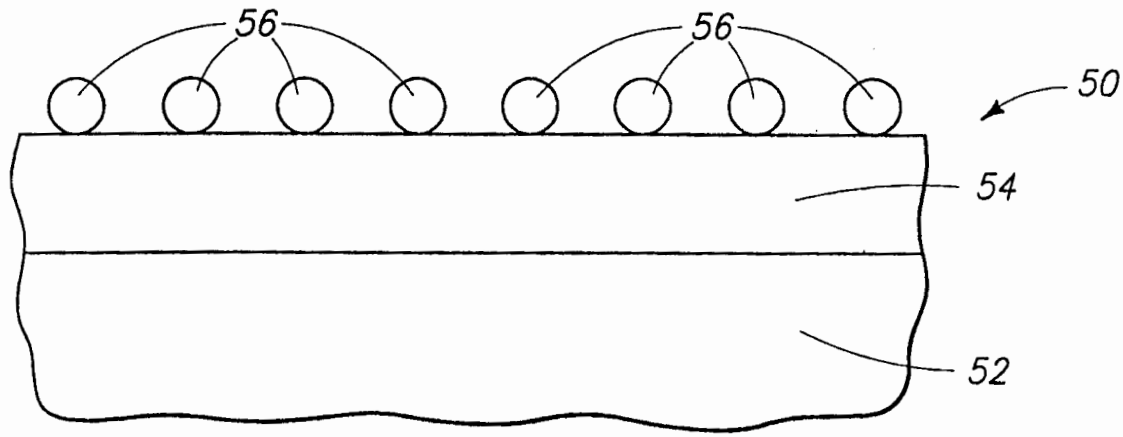


Fig 8

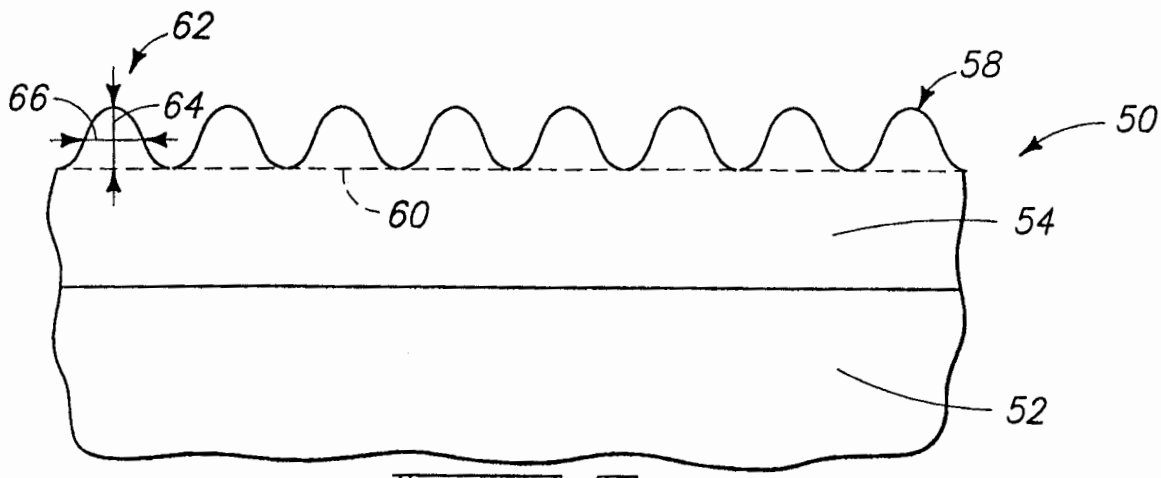


Fig 9

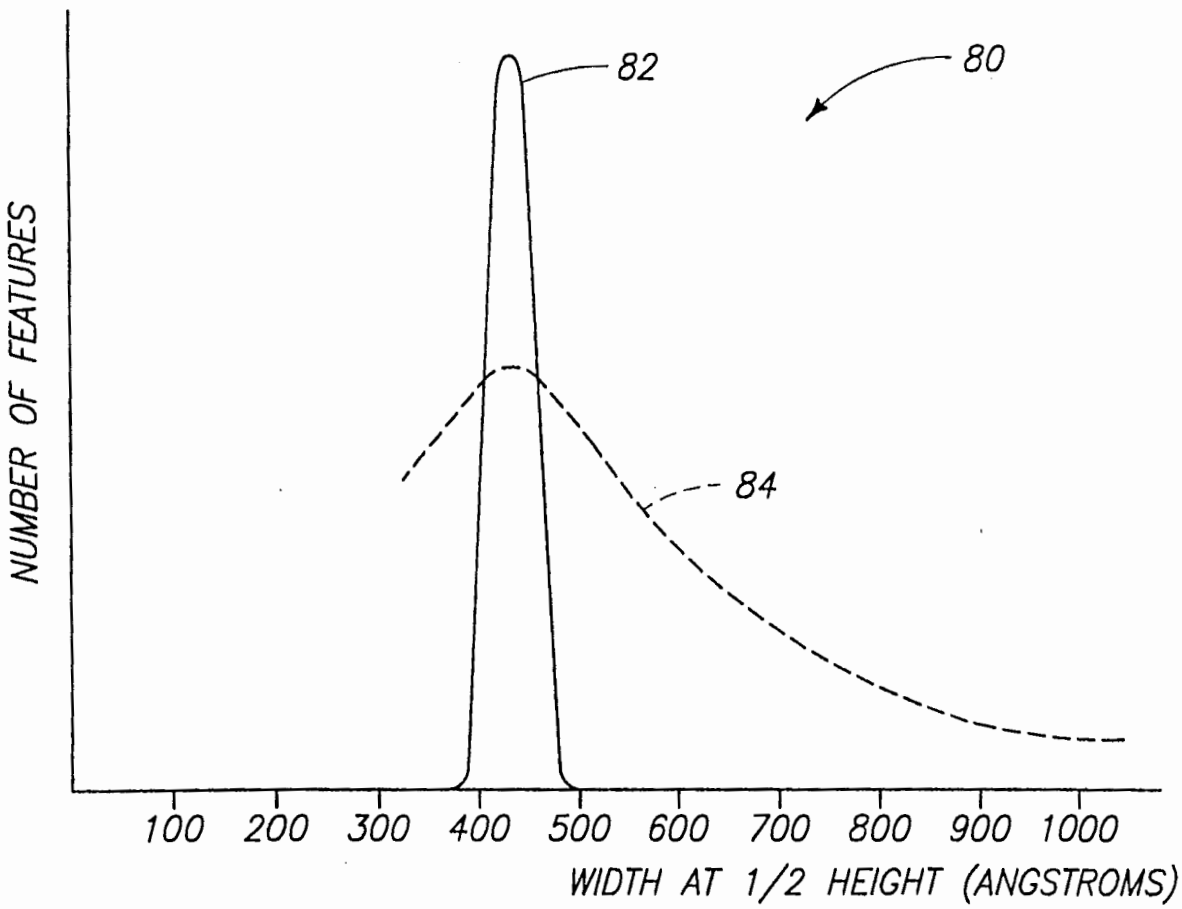

82
80
NUMBER OF FEATURES
84
100 200 300 400 500 600 700 800 900 1000
WIDTH AT 1/2 HEIGHT (ANGSTROMS)

Fig. 10

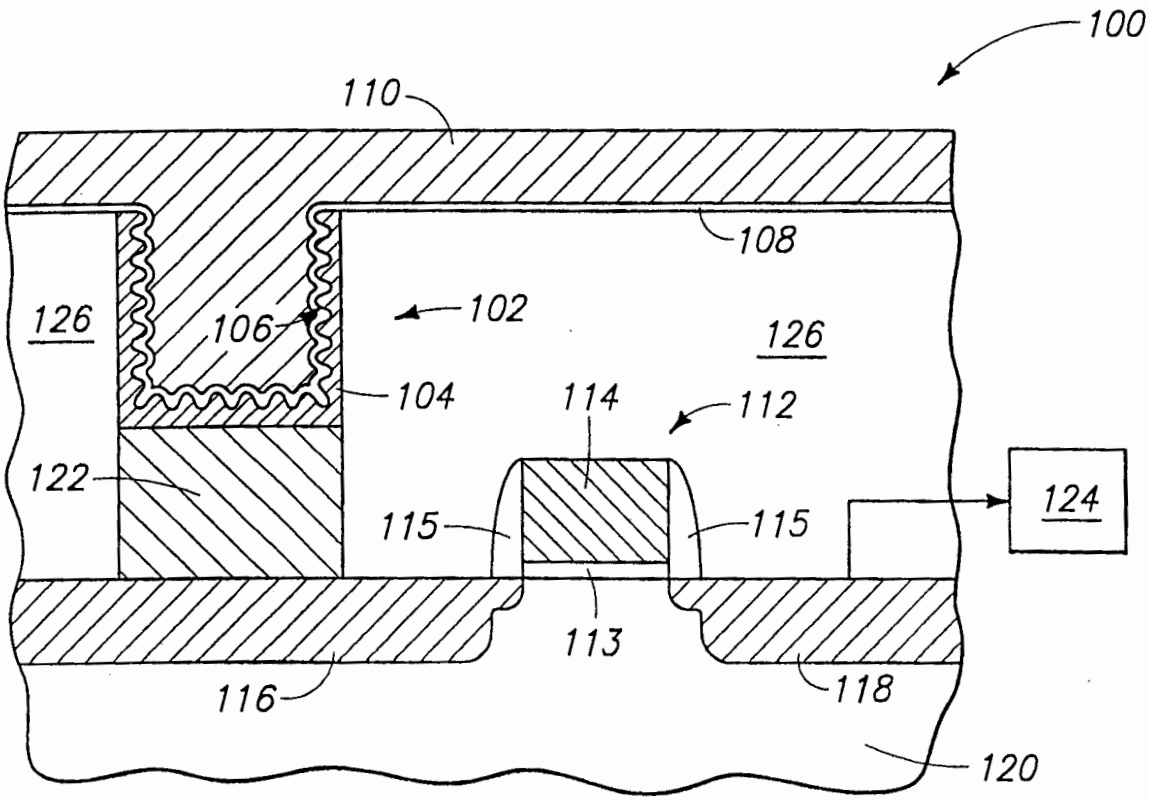

100
110
108
126
106
102
126
104
114
112
122
124
115
115
113
116
118
120

Fig. 11

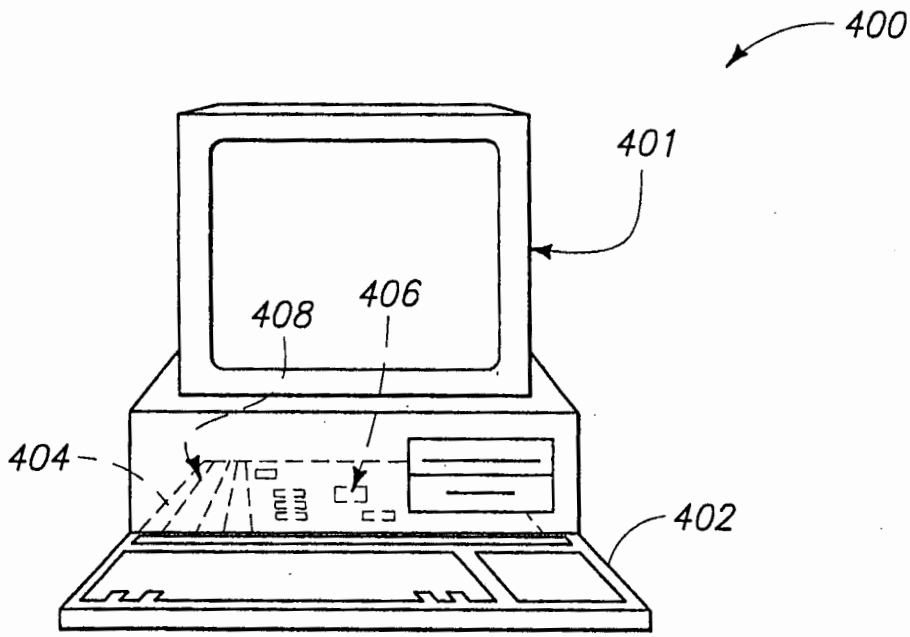


Fig 12

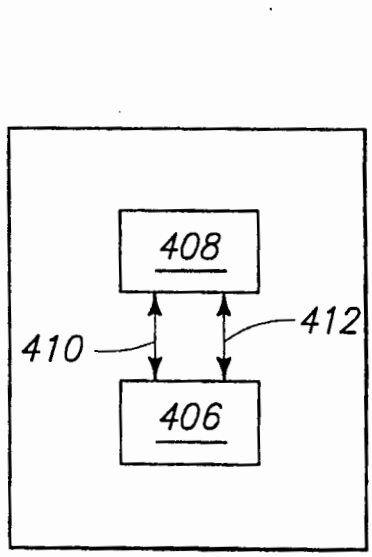


Fig 13

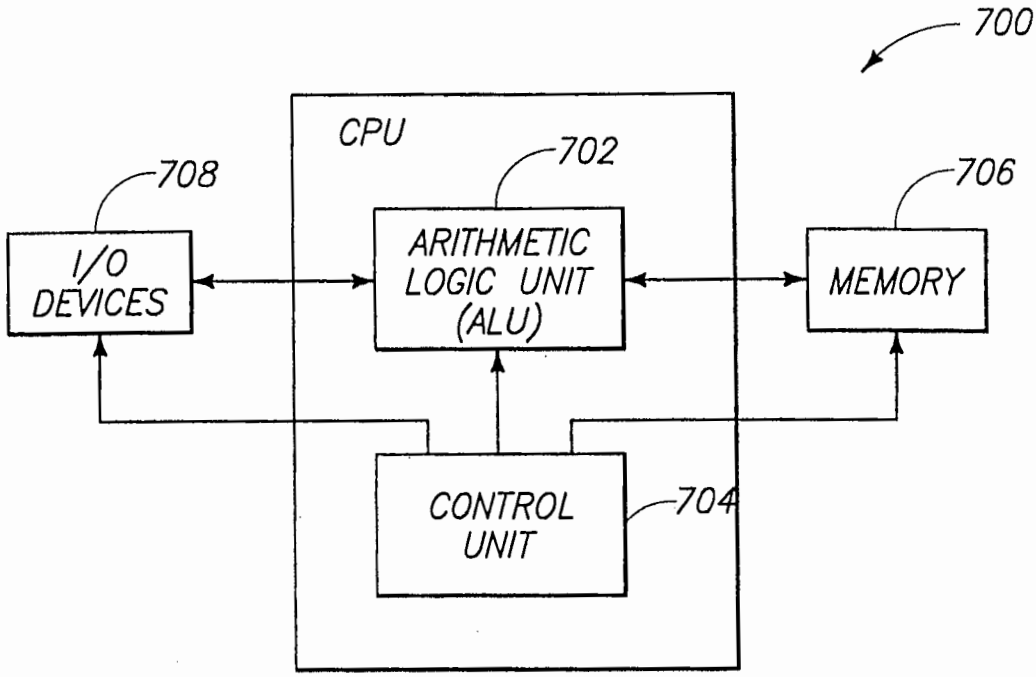

700
CPU
702
708
706
I/O DEVICES
ARITHMETIC LOGIC UNIT (ALU)
MEMORY
CONTROL UNIT
704

Fig 14

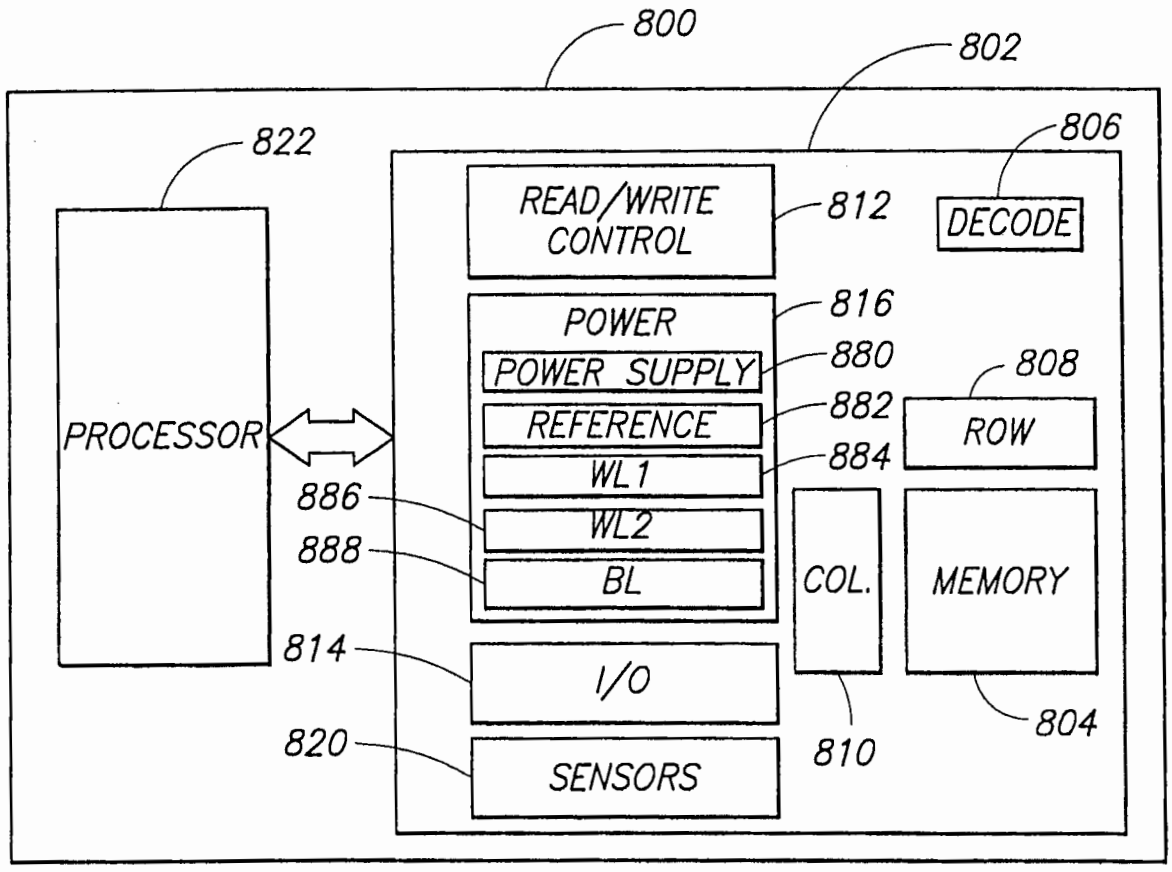

800
802
822
806
READ/WRITE CONTROL
812
DECODE
816
POWER
POWER SUPPLY
880
808
REFERENCE
882
ROW
PROCESSOR
884
WL1
886
WL2
888
BL
COL.
MEMORY
814
I/O
804
820
SENSORS
810

Fig 15