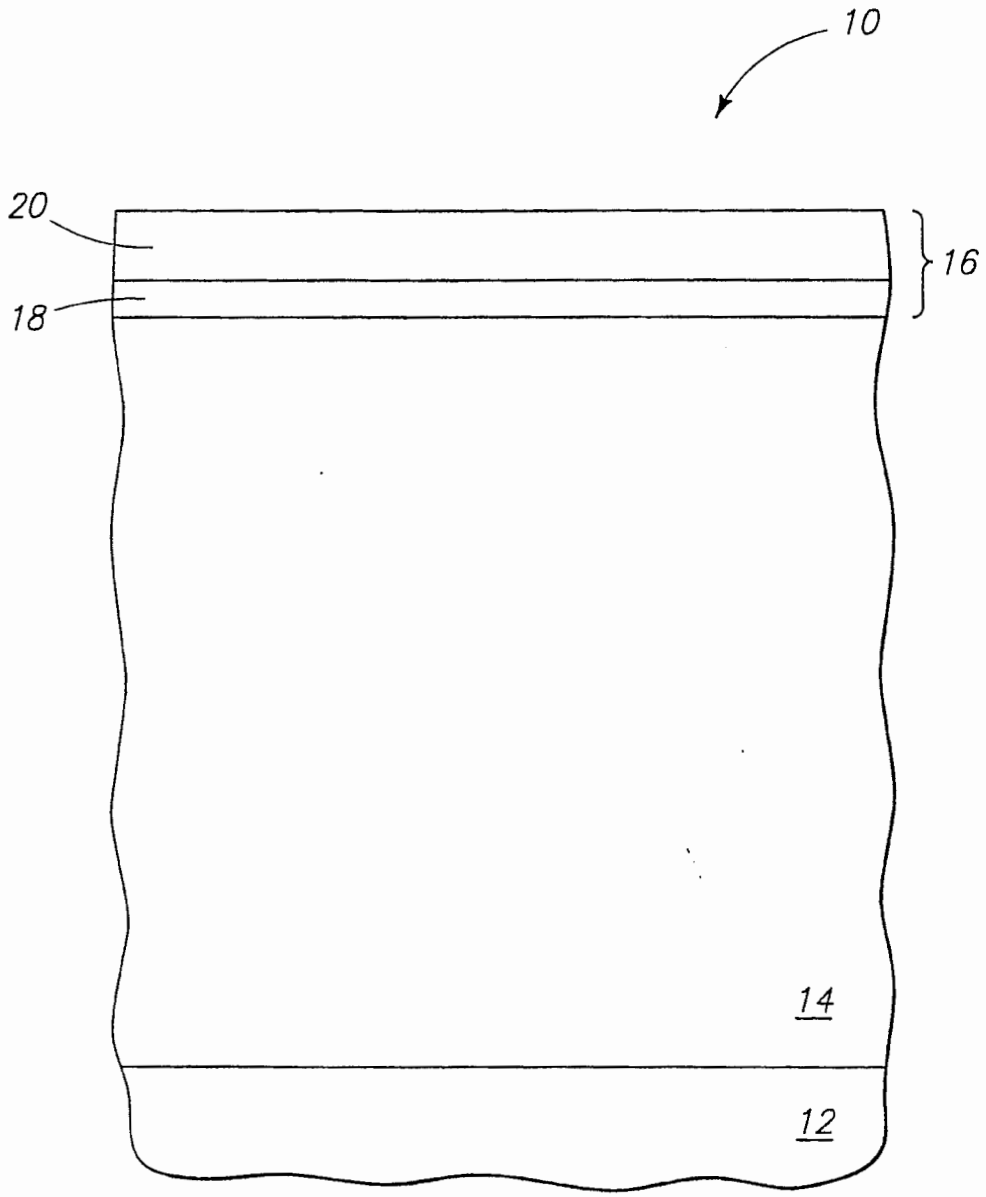


Fig. 1

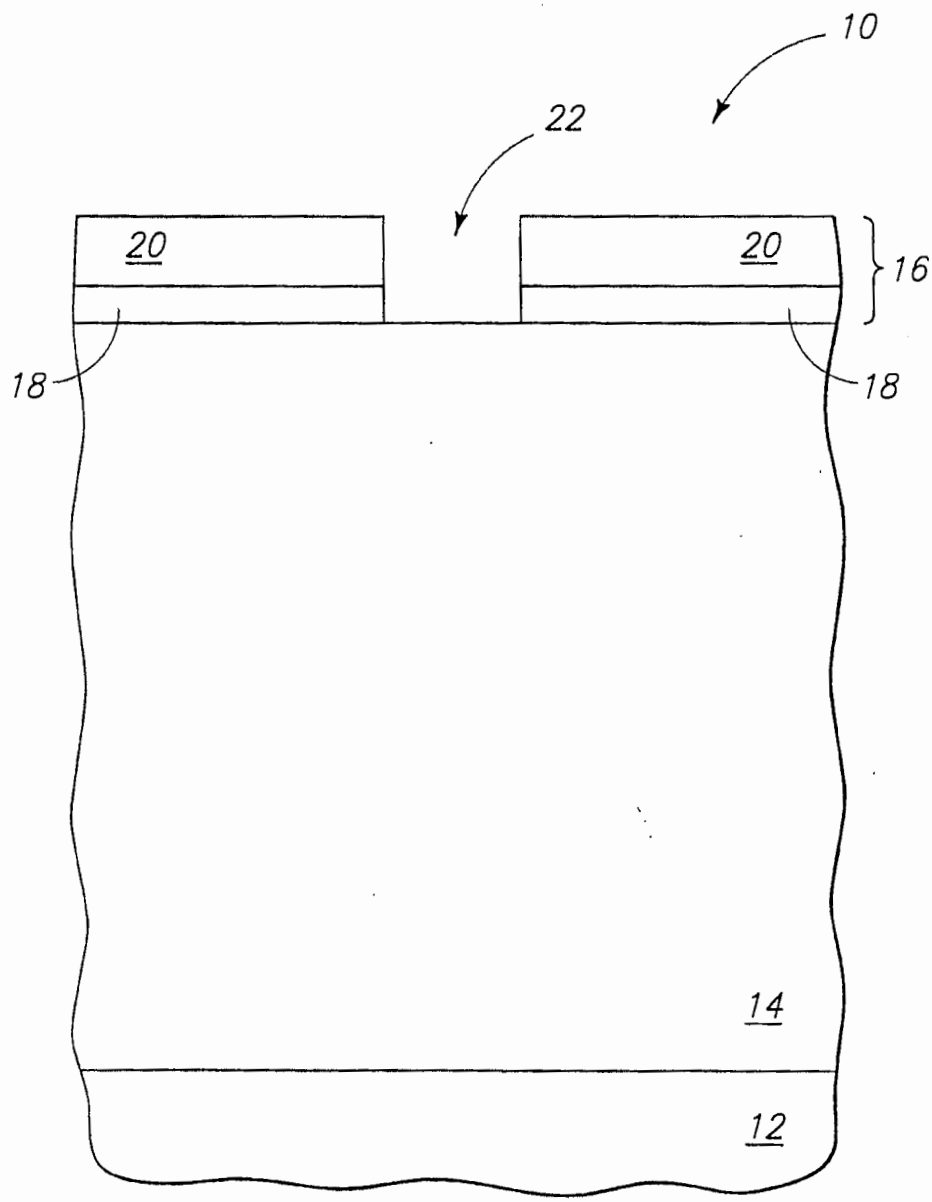


Fig 2

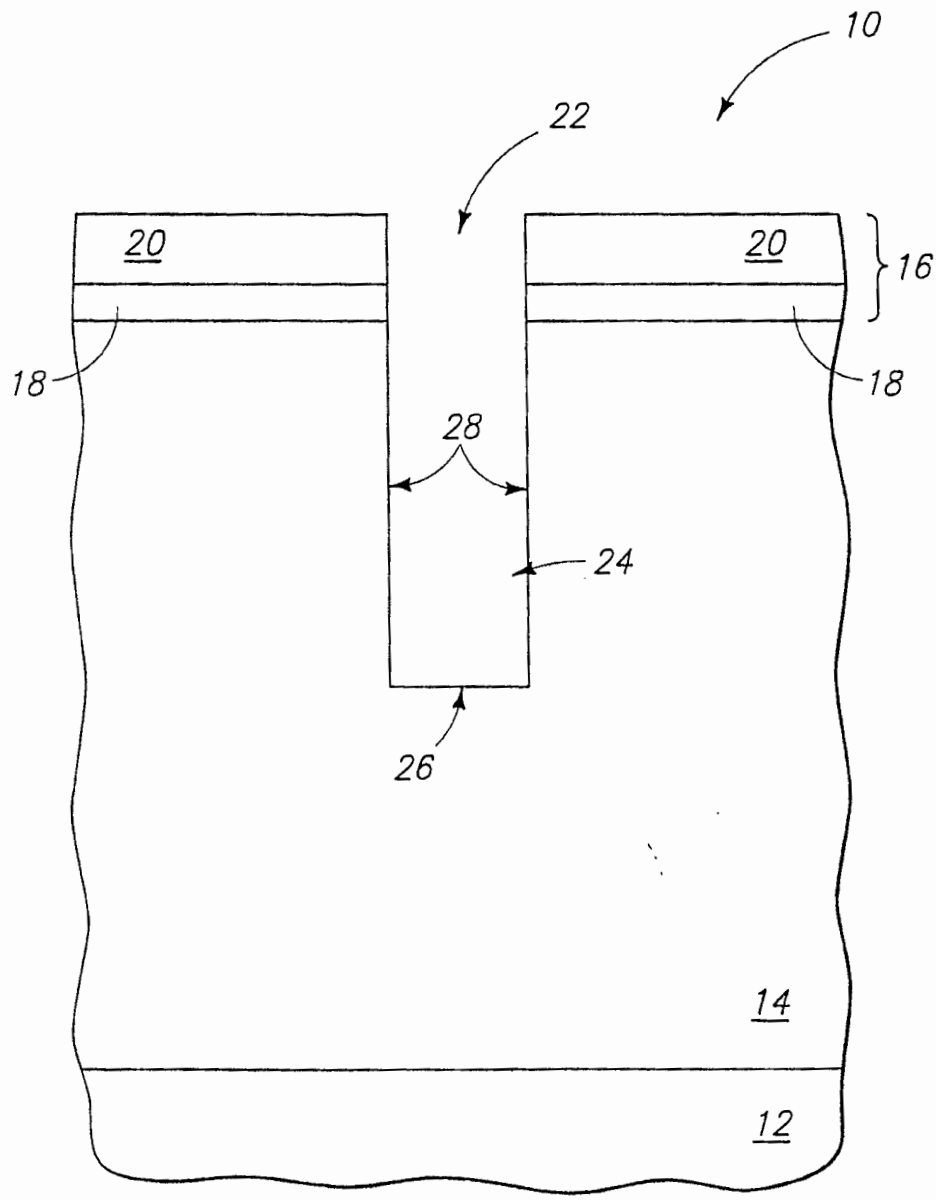


Fig 3

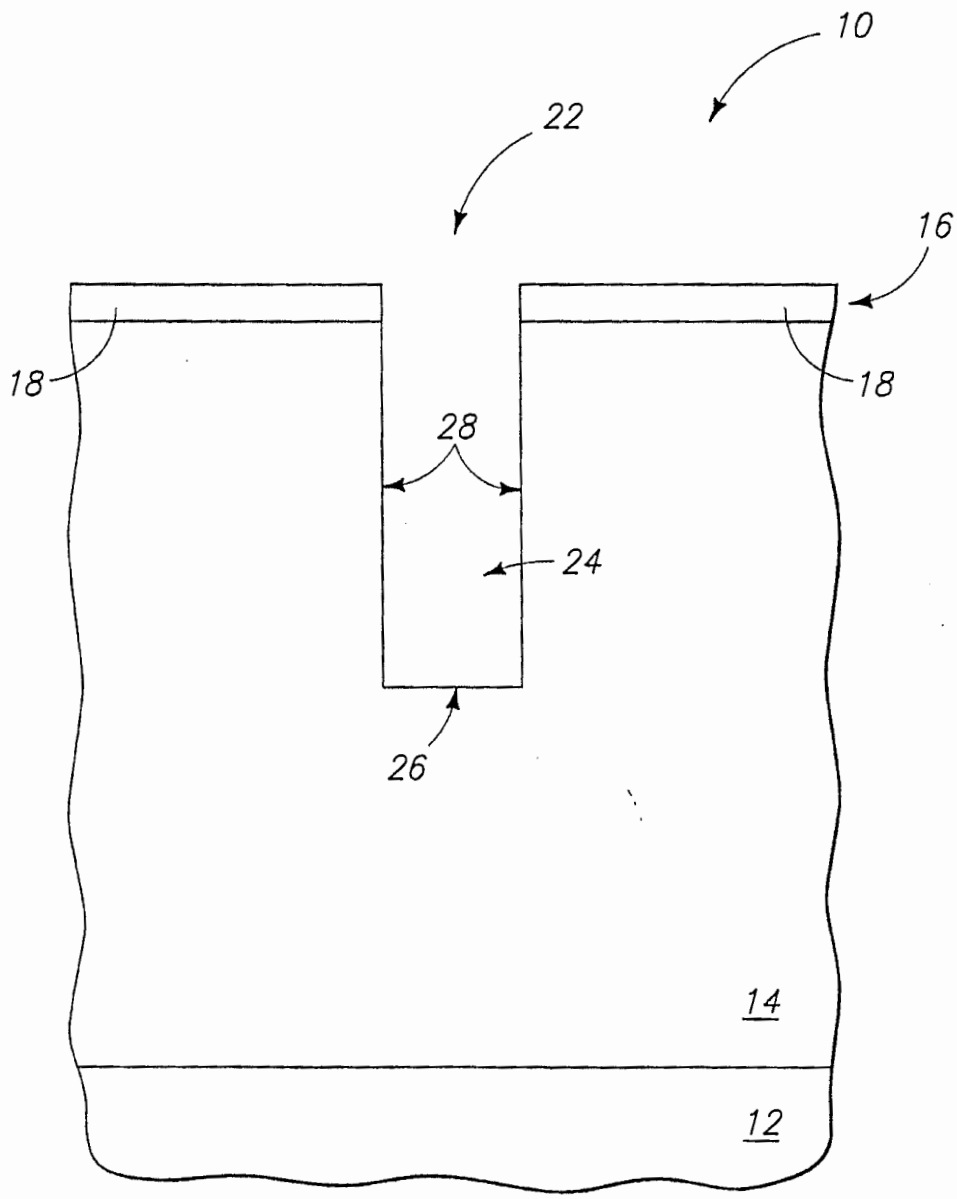


Fig 4

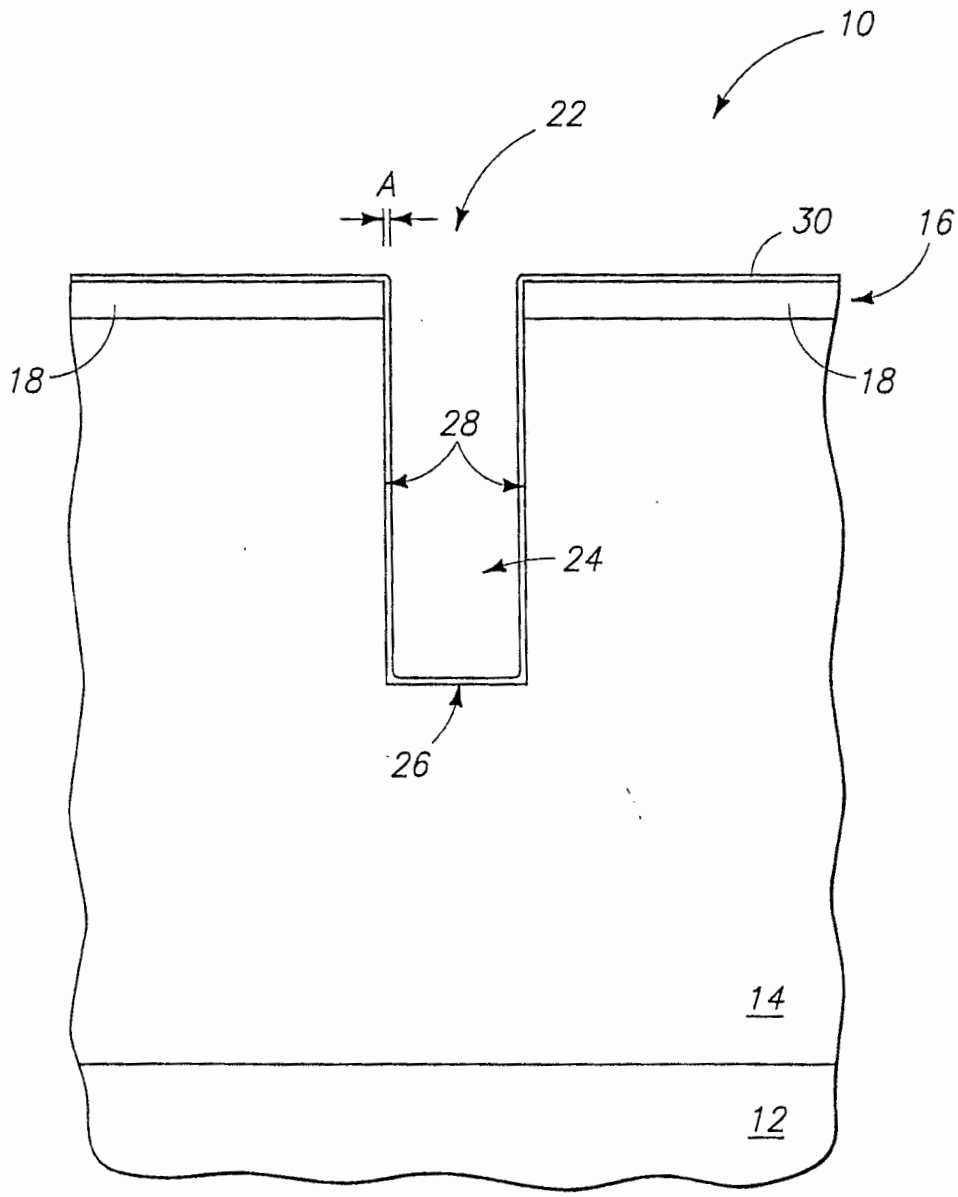


Fig 5

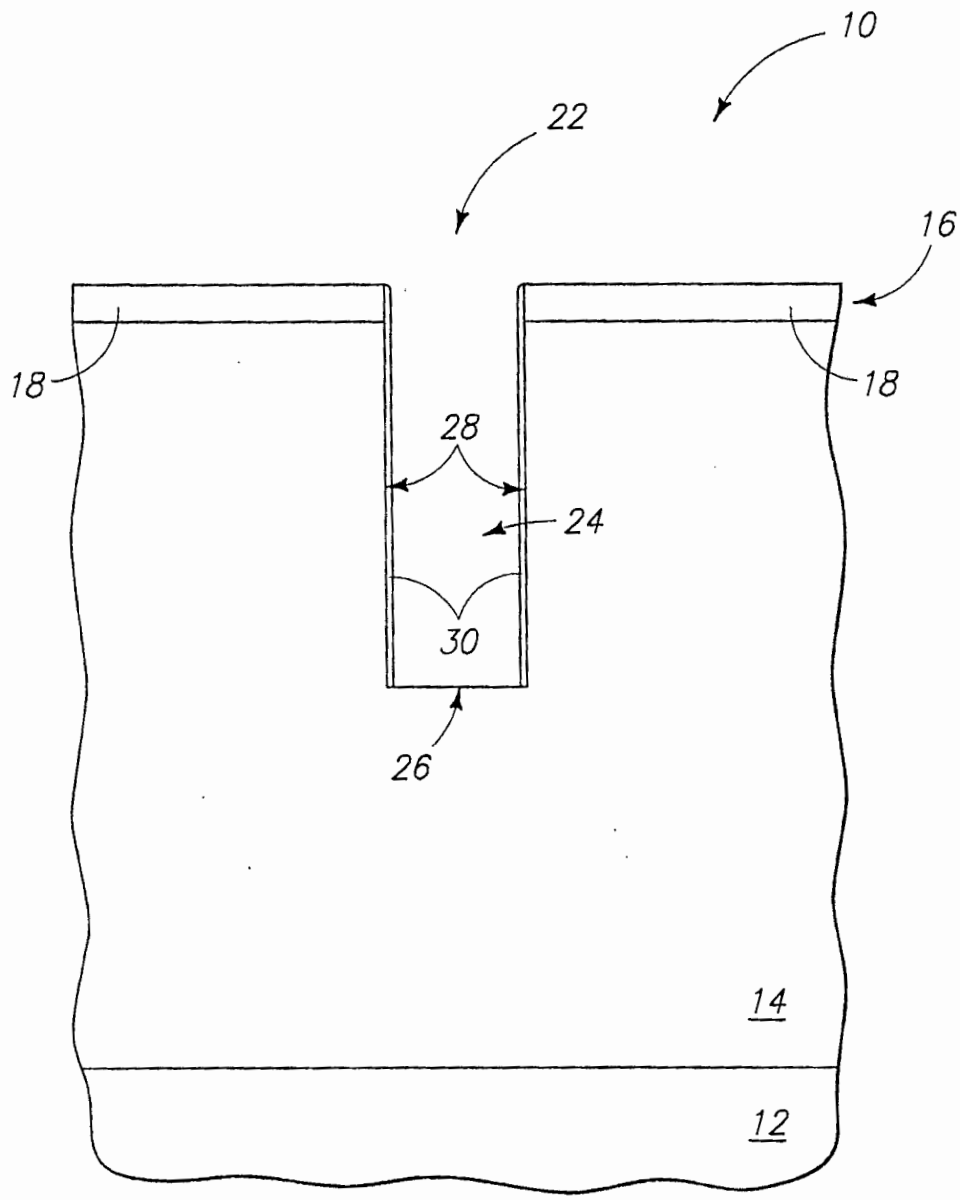


Fig 6

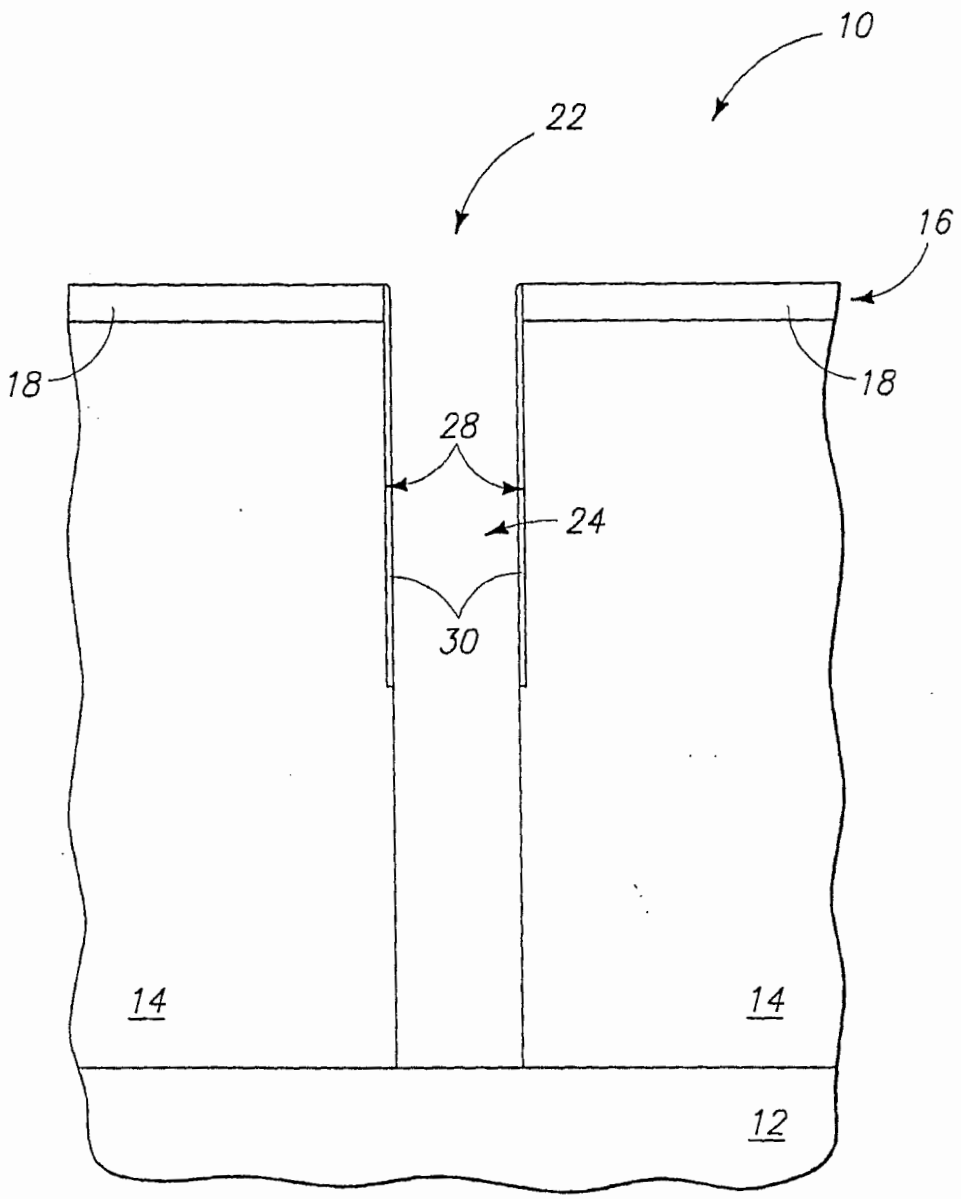


Fig. 11

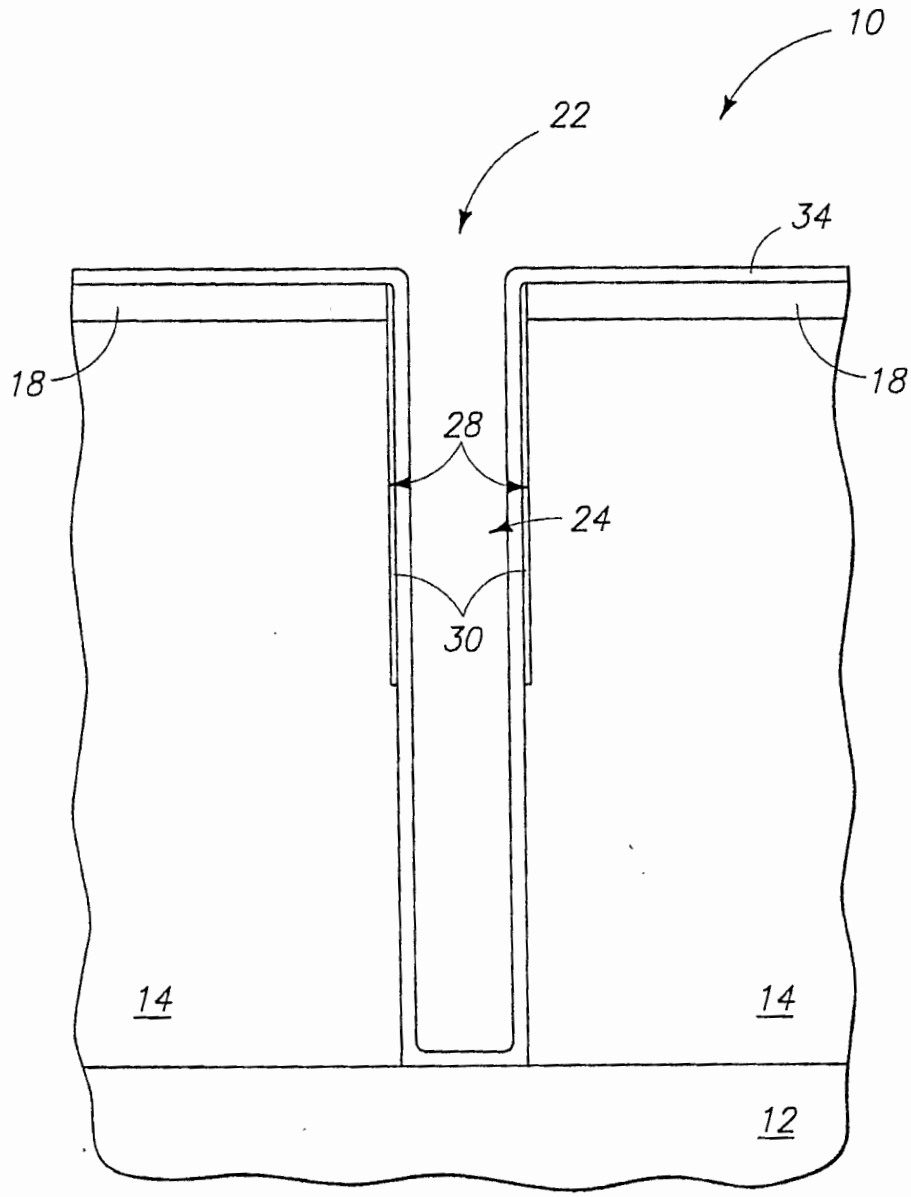


Fig. 8B

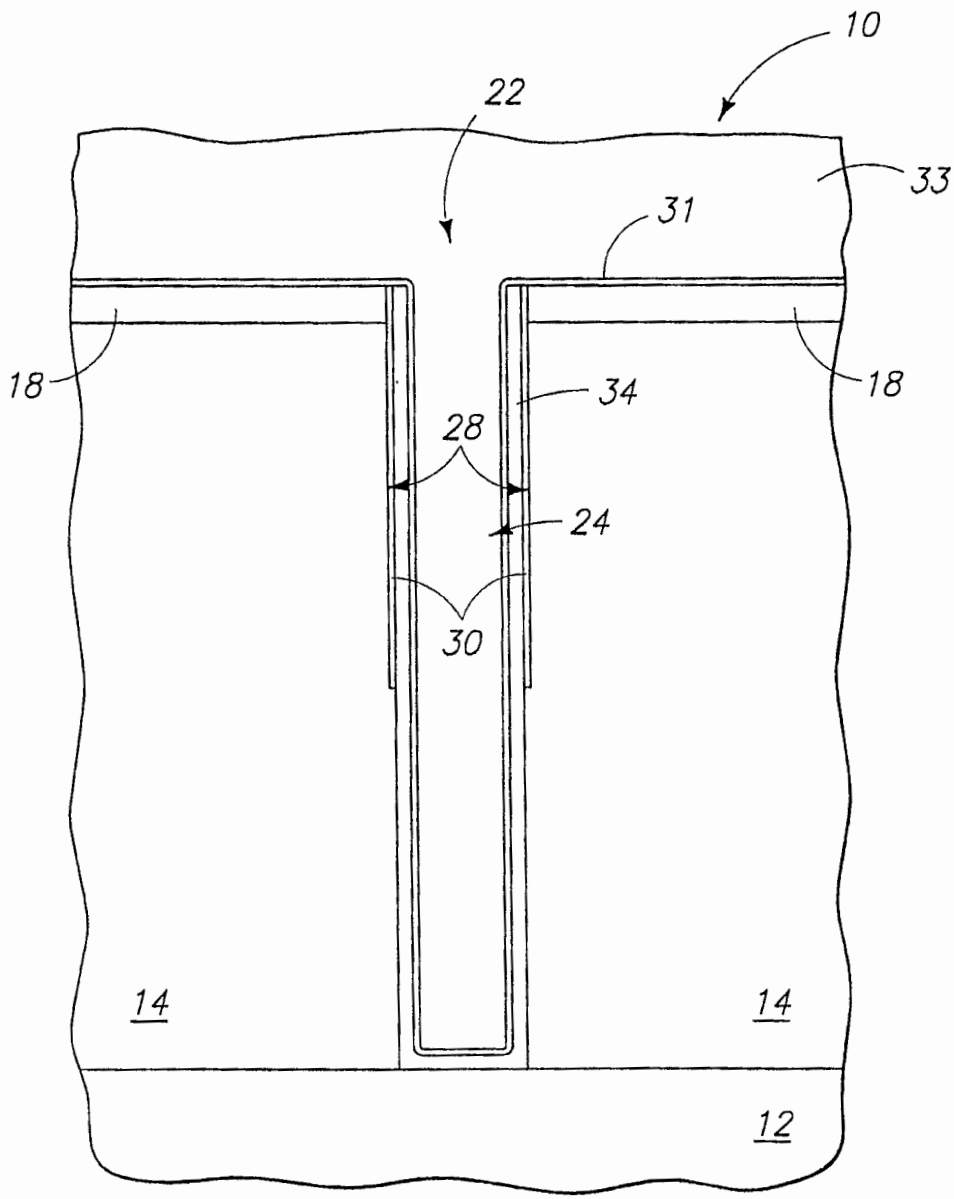


Fig 9

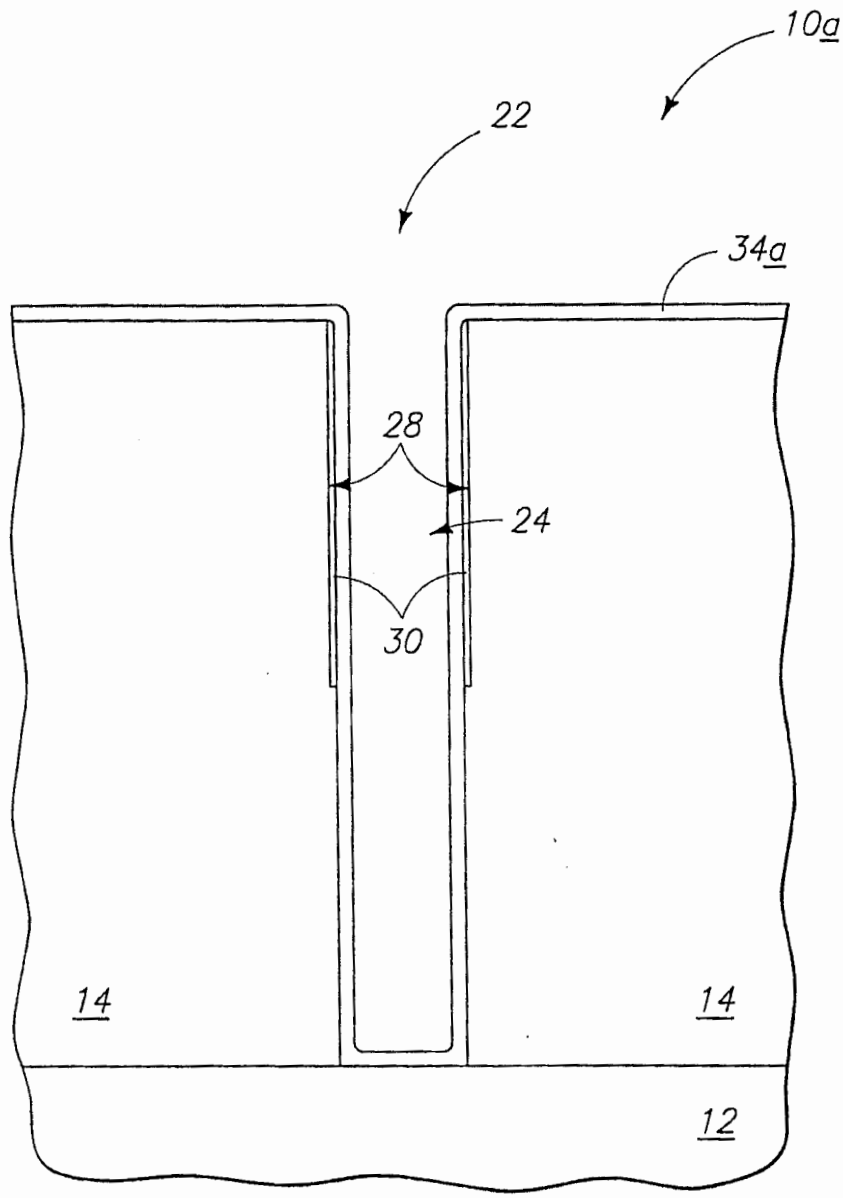


Fig 10

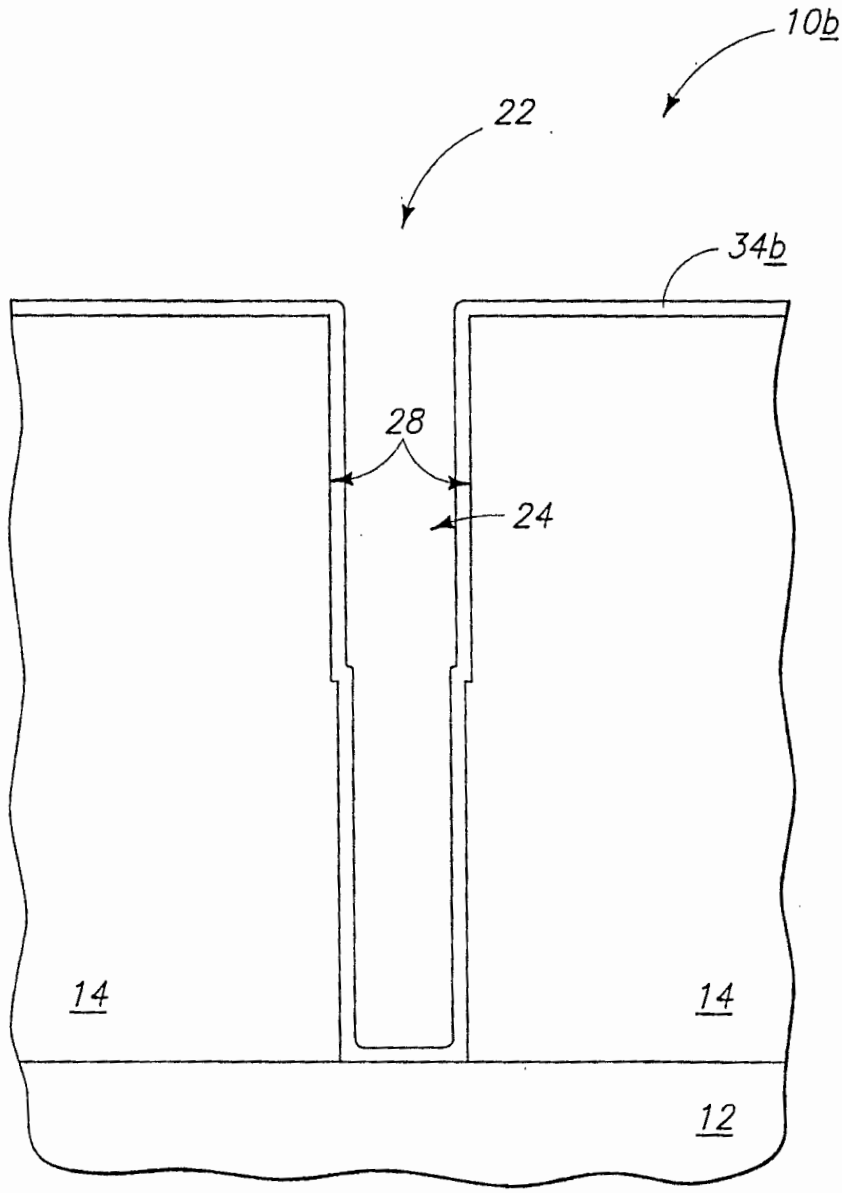


Fig 11

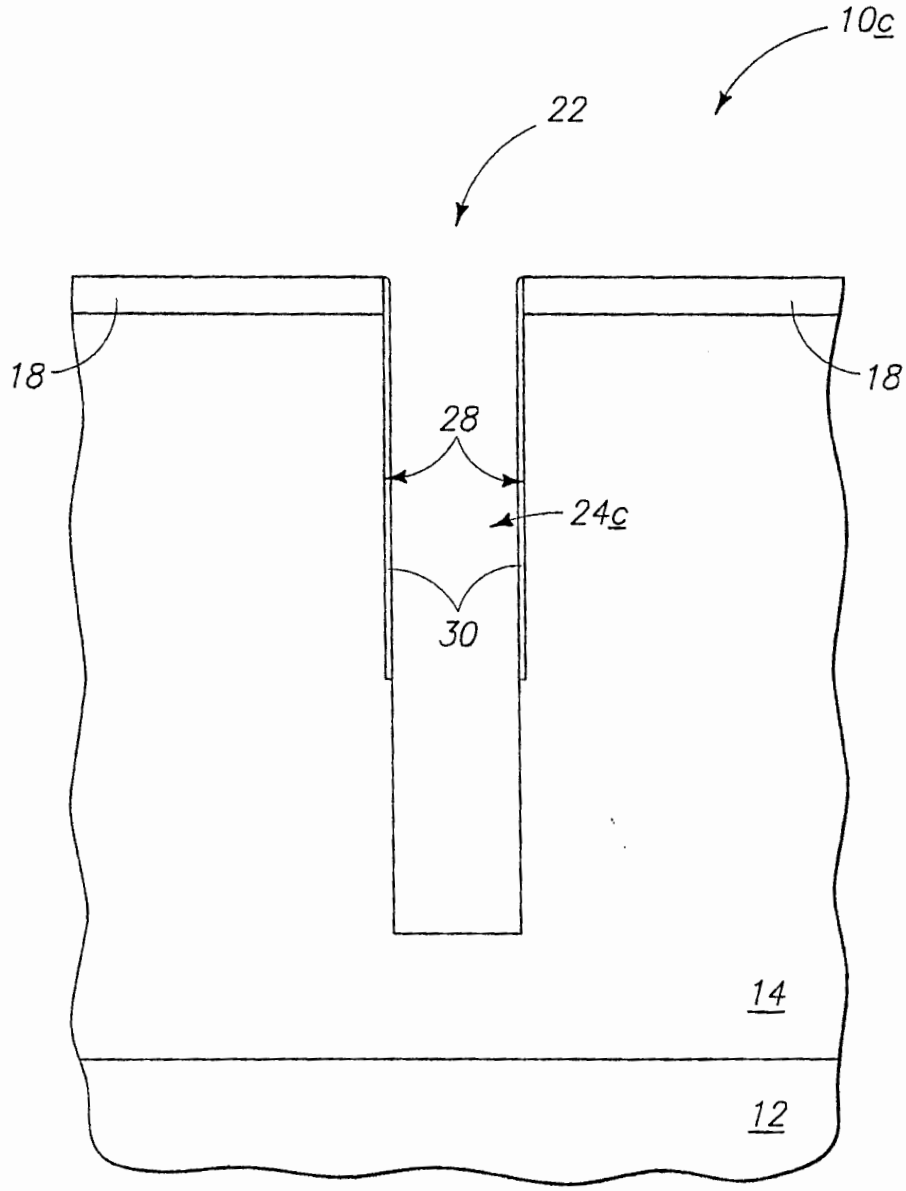


Fig. 12

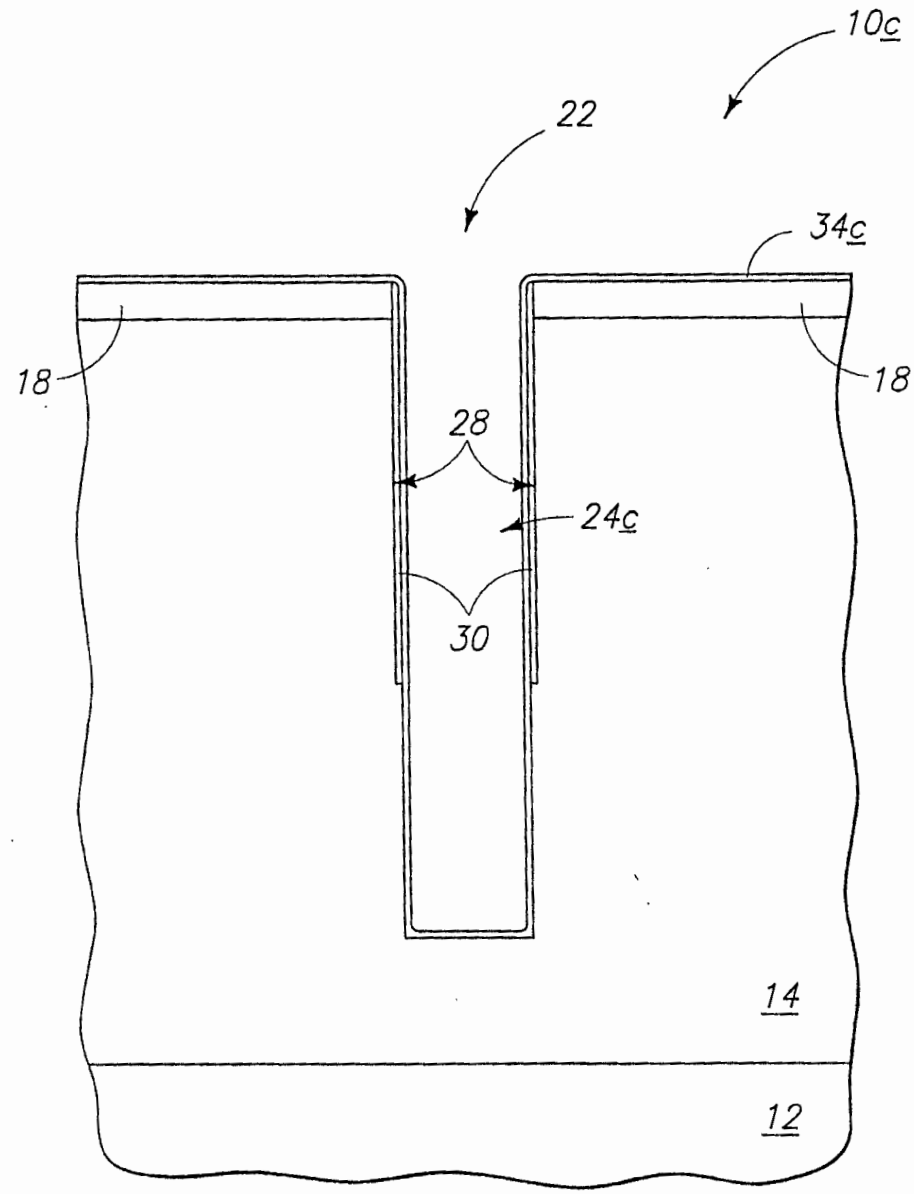


FIG 13

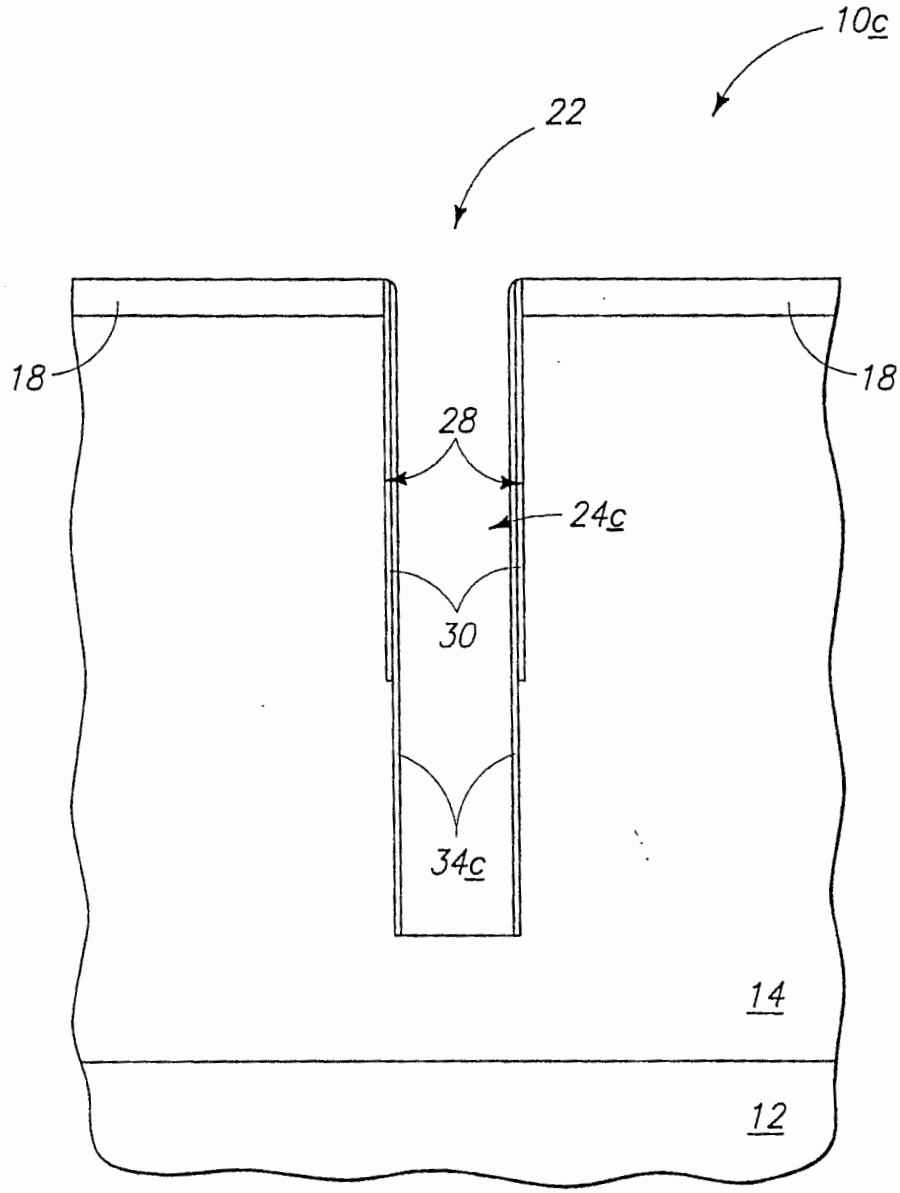


Fig. 14

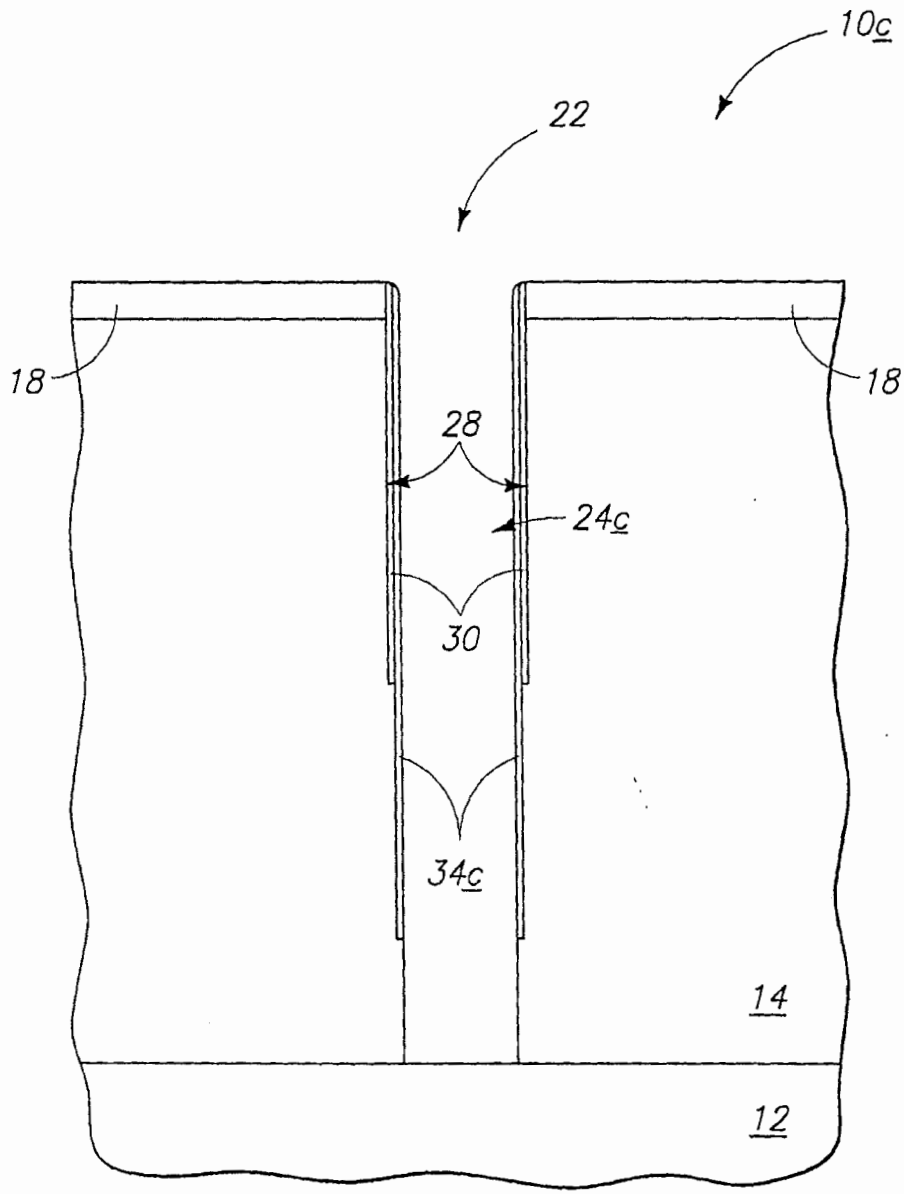


Fig 15

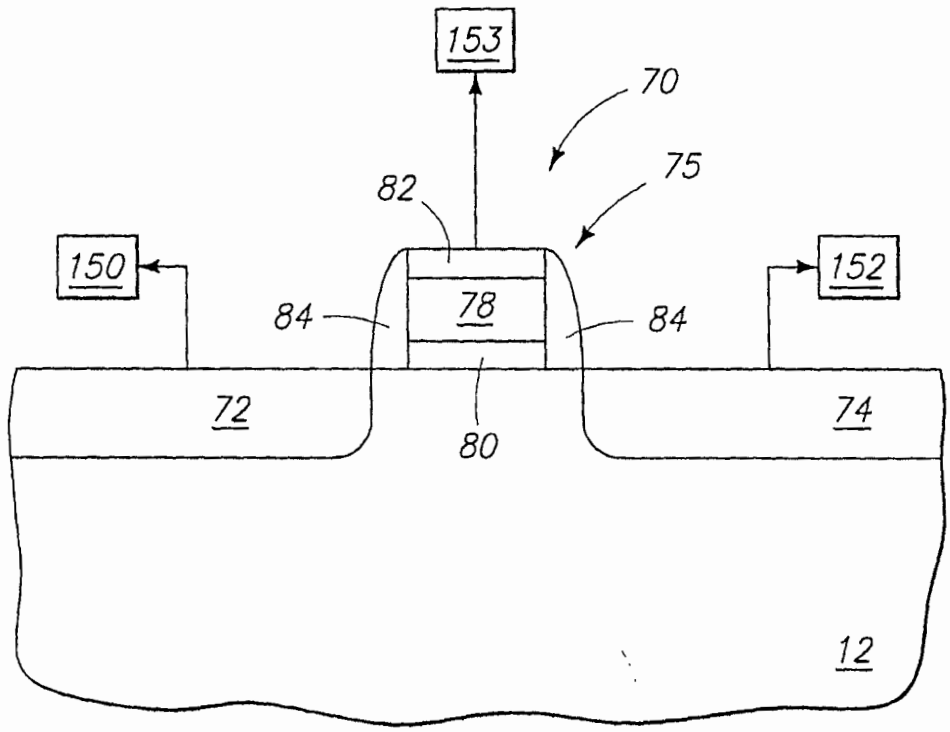


Fig 16

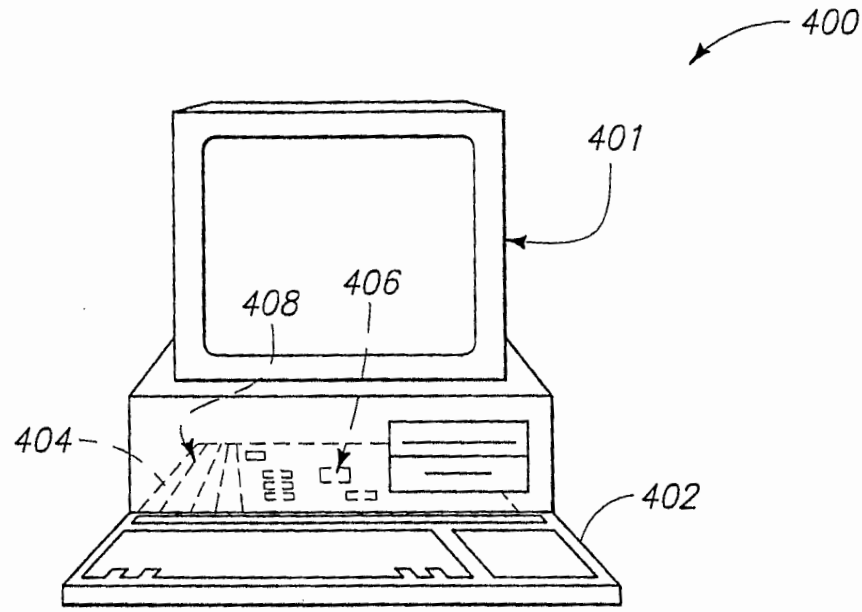


FIG. 11

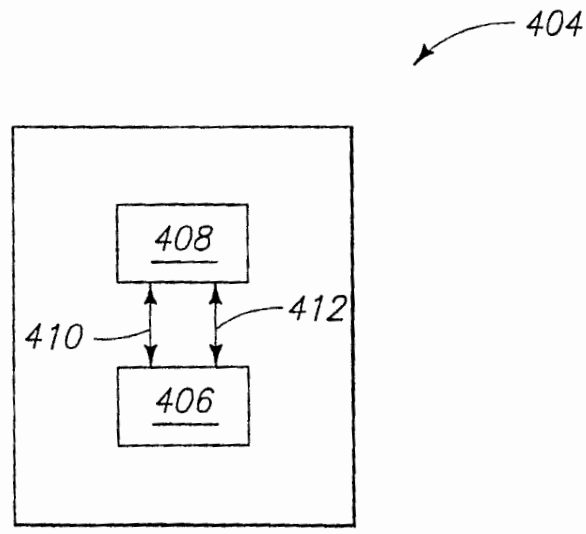


FIG. 11B

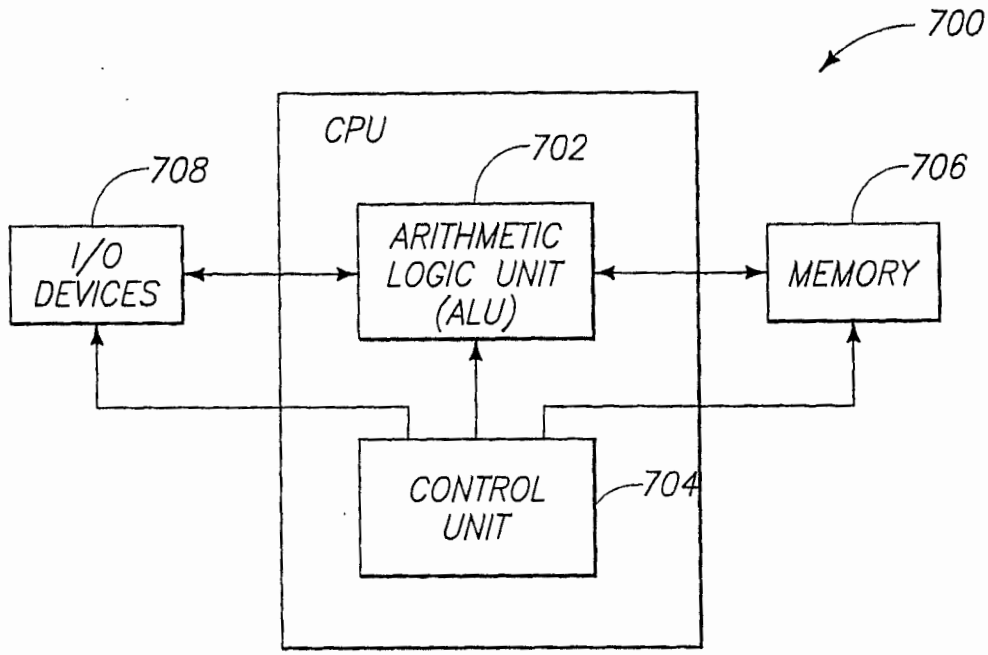

700
CPU
702
708
706
I/O DEVICES
ARITHMETIC LOGIC UNIT (ALU)
MEMORY
CONTROL UNIT
704

Fig 19

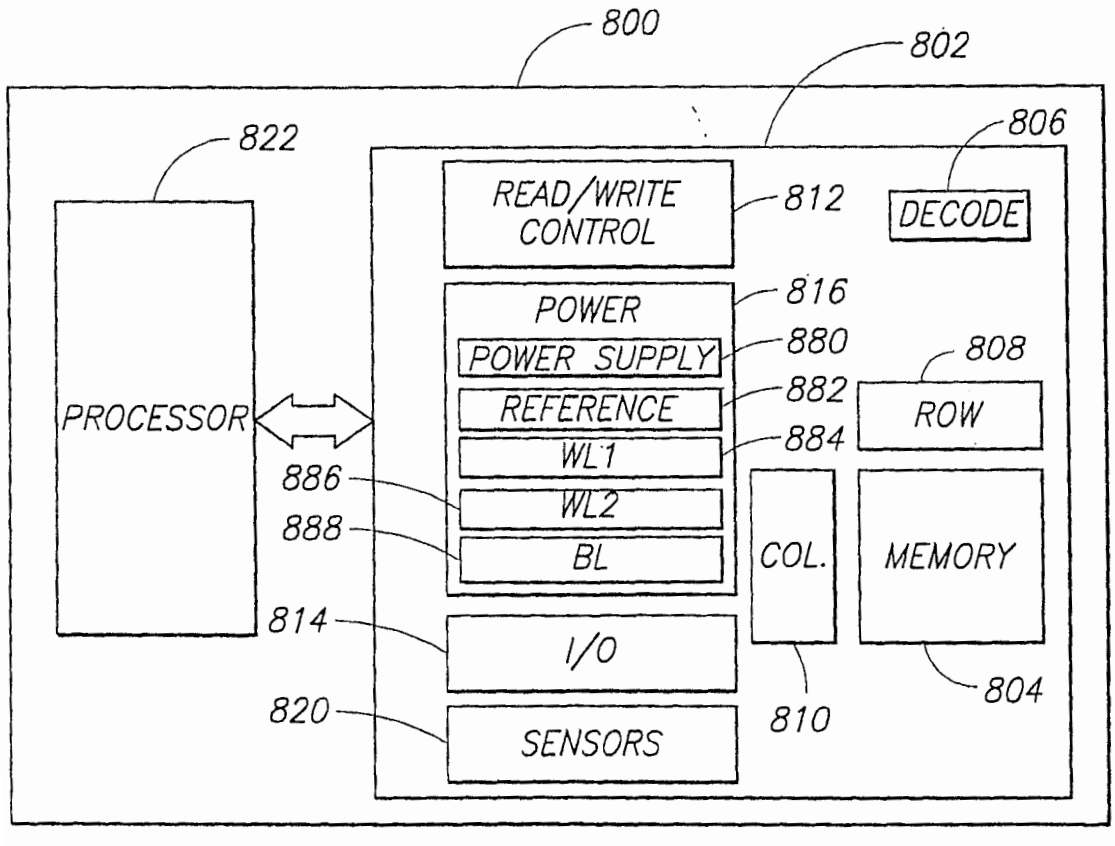

800
802
822
806
READ/WRITE CONTROL
812
DECODE
816
POWER
POWER SUPPLY
880
808
REFERENCE
882
ROW
PROCESSOR
884
WL1
886
WL2
888
BL
COL.
MEMORY
814
I/O
804
820
SENSORS
810

Fig 20