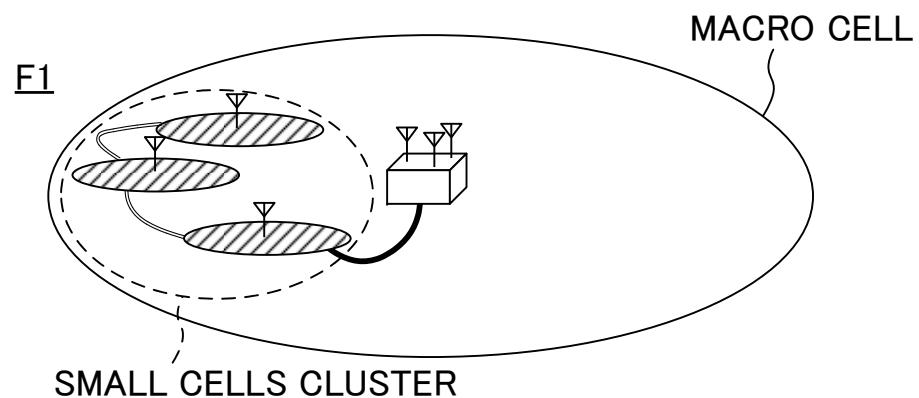
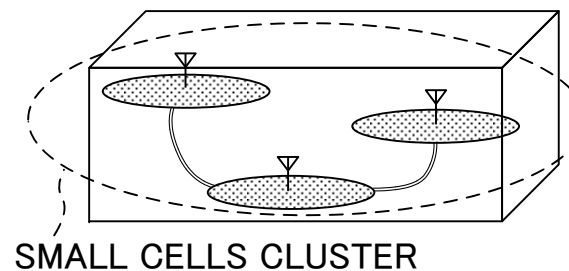


CO-CHANNEL DEPLOYMENT

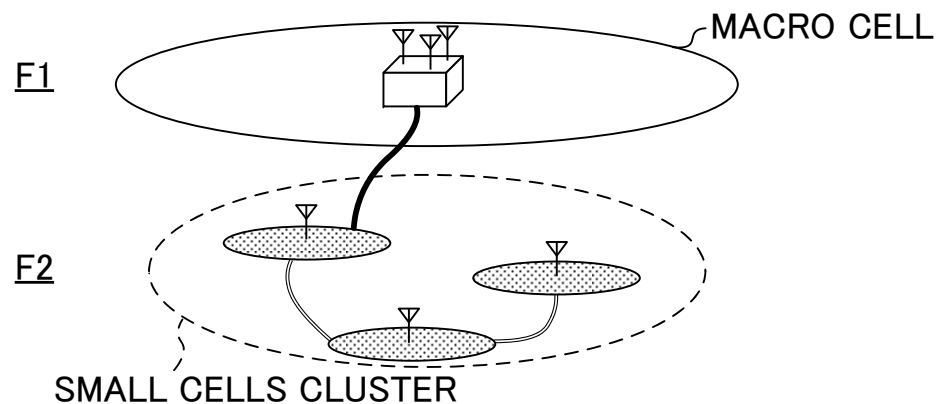


WITHOUT MACRO COVERAGE

F1 OR F2



NON-CO-CHANNEL DEPLOYMENT



NON-CO-CHANNEL DEPLOYMENT

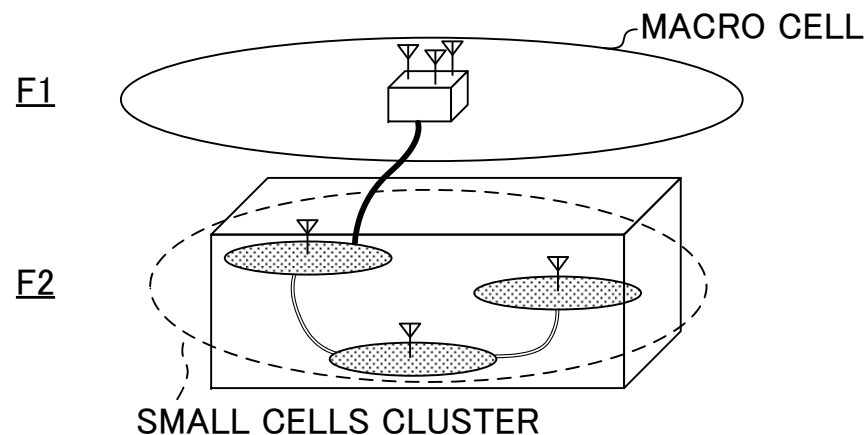


FIG.1

— BACKHAUL LINK WITHIN CLUSTER
 — BACKHAUL LINK BETWEEN SMALL CELLS AND MACRO CELL

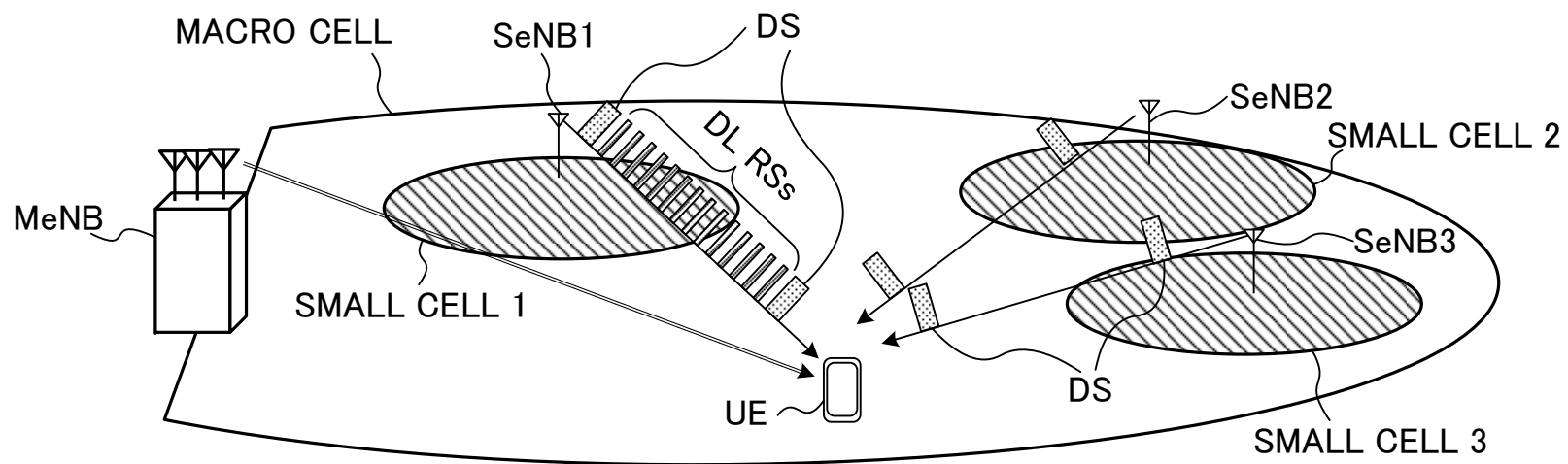


FIG.2A

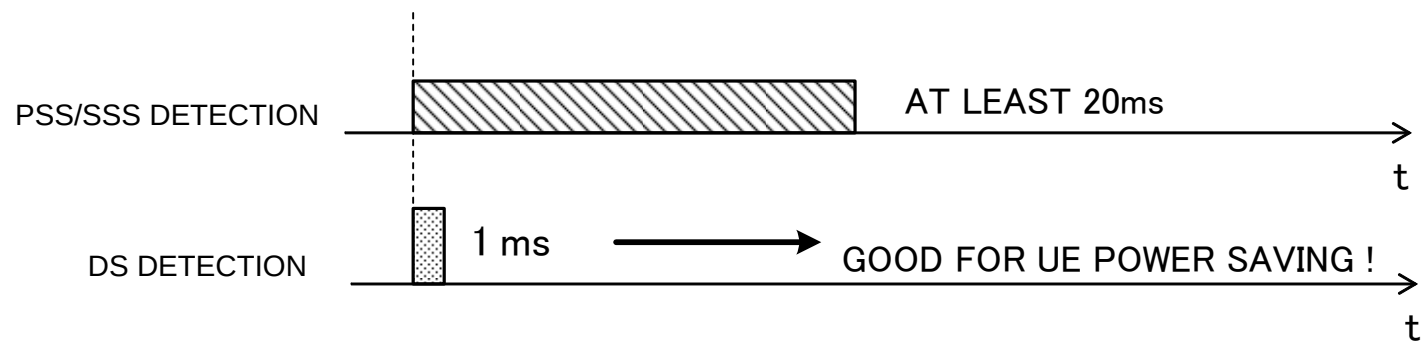


FIG.2B

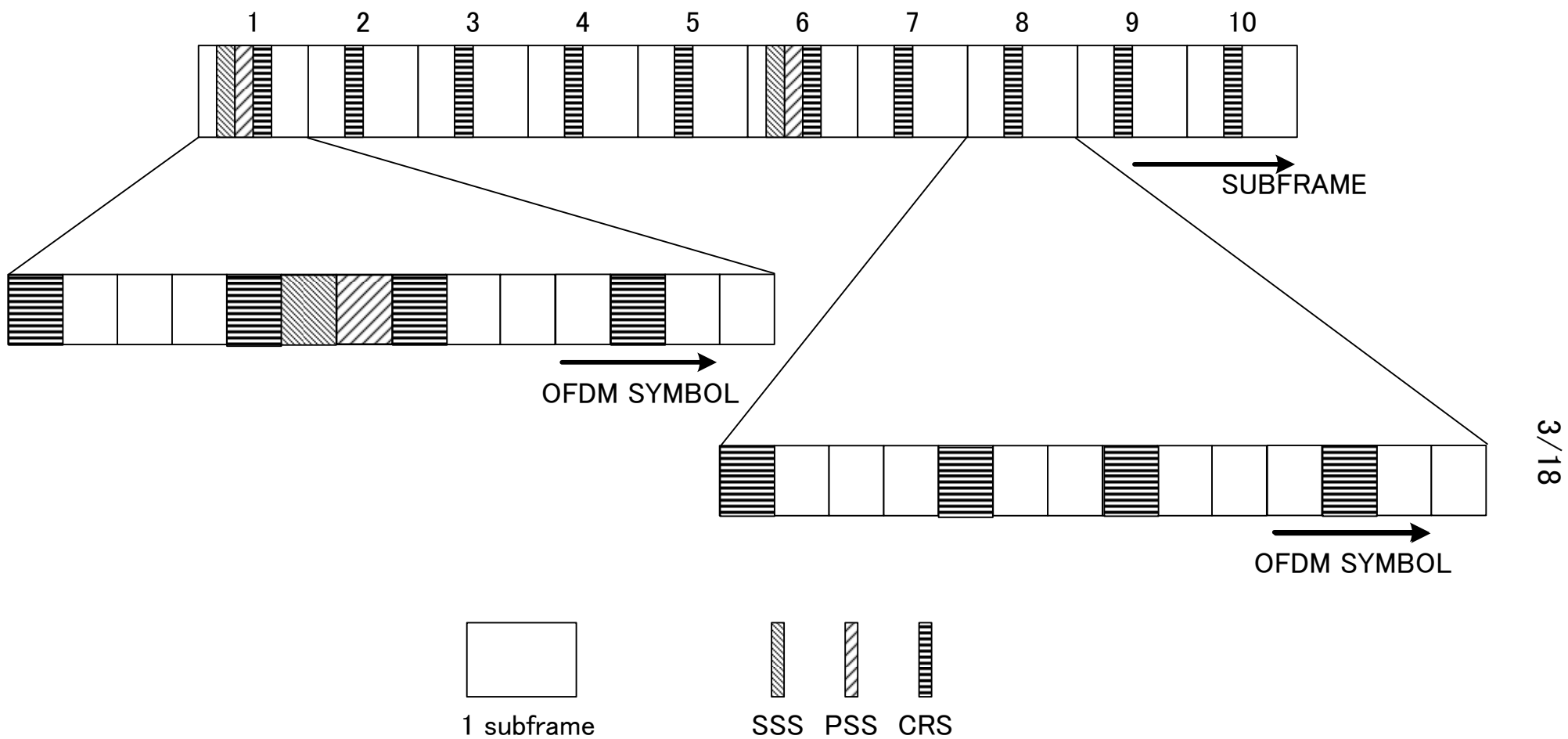


FIG.3

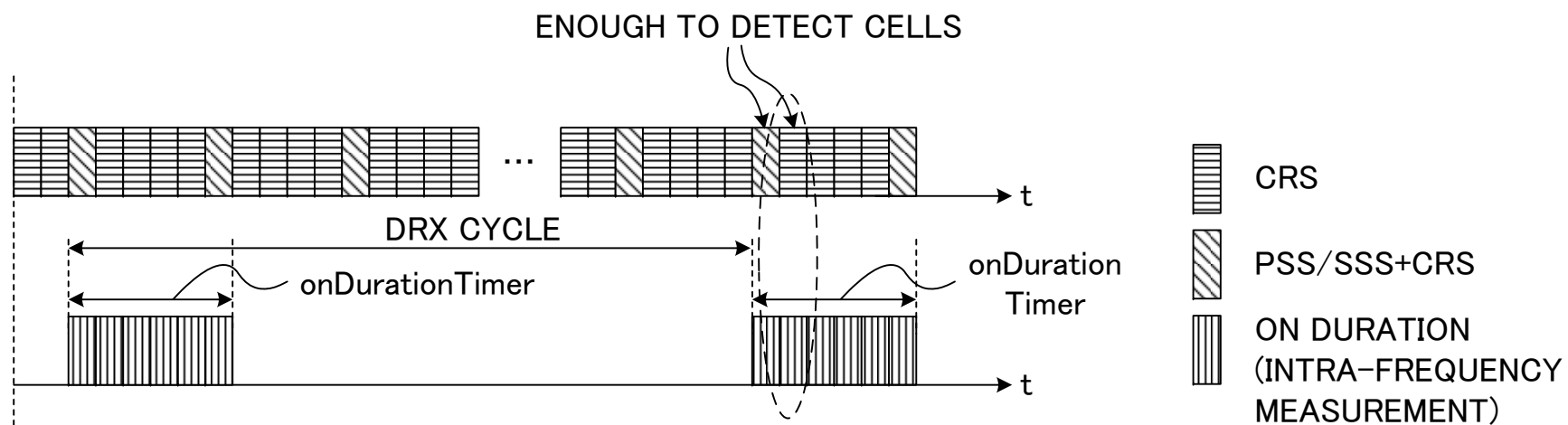


FIG.4

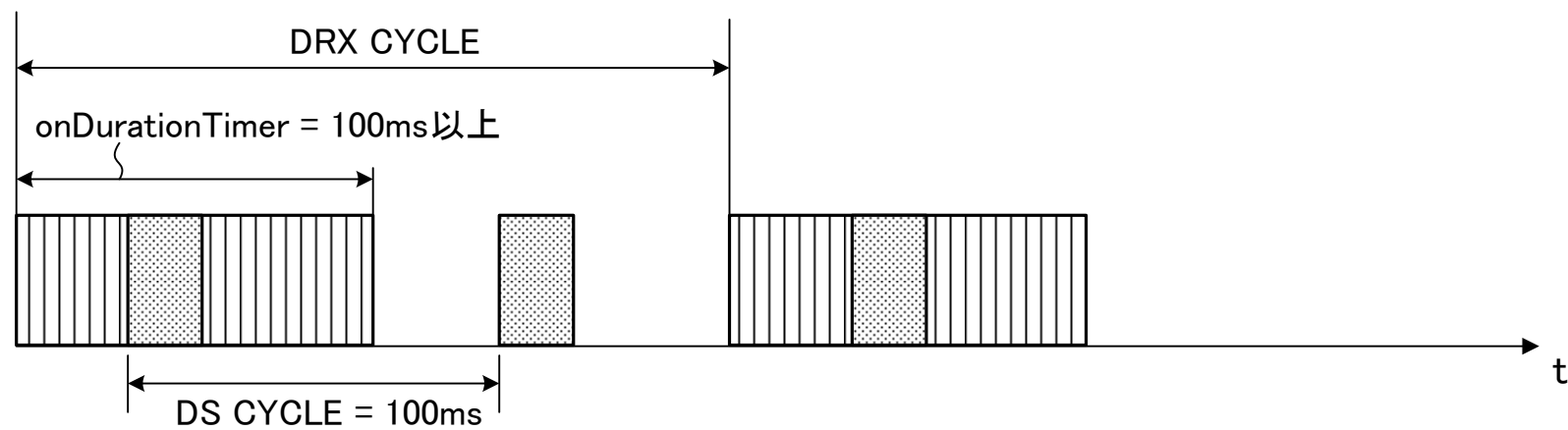
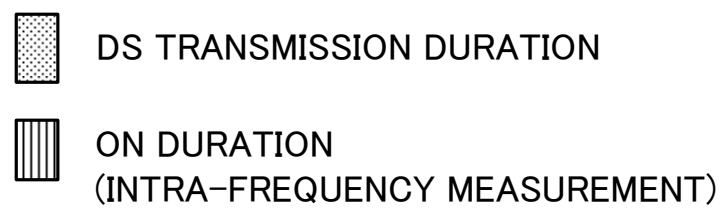
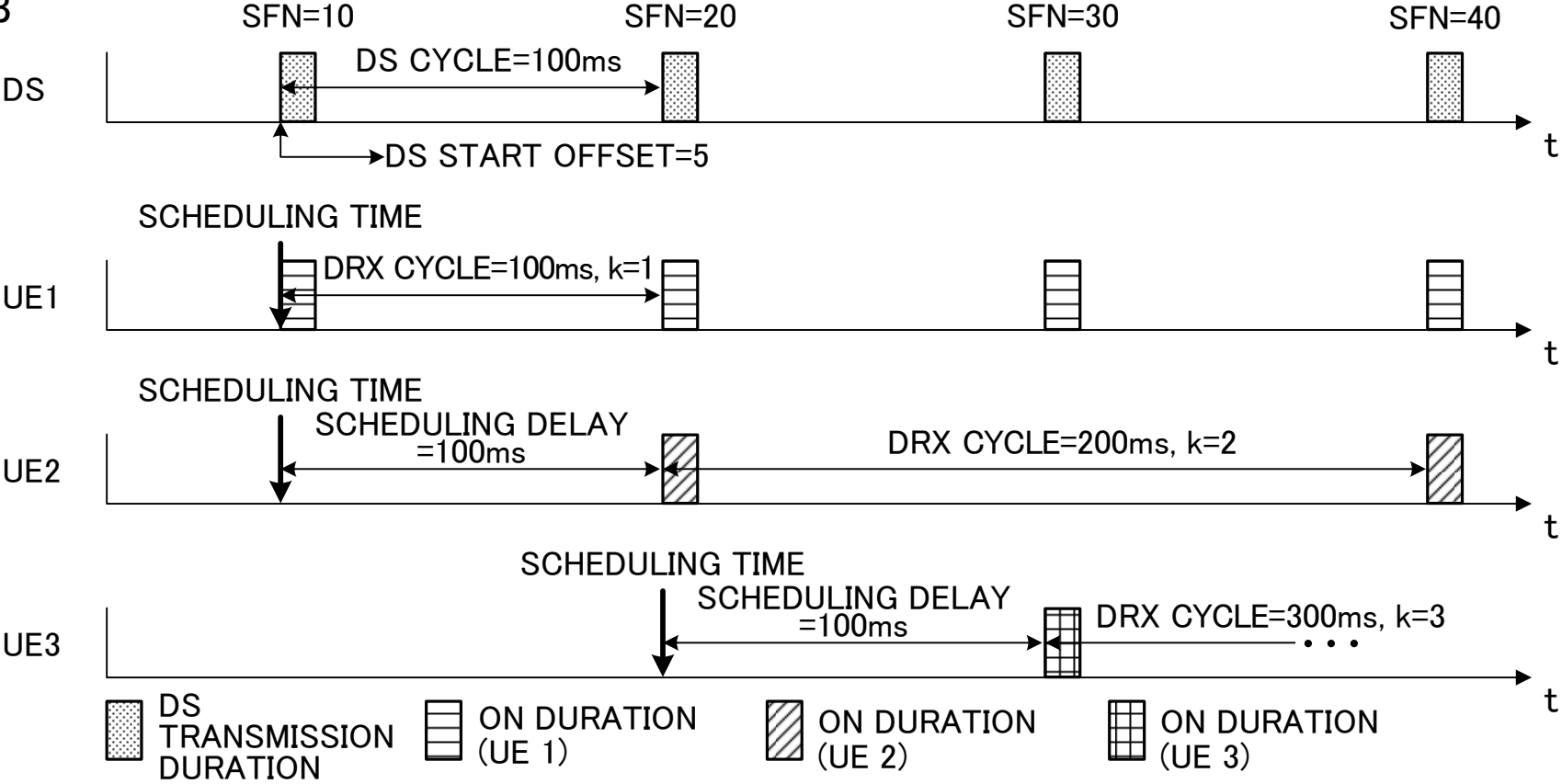


FIG.6

FIG.7A

	DS CYCLE	DS START OFFSET (SUBFRAME NUMBER)	k	DRX CYCLE	WHEN TO START onDuration Timer(SFN, SUBFRAME NUMBER)
UE1	100ms	5	1	100ms	(10, 5); (20, 5); (30, 5); (40, 5)
UE2	100ms	5	2	200ms	(20, 5); (40, 5)
UE3	100ms	5	3	300ms	(30, 5)

FIG.7B



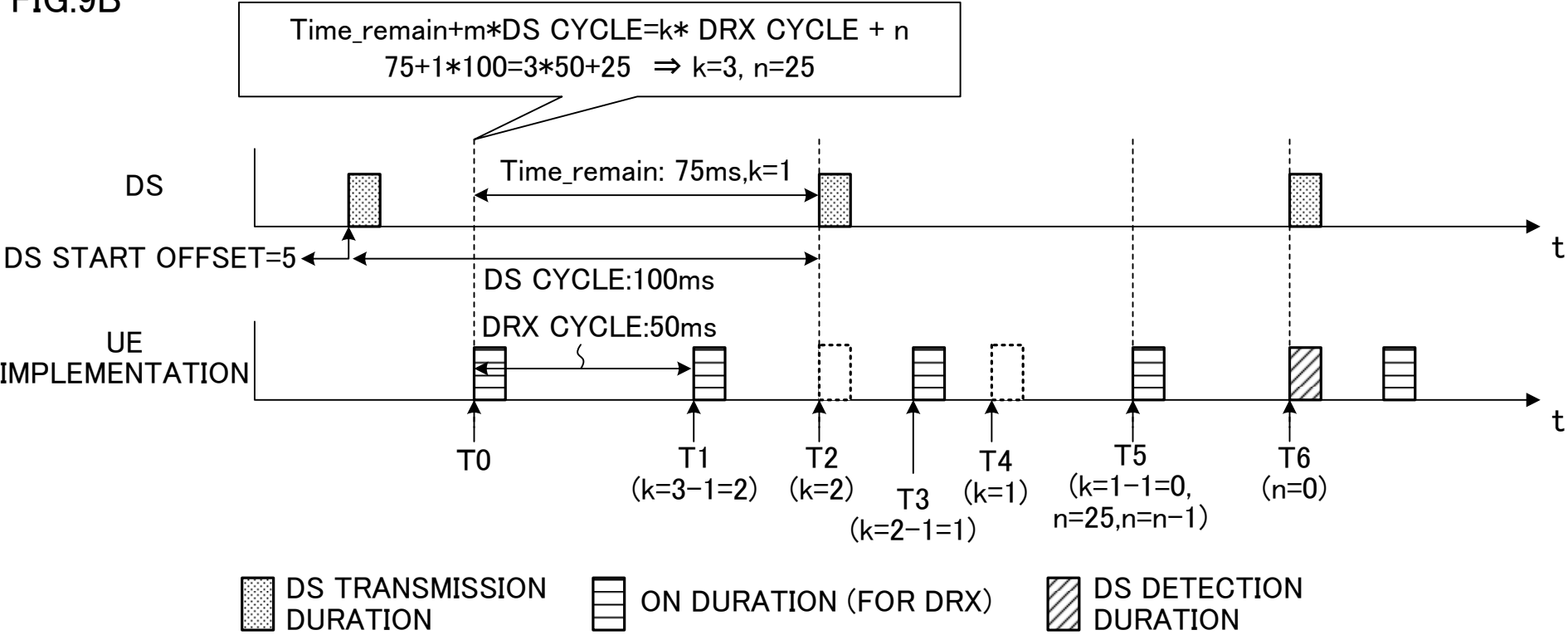
80%≤P_remain≤100%	50%≤P_remain<80%	20%≤P_remain<50%	P_remain<20%
m=0	m=1	m=2	m=6

FIG.8

FIG.9A

DS CYCLE	DS START OFFSET	$50\% \leq P_{\text{remain}} < 80\%$	DRX CYCLE
100ms	5	$m=1$	50ms

FIG.9B



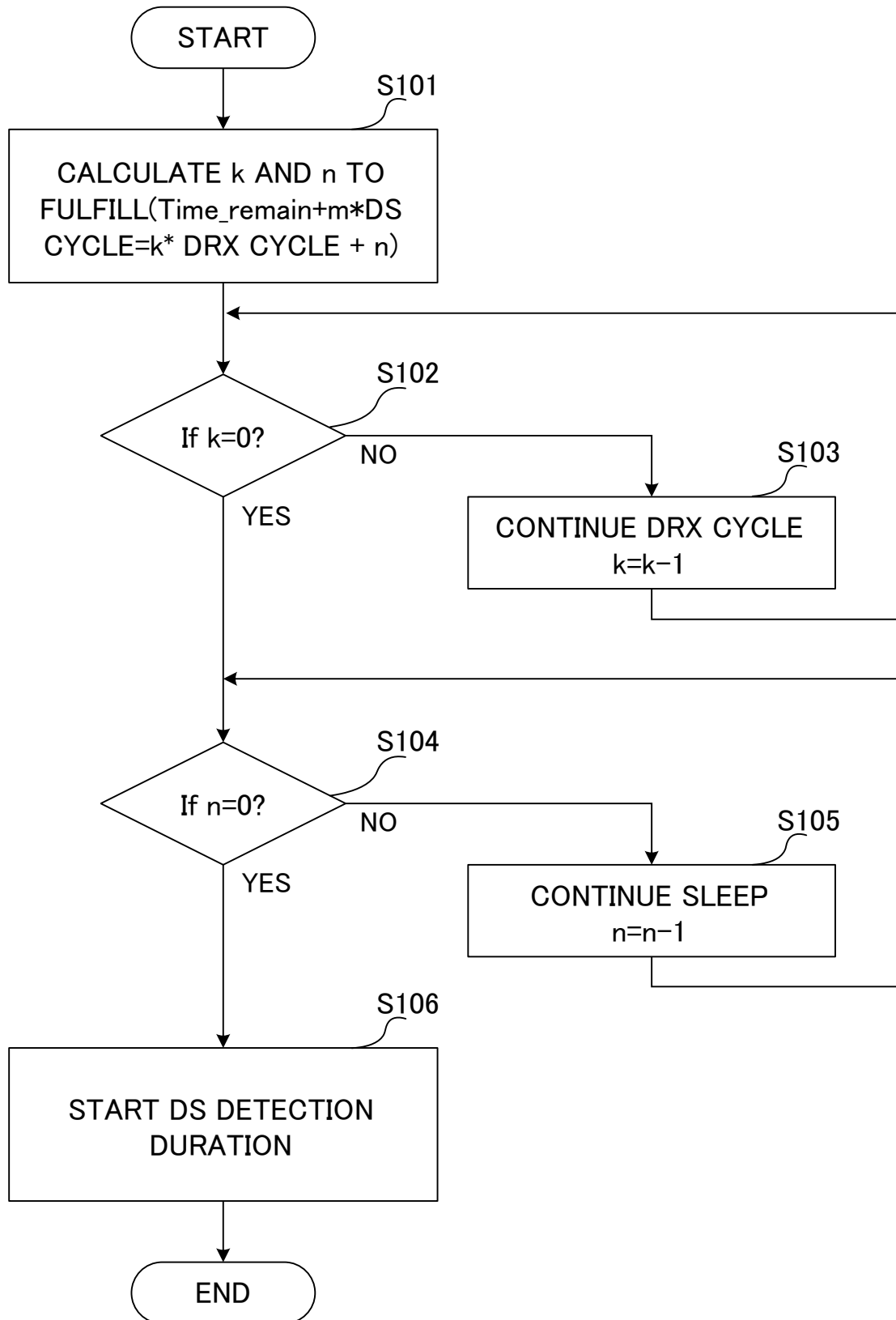
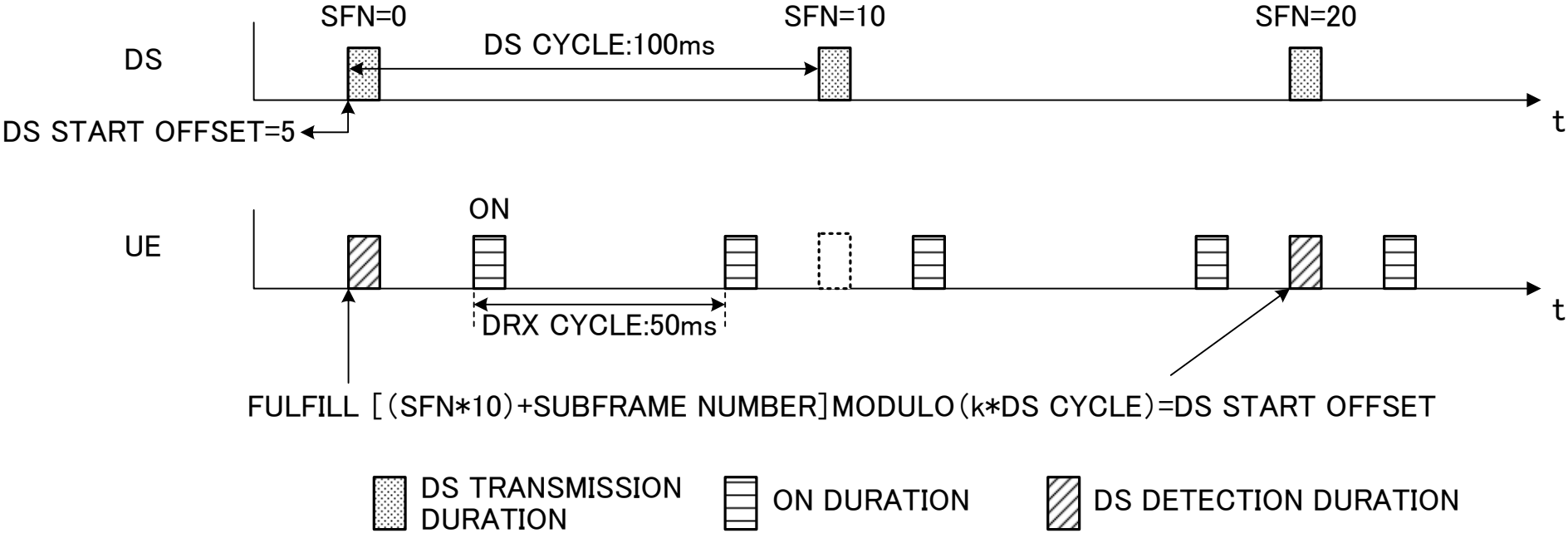


FIG.10

FIG.11A

DS CYCLE	DS START OFFSET	k	DRX CYCLE
100ms	5	2	50ms

FIG.11B



OPTION	BASELINE	1	2 AND 3
DRX CYCLE	200ms	200ms	200ms
onDurationTimer	100ms	50ms	50ms
OPTION 2, m=1. OPTION 3, k=2 .			

FIG.12A

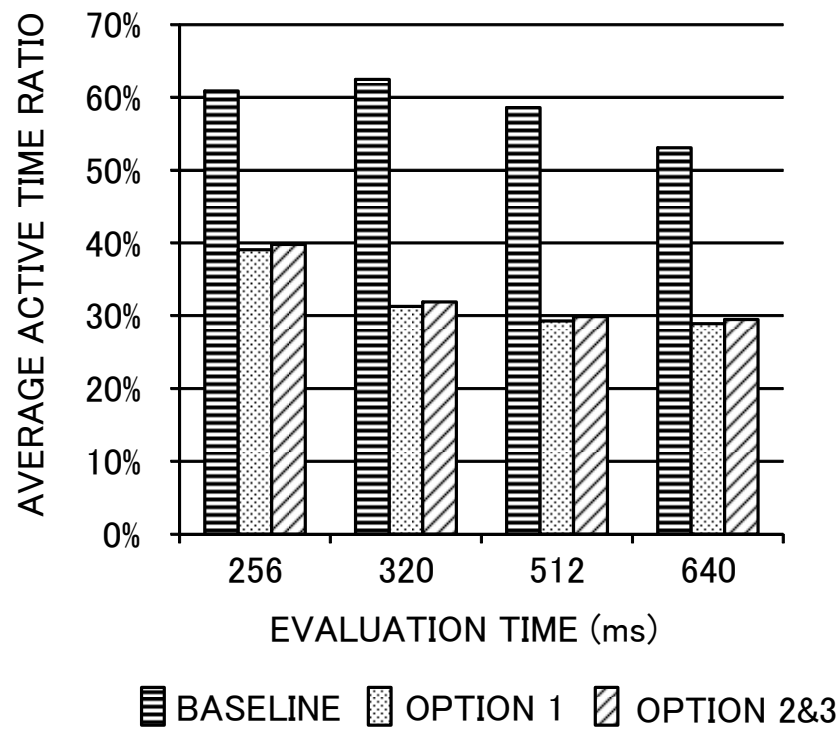


FIG.12B

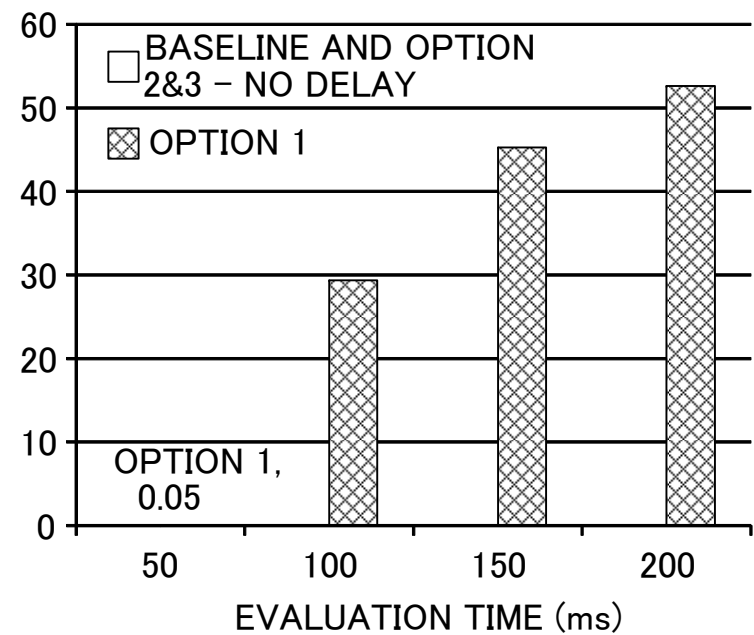


FIG.12C

	UE POWER SAVING	CHANCE TO DETECT DS RATE	UE BEHAVIOR CONTROL	SCHEDULING DELAY
BASELINE	BAD, EXCEPT FOR ACTUAL onDuration Timer NEEDED IS 100ms	UNCERTAINTY, <100% (NOTE 2)	NETWORK	NO DELAY
OPTION 1	BETTER COMPARED TO OPTION 2&3	UNCERTAINTY, <100%	NETWORK	LARGE DELAY
OPTION 2	GOOD (NOTE 1)	CERTAINTY, <100%	UE-SELF	NO DELAY
OPTION 3			NETWORK	NO DELAY

NOTE 1: UE POWER SAVING DEPENDS ON ACTIVE TIME OF DRX

NOTE 2: THE CHANCE TO DETECT DS DEPENDS ON DRX CONFIGURATION

FIG.13

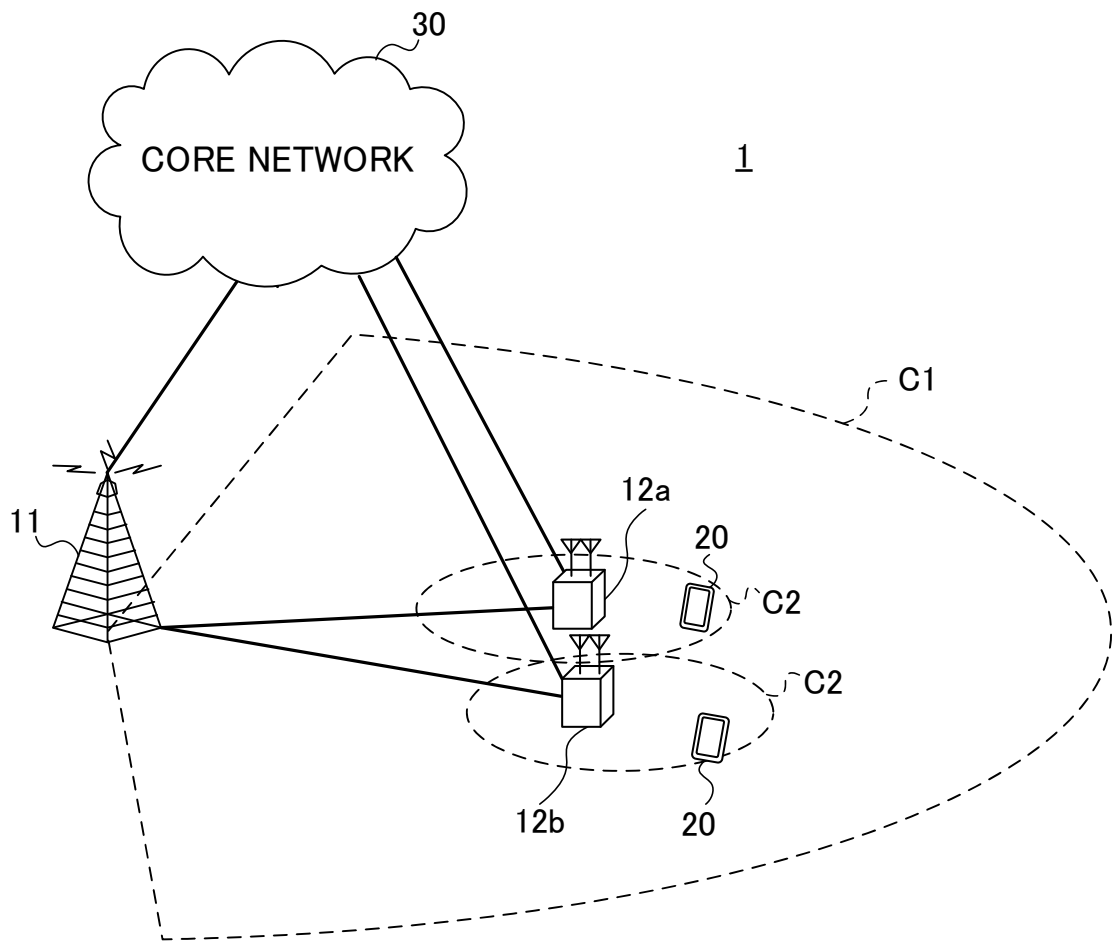


FIG.14

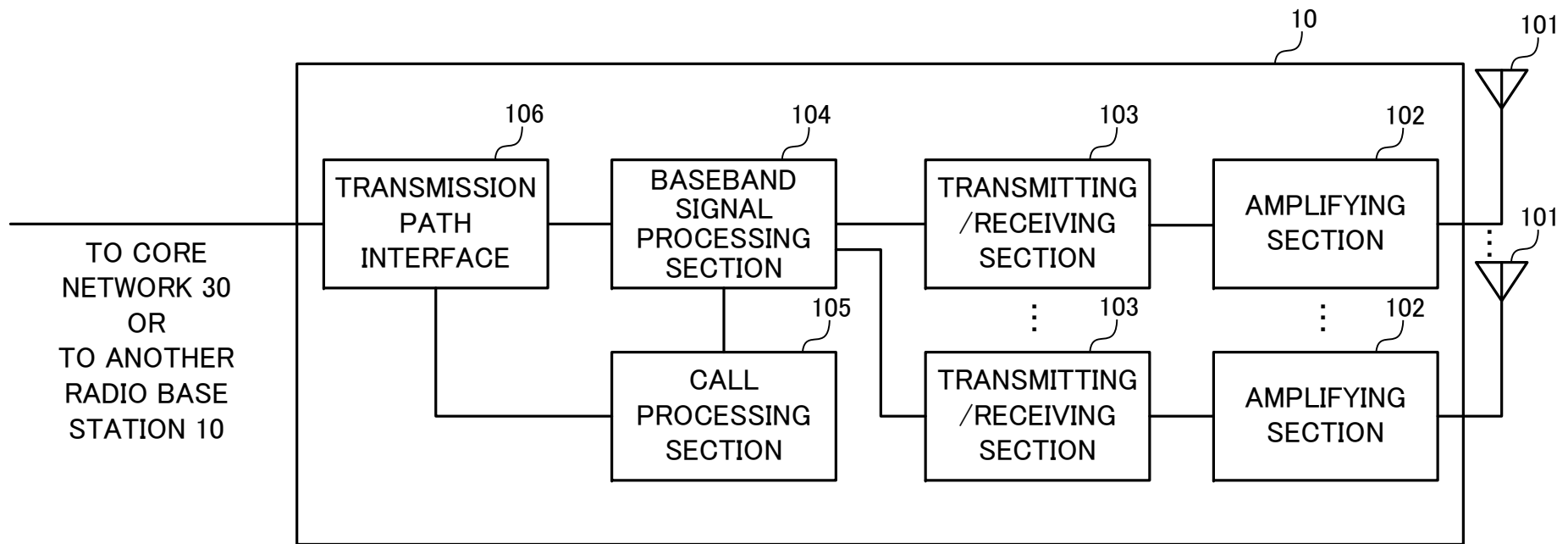


FIG.15

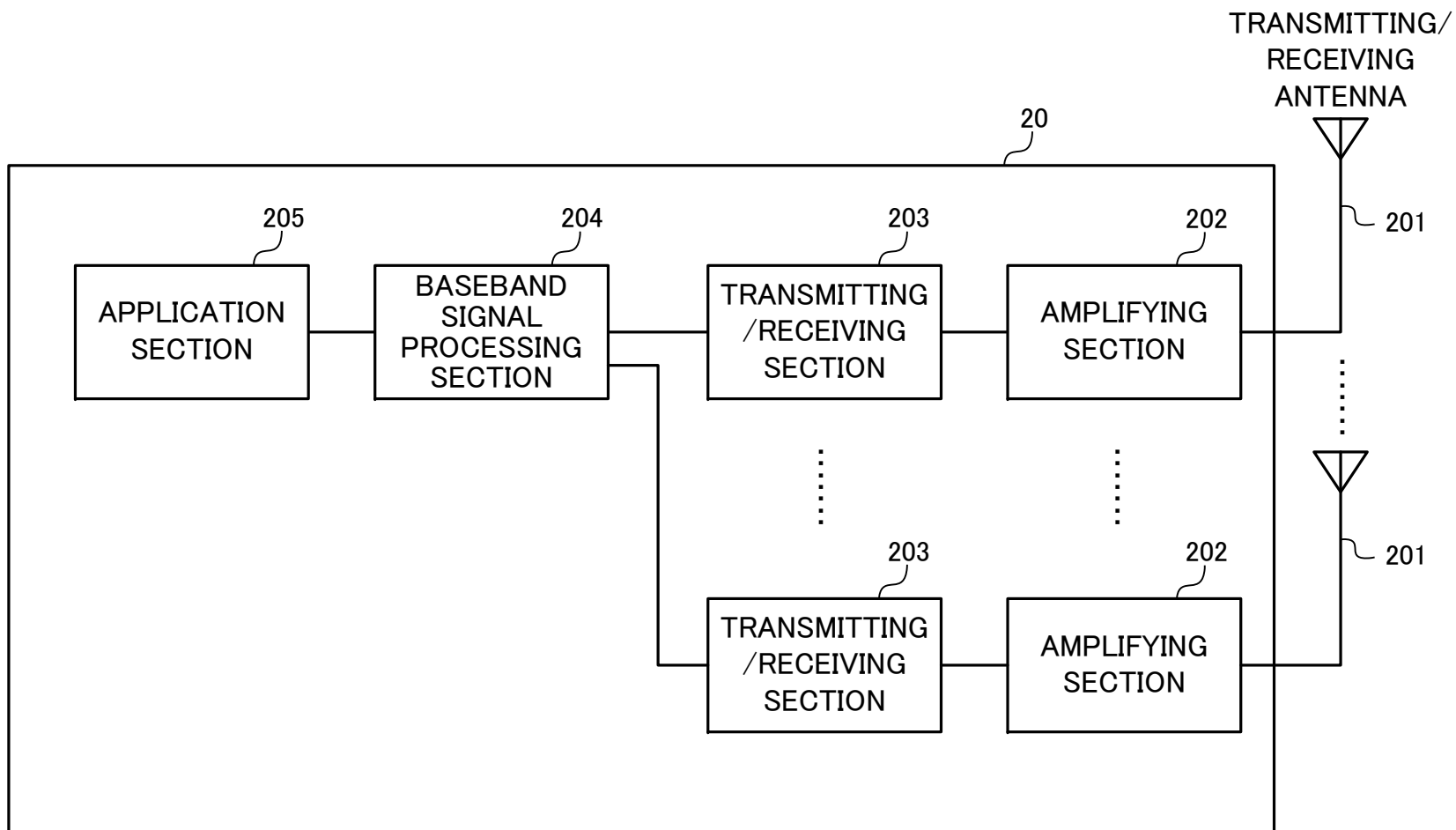


FIG.16

MACRO BASE STATION 11

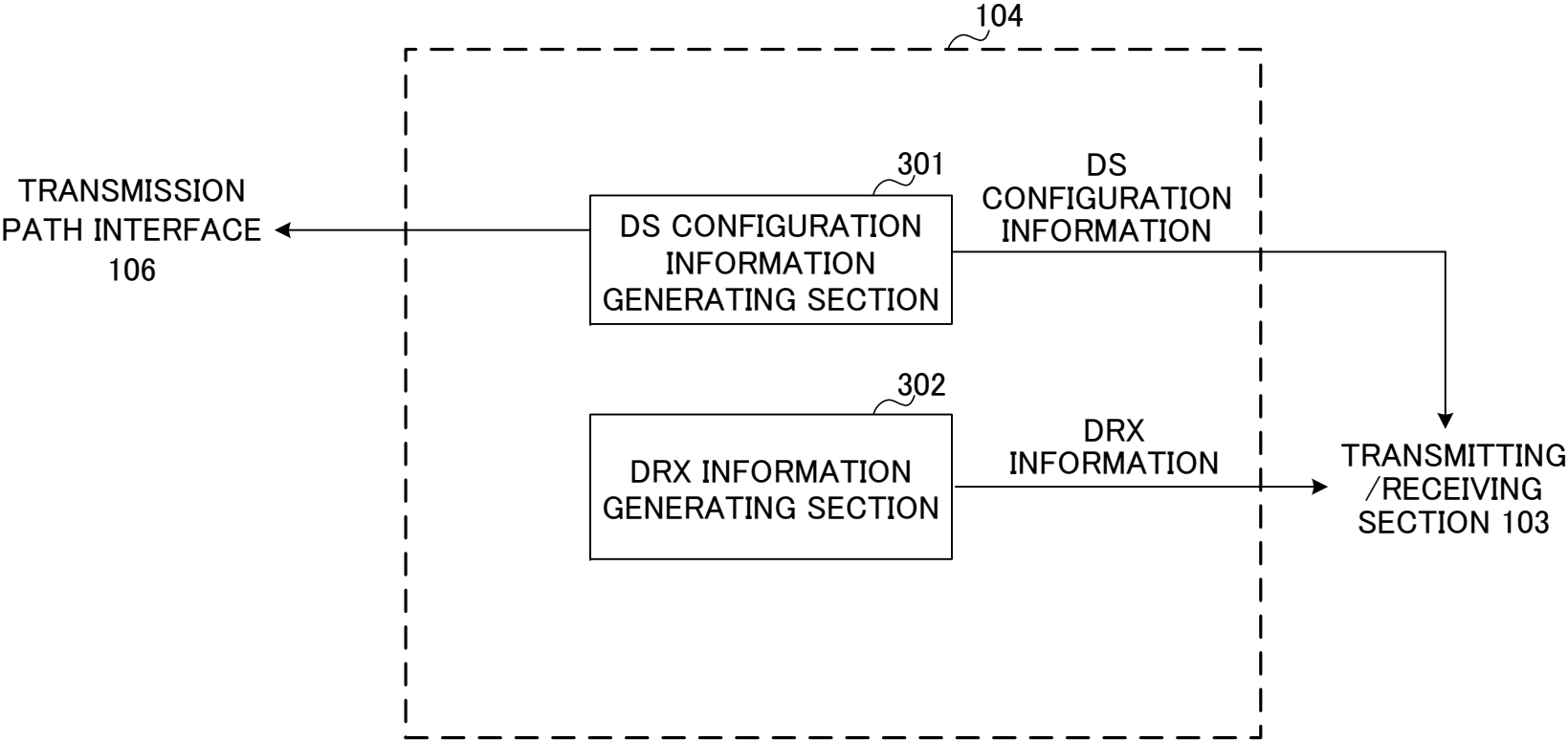


FIG.17

USER TERMINAL 20

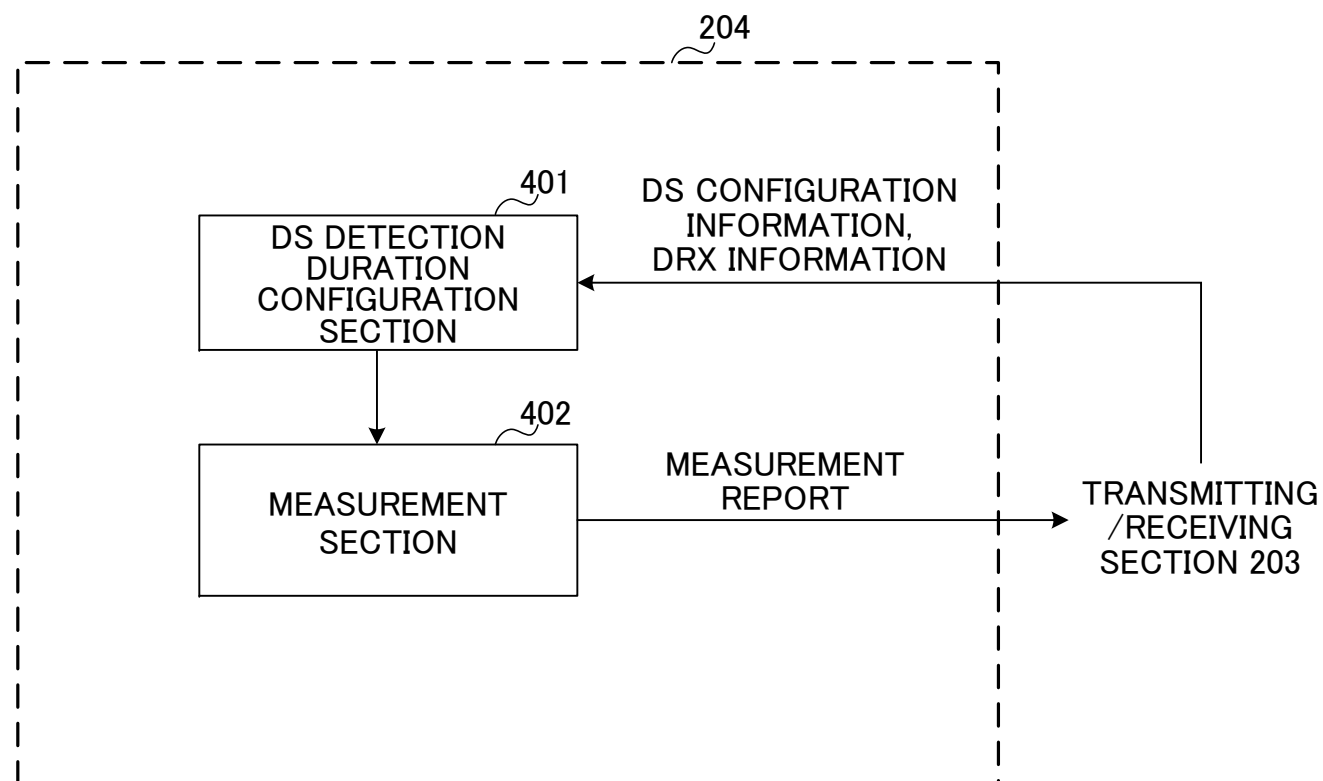


FIG.18