

1/5

100

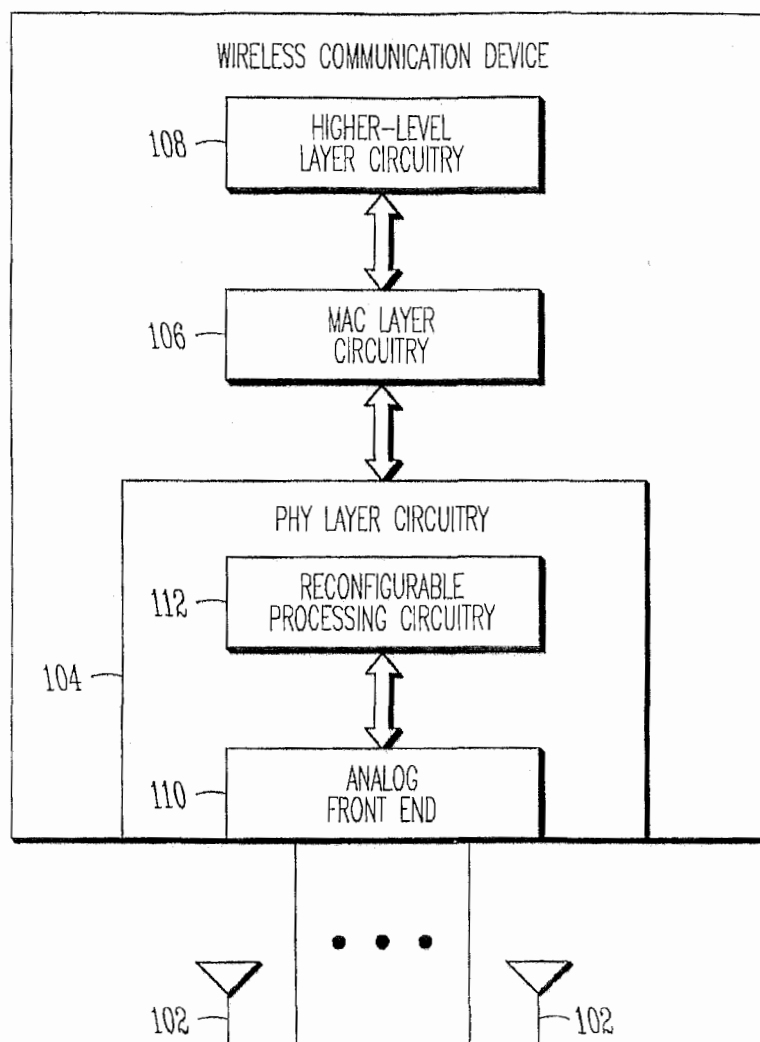
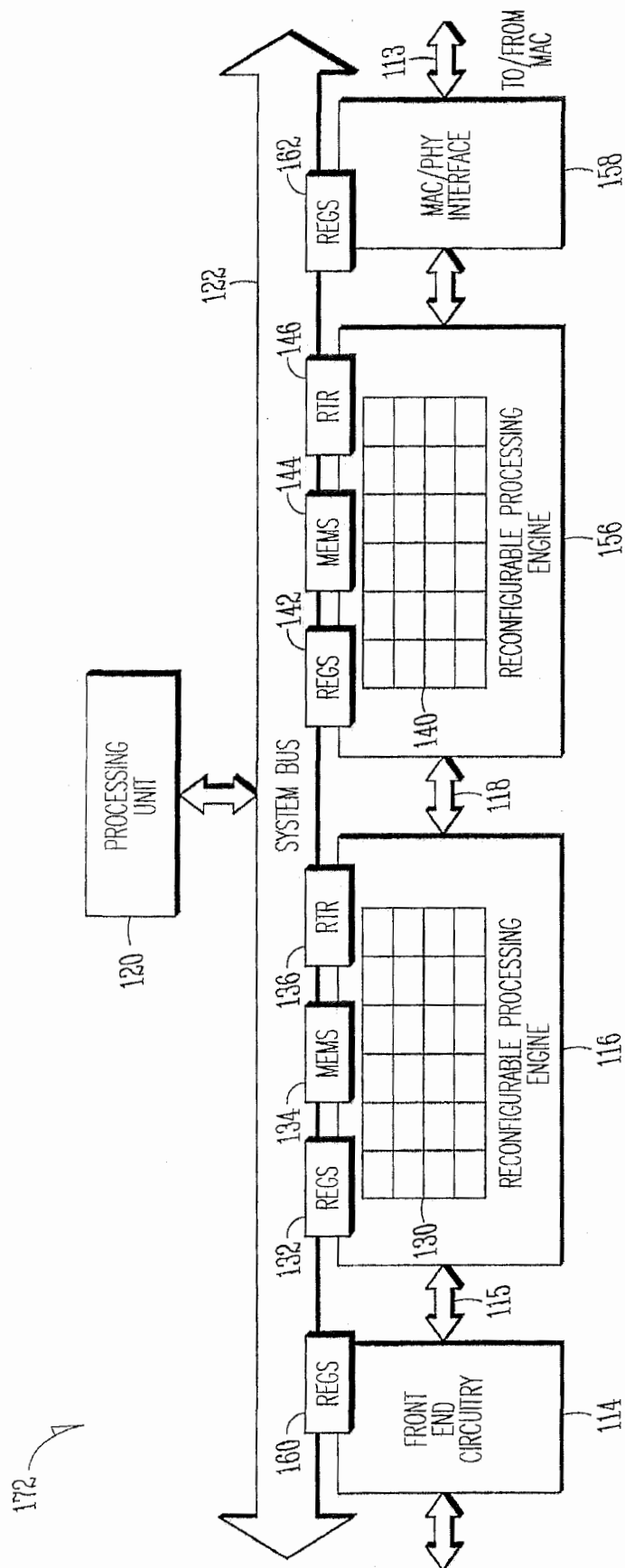


FIG. 1A

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RECONFIGURABLE PROCESSING CIRCUITRY

FIG. 1B

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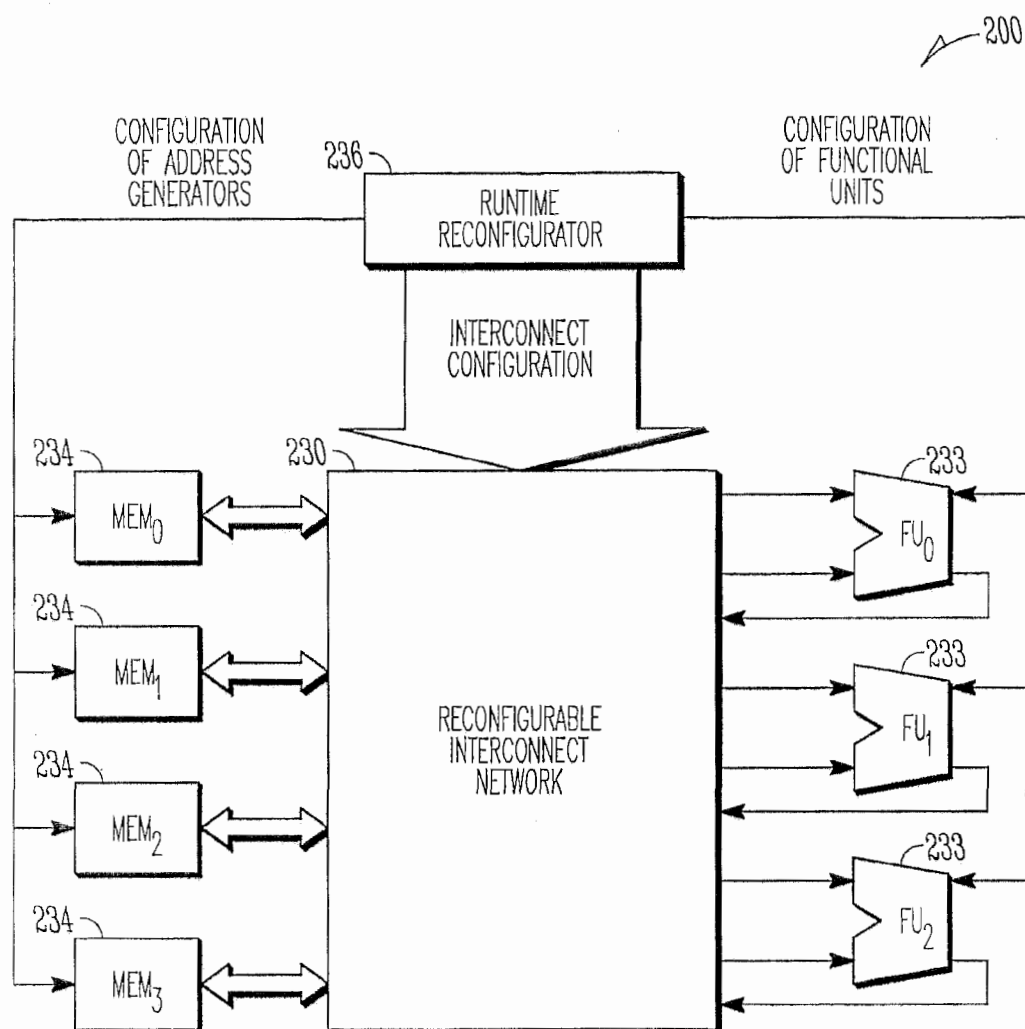


FIG. 2

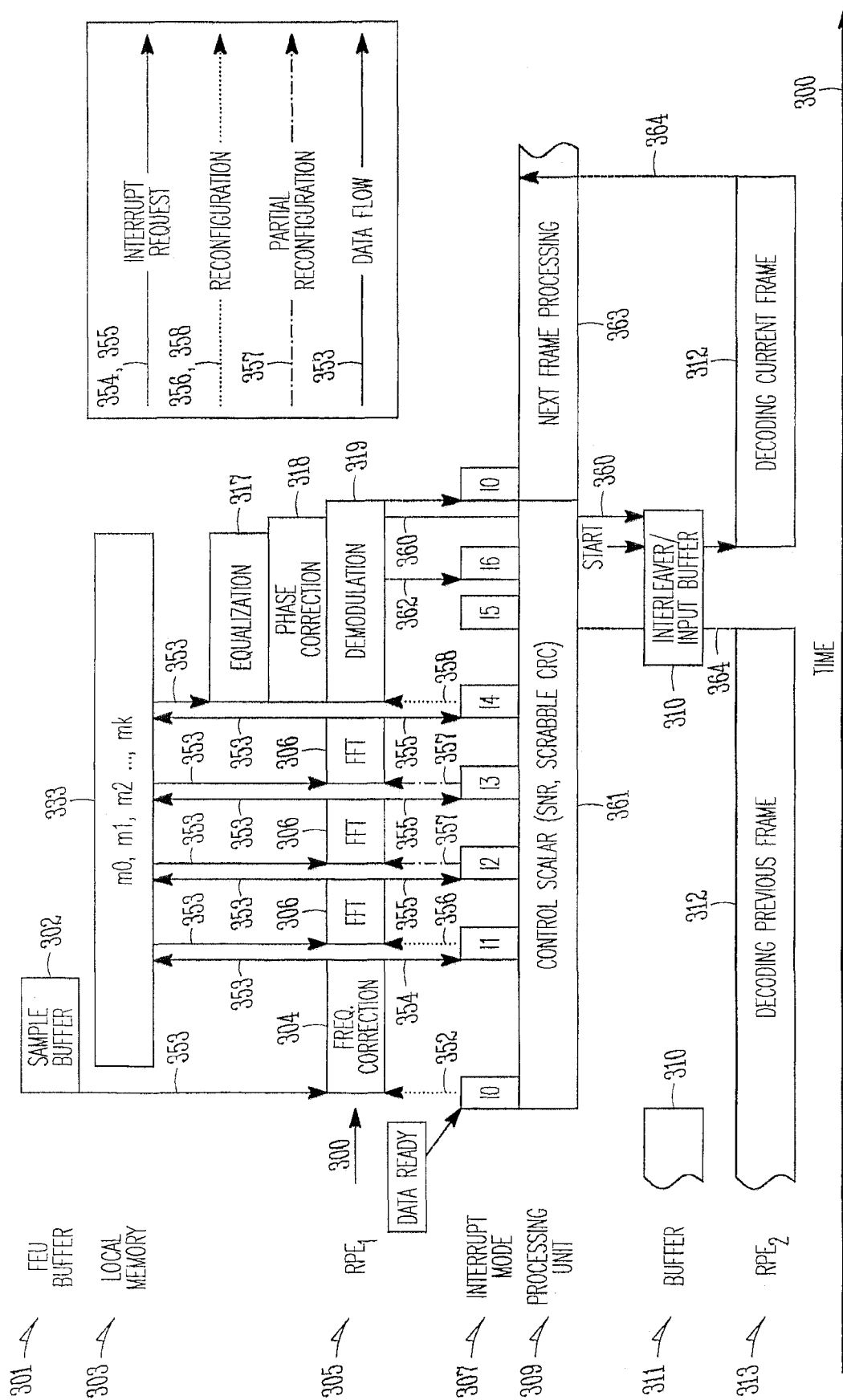


FIG. 3

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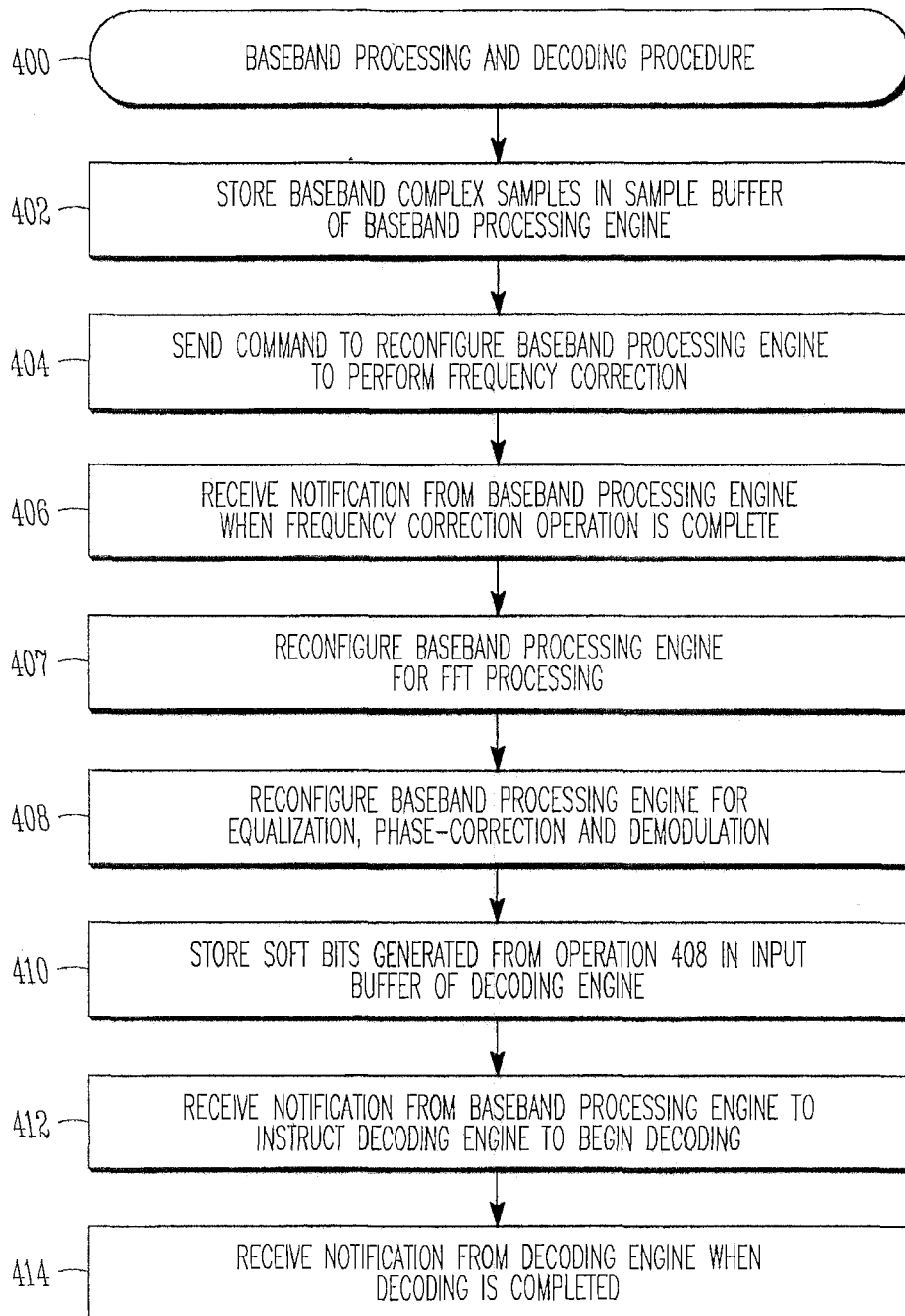


FIG. 4