

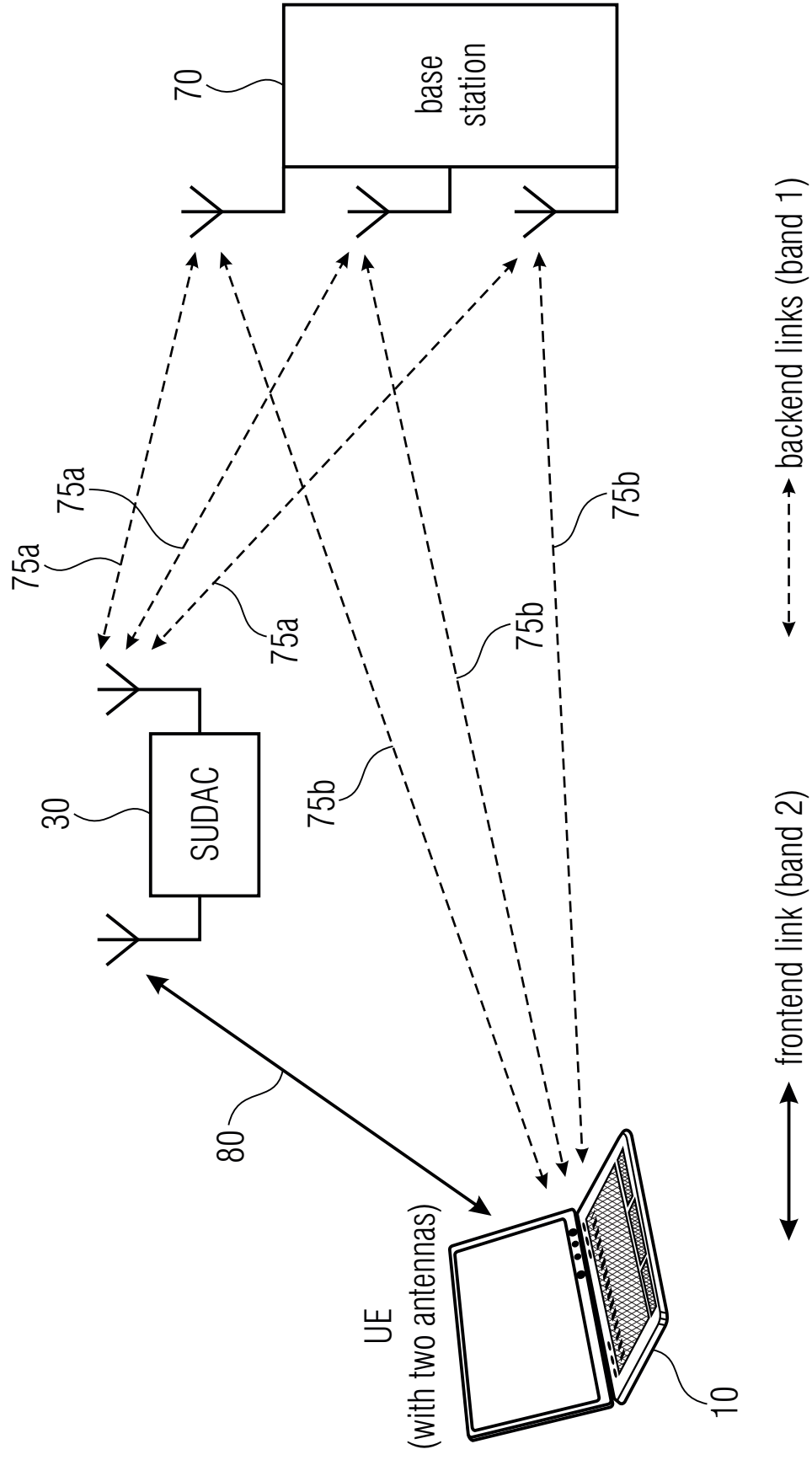


FIG 1

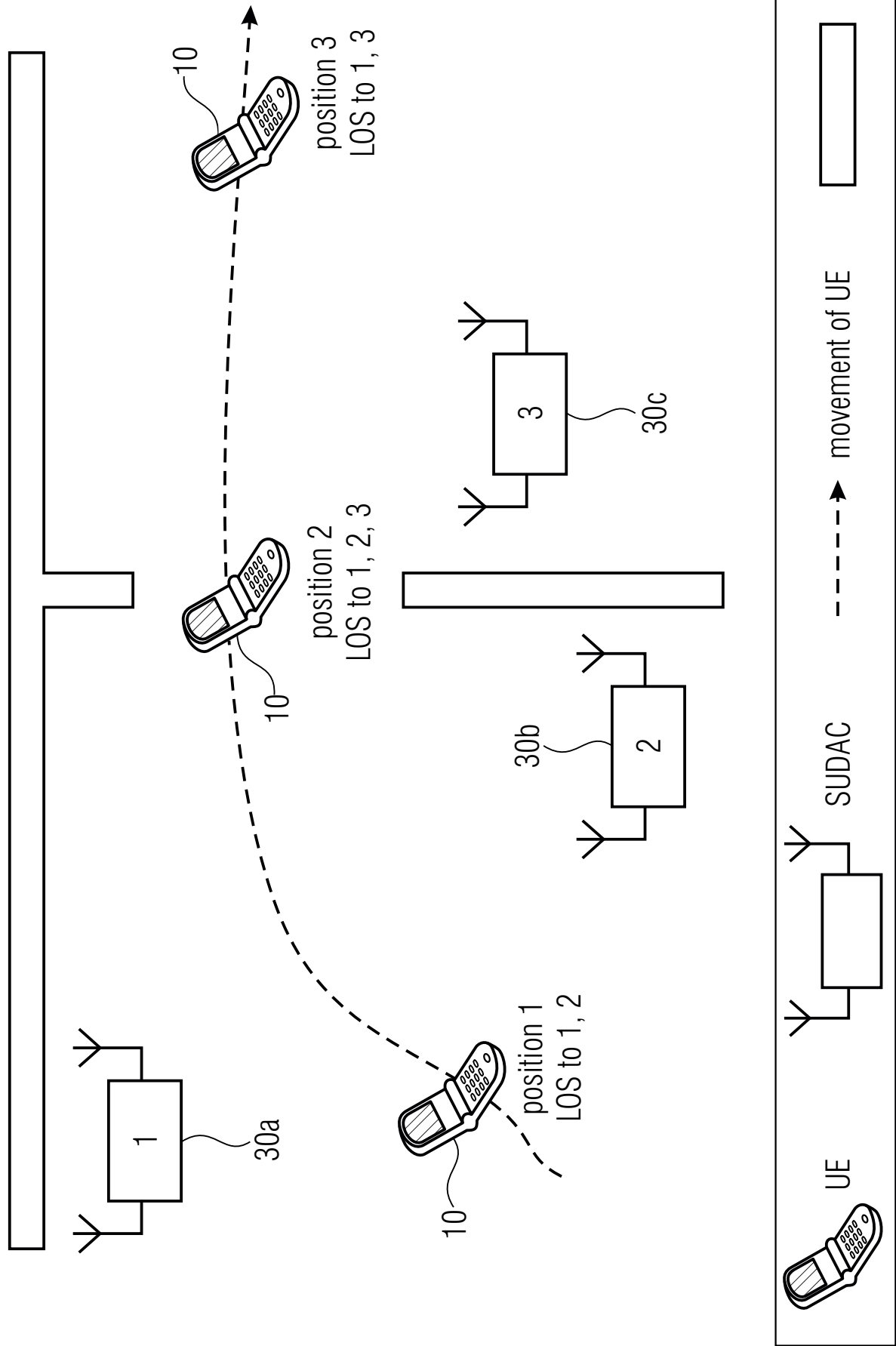


FIG 2

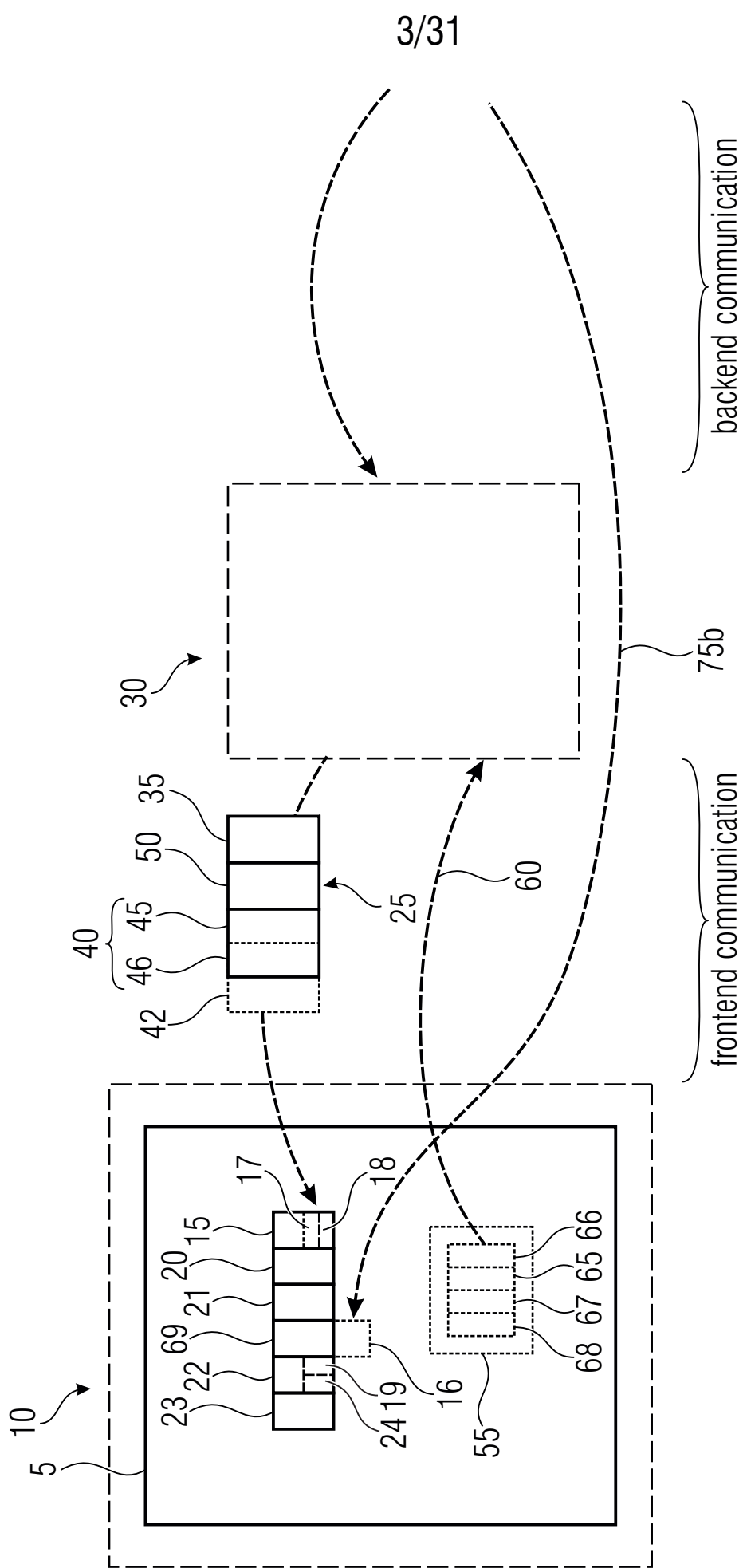


FIG 3

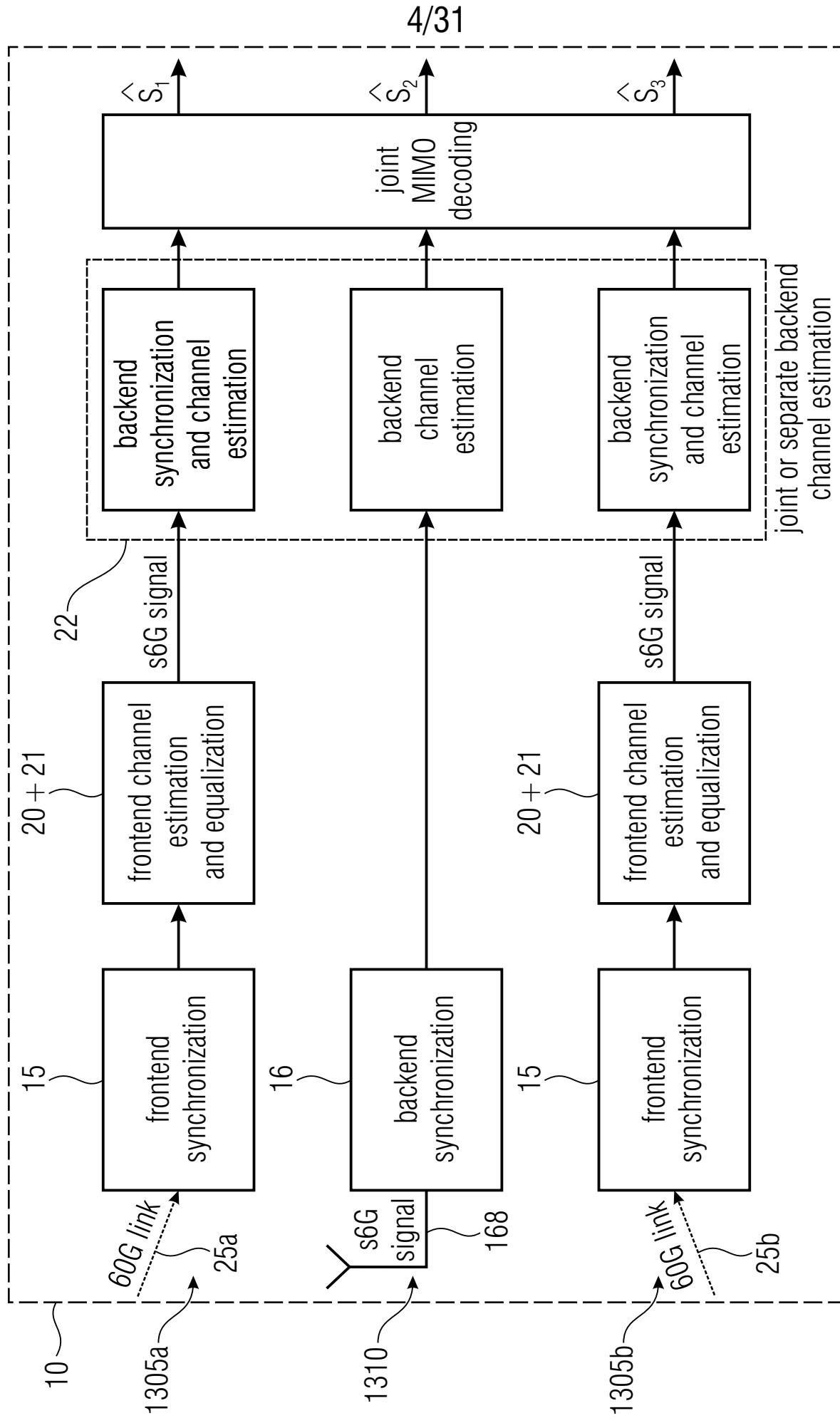


FIG 4

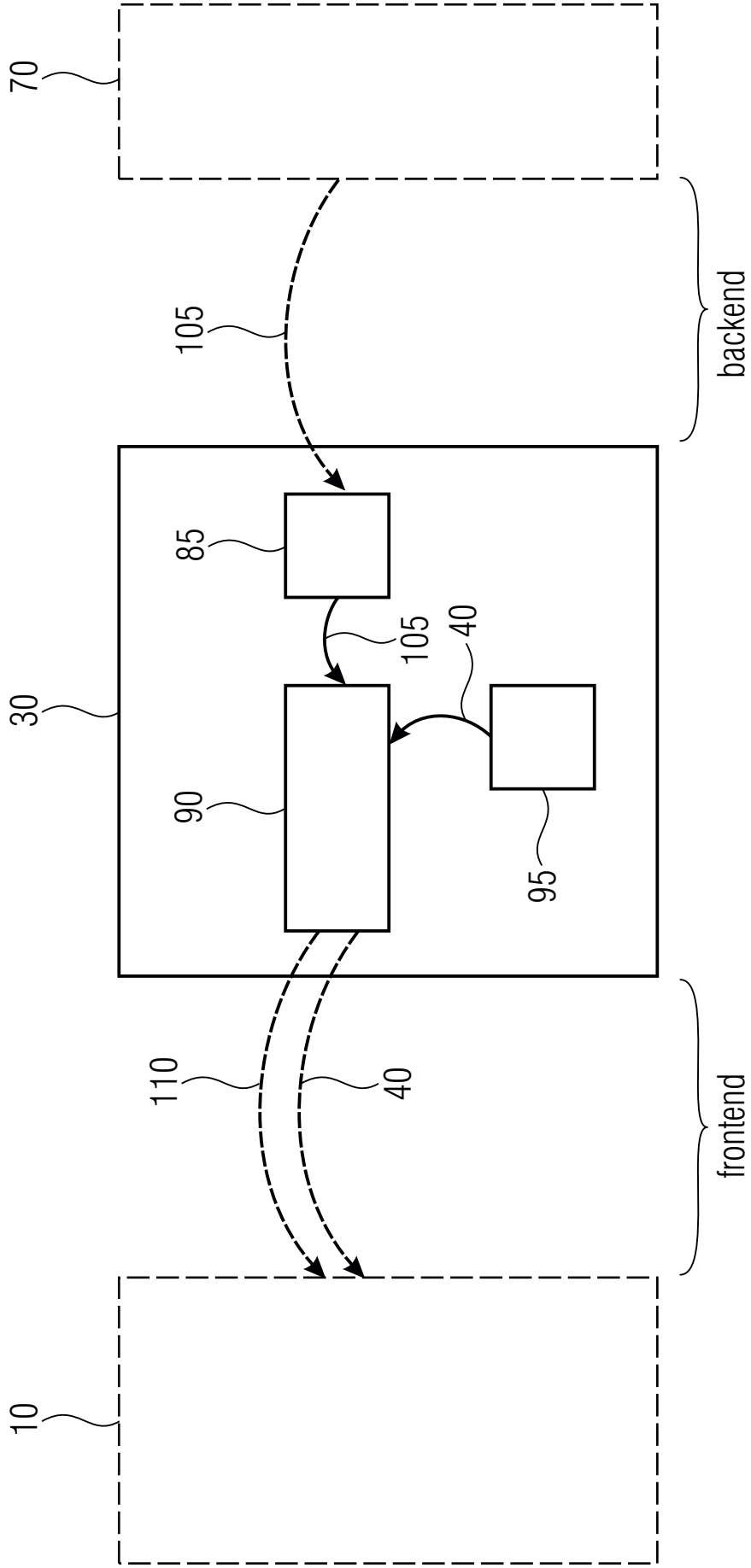


FIG 5

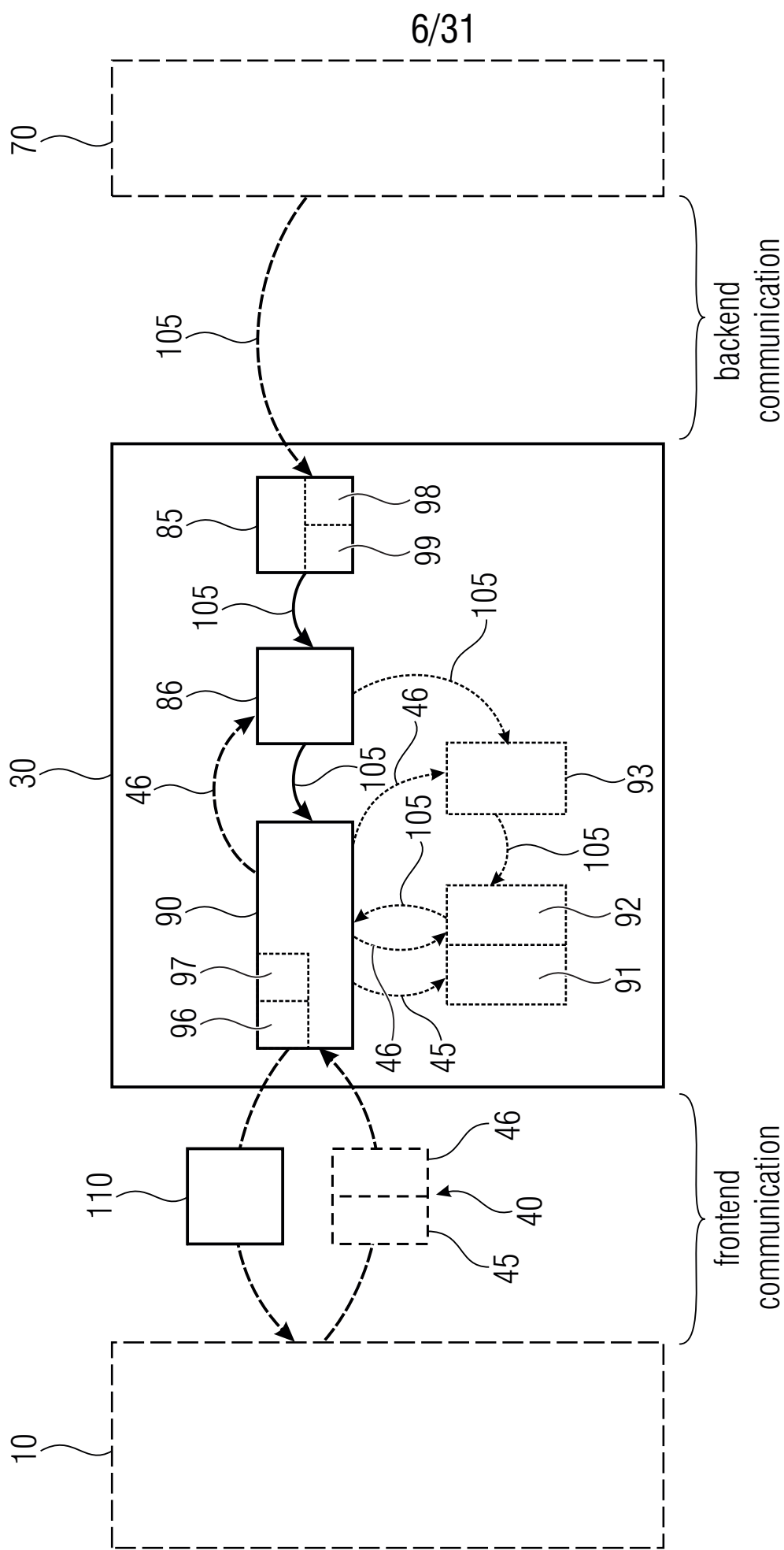


FIG 6

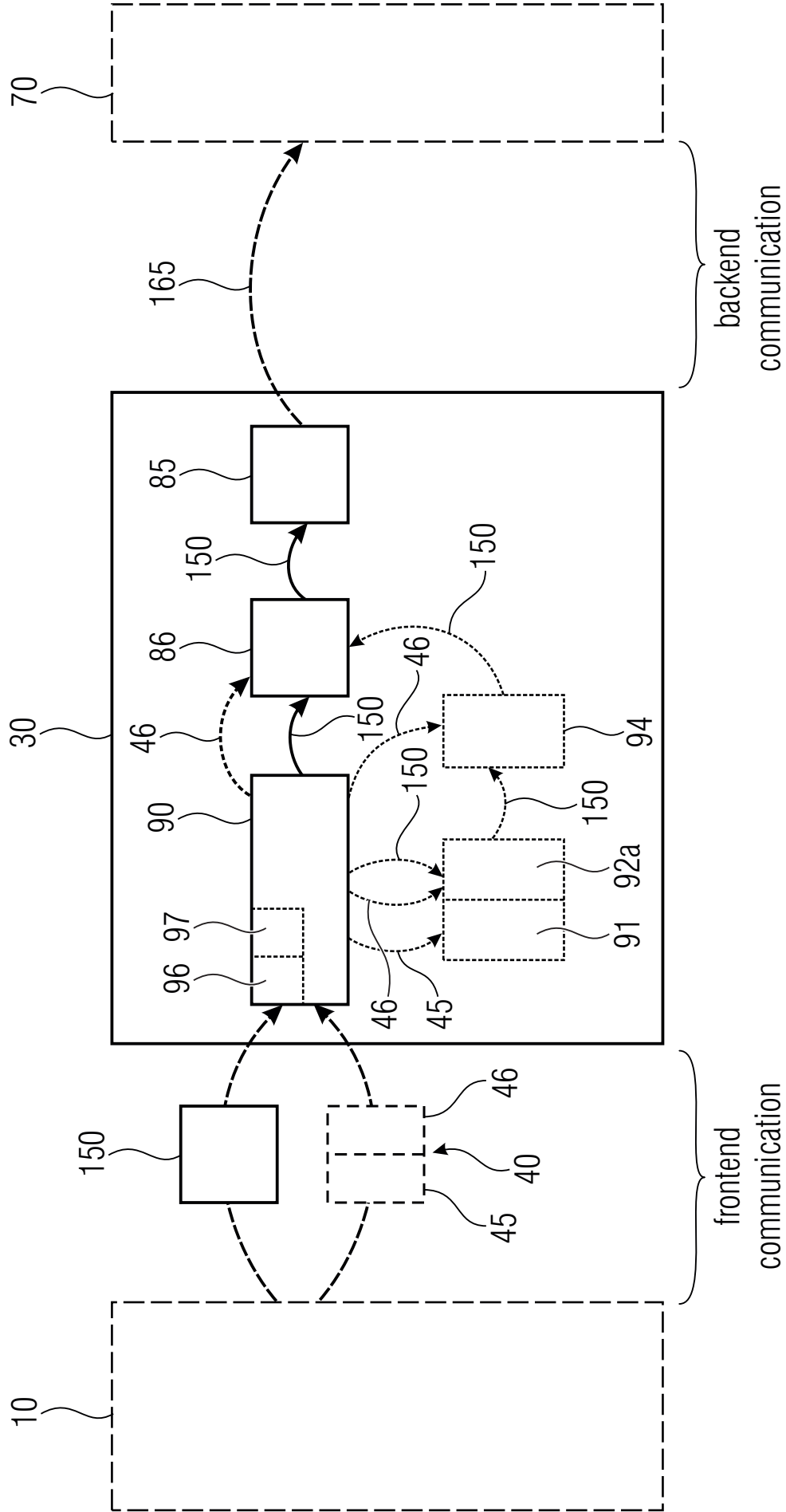


FIG 7

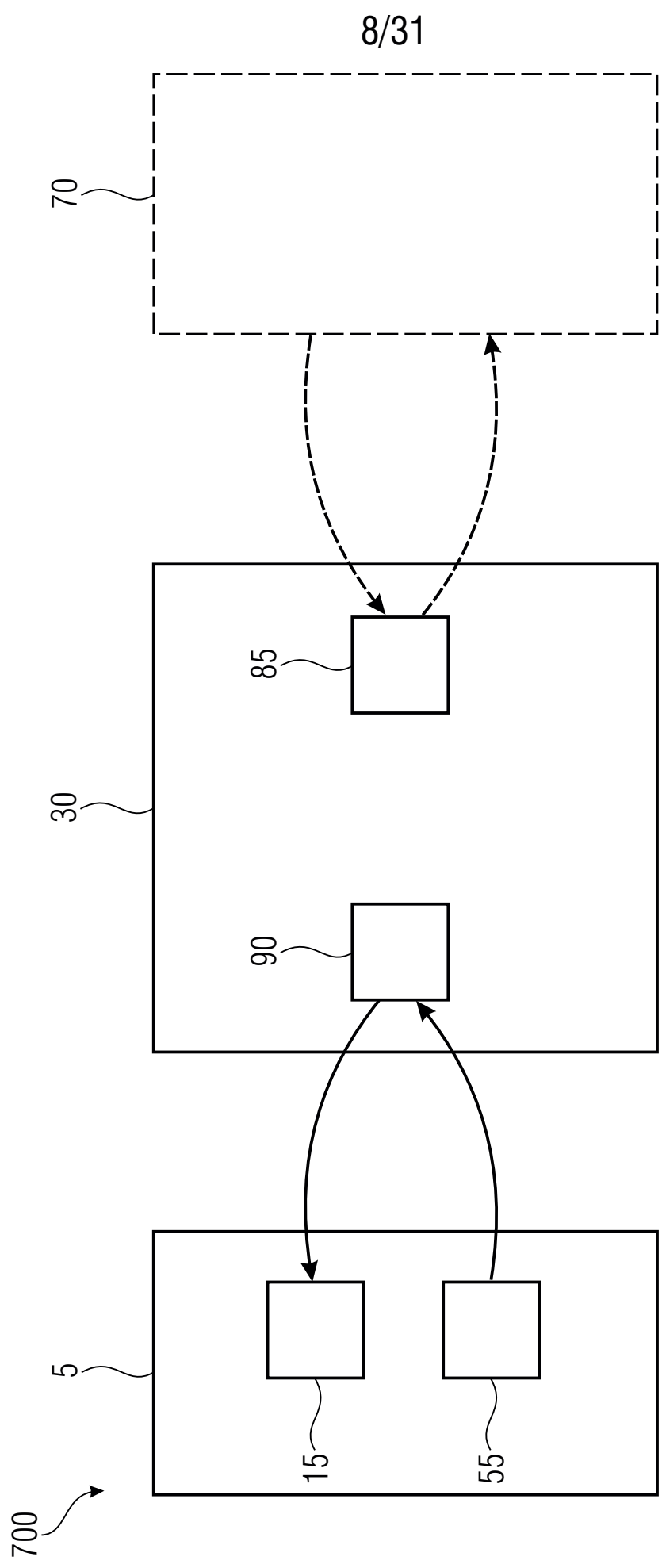


FIG 8

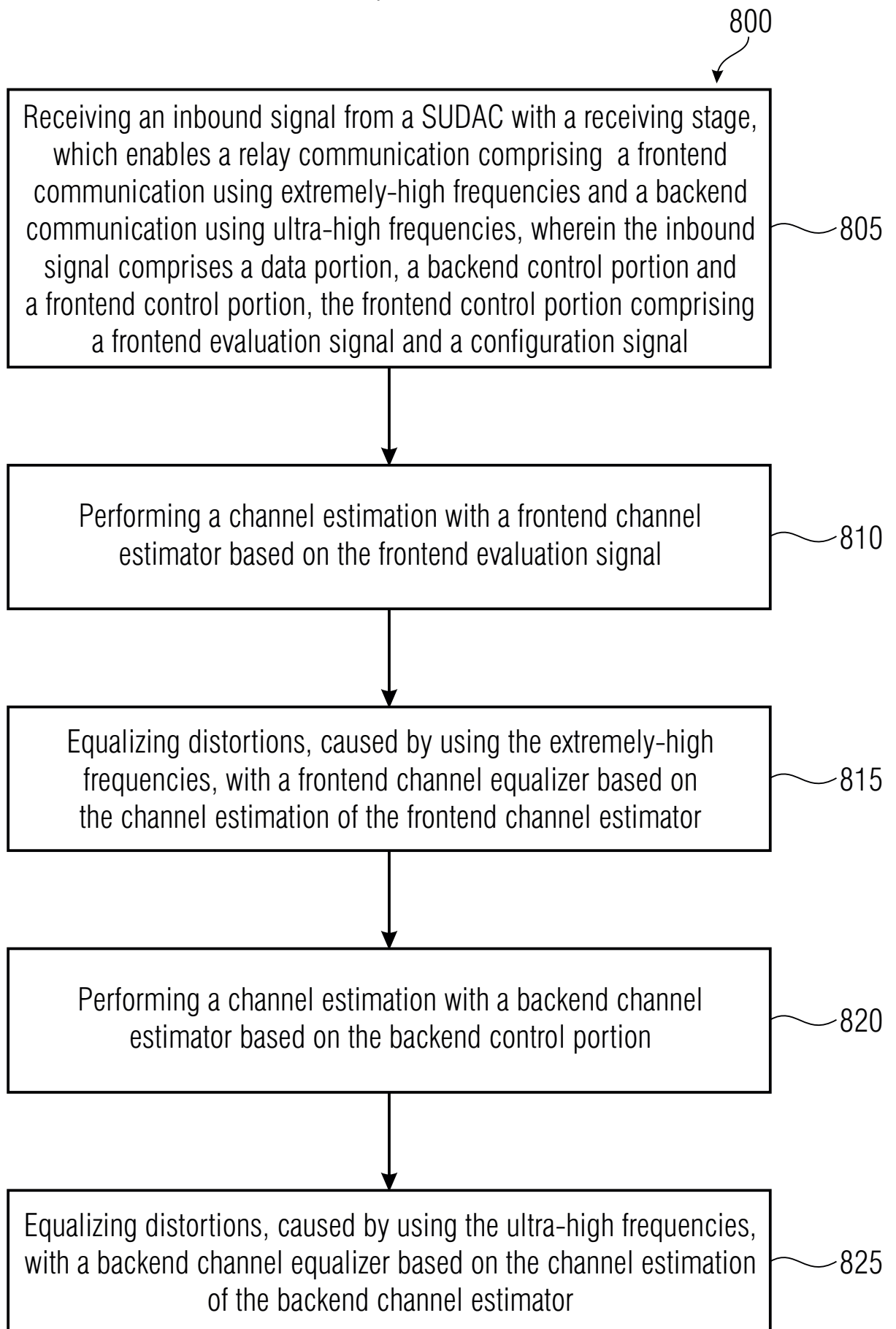


FIG 9

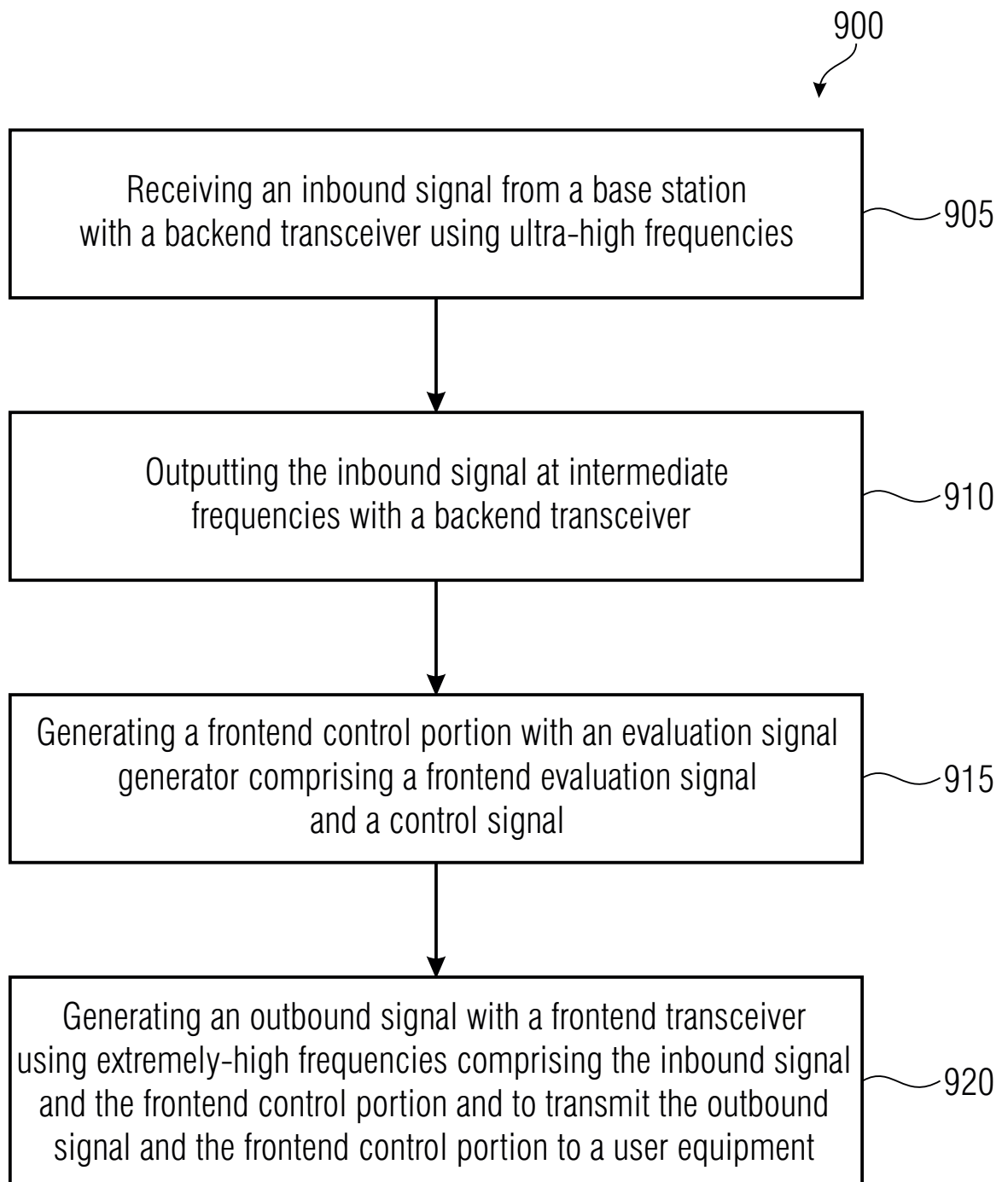


FIG 10

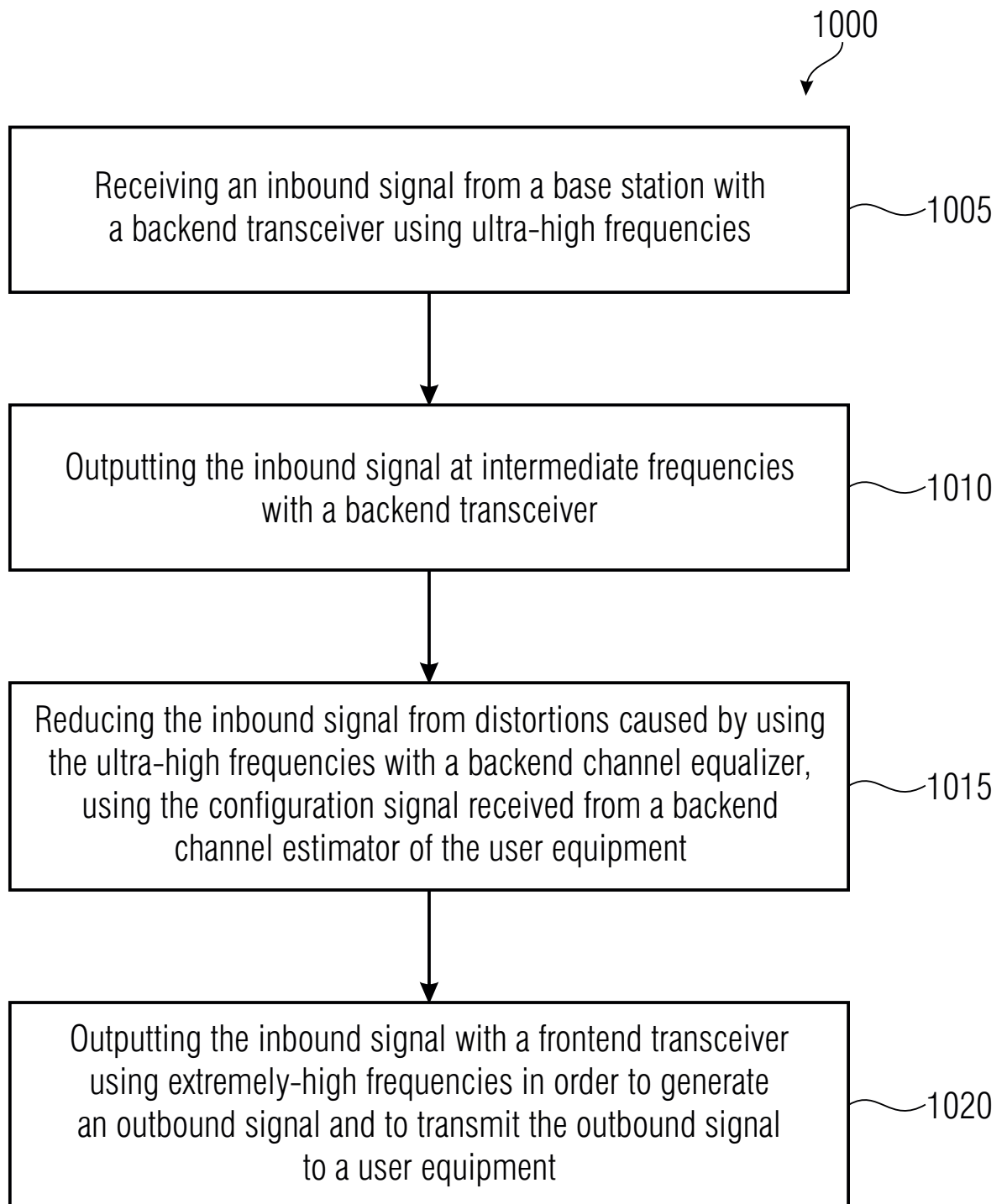


FIG 11

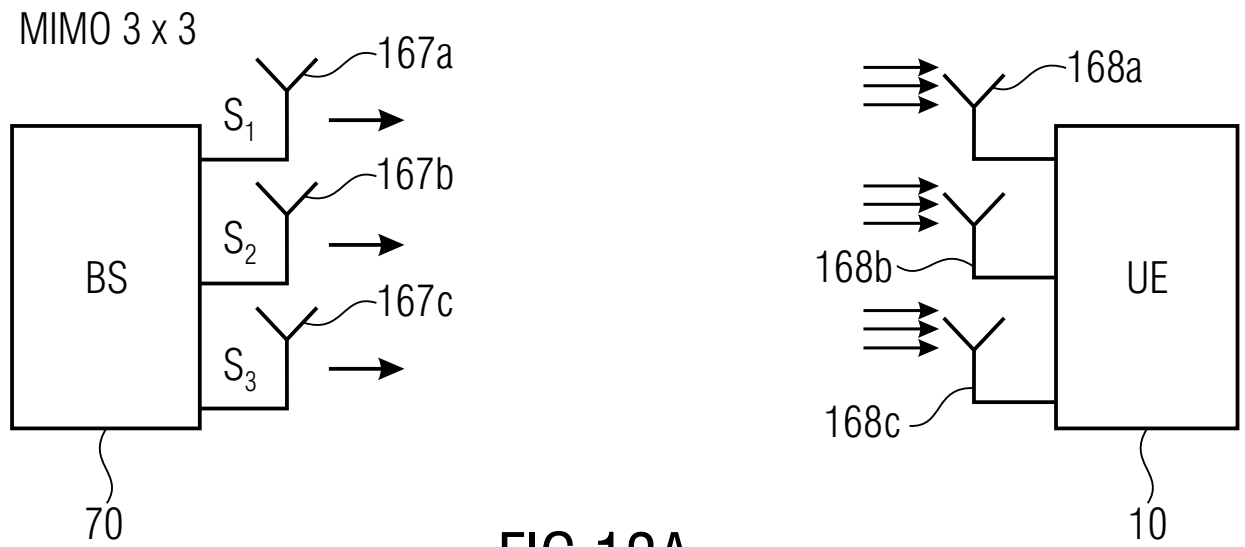


FIG 12A

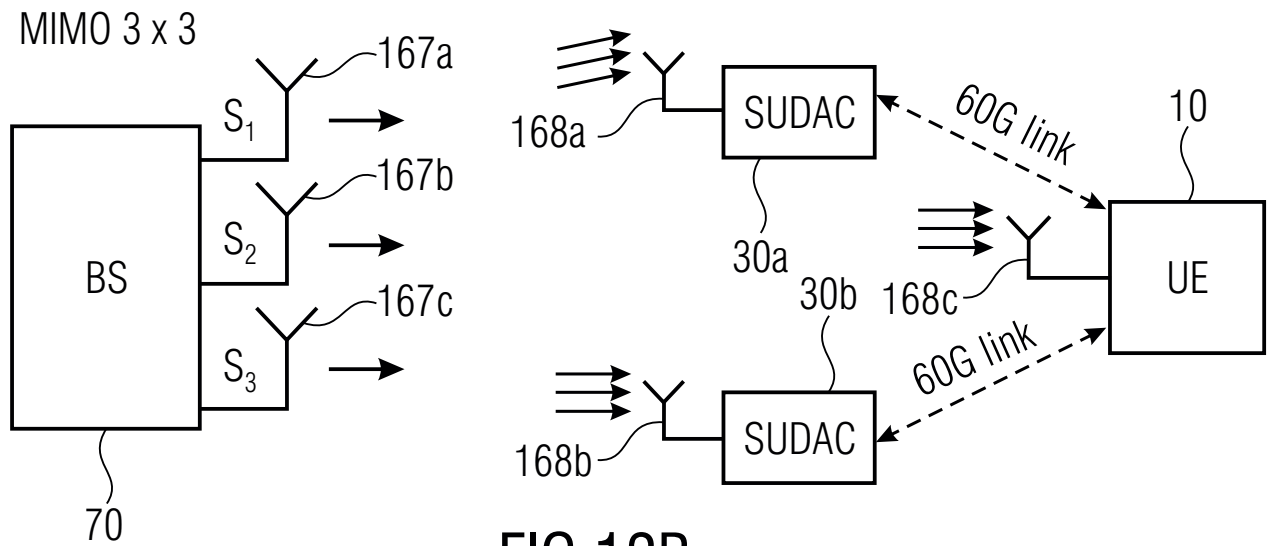


FIG 12B

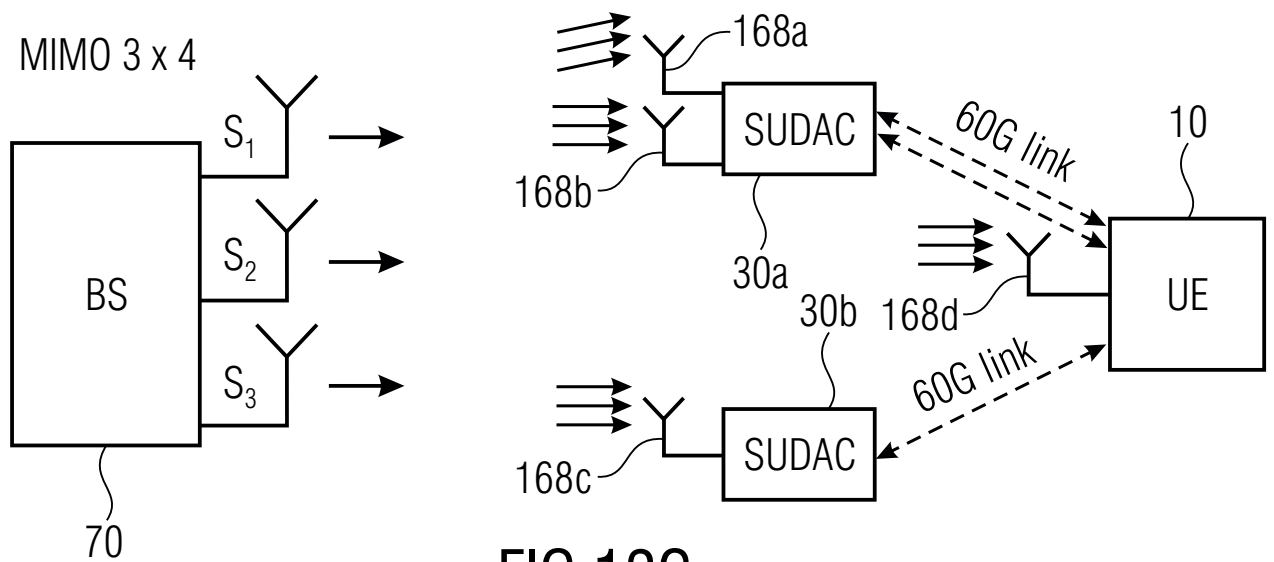


FIG 12C

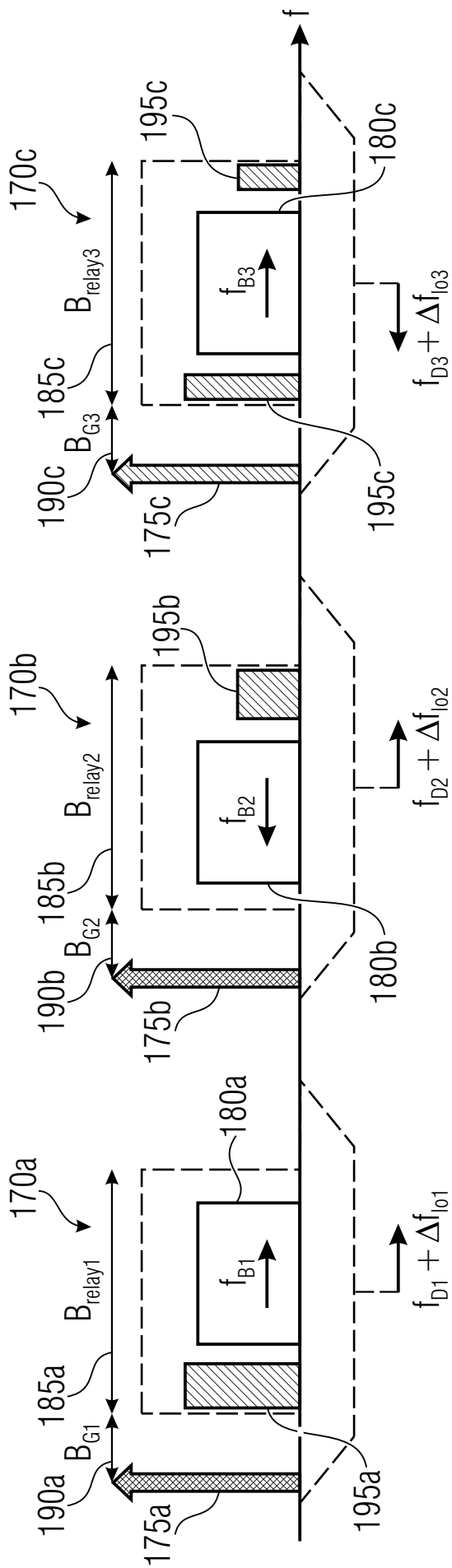


FIG 13A

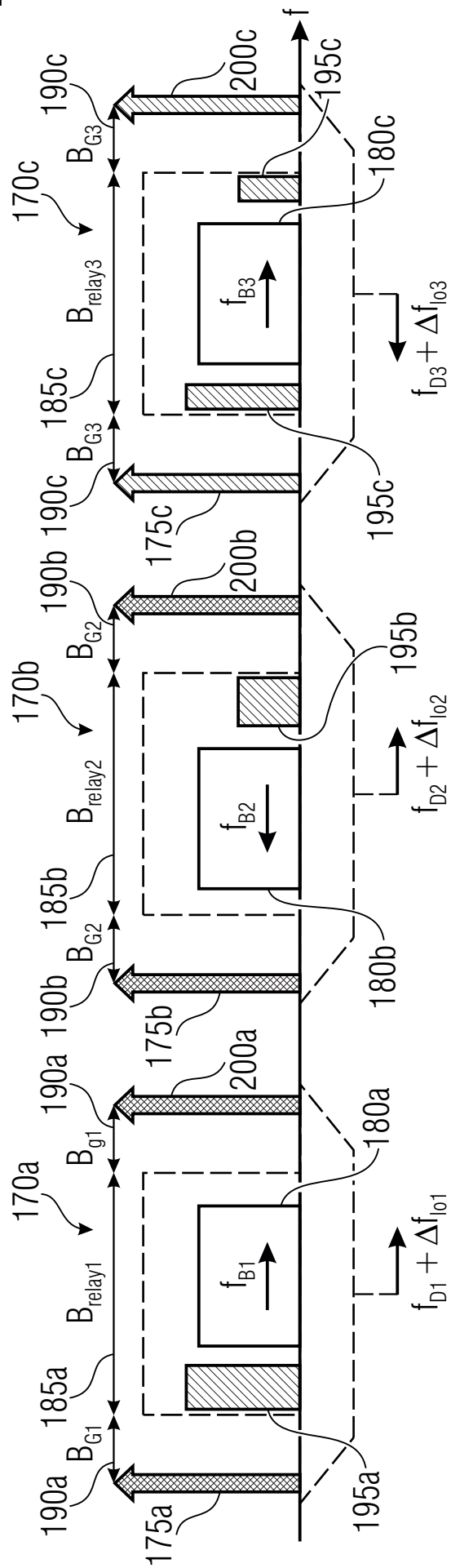


FIG 13B

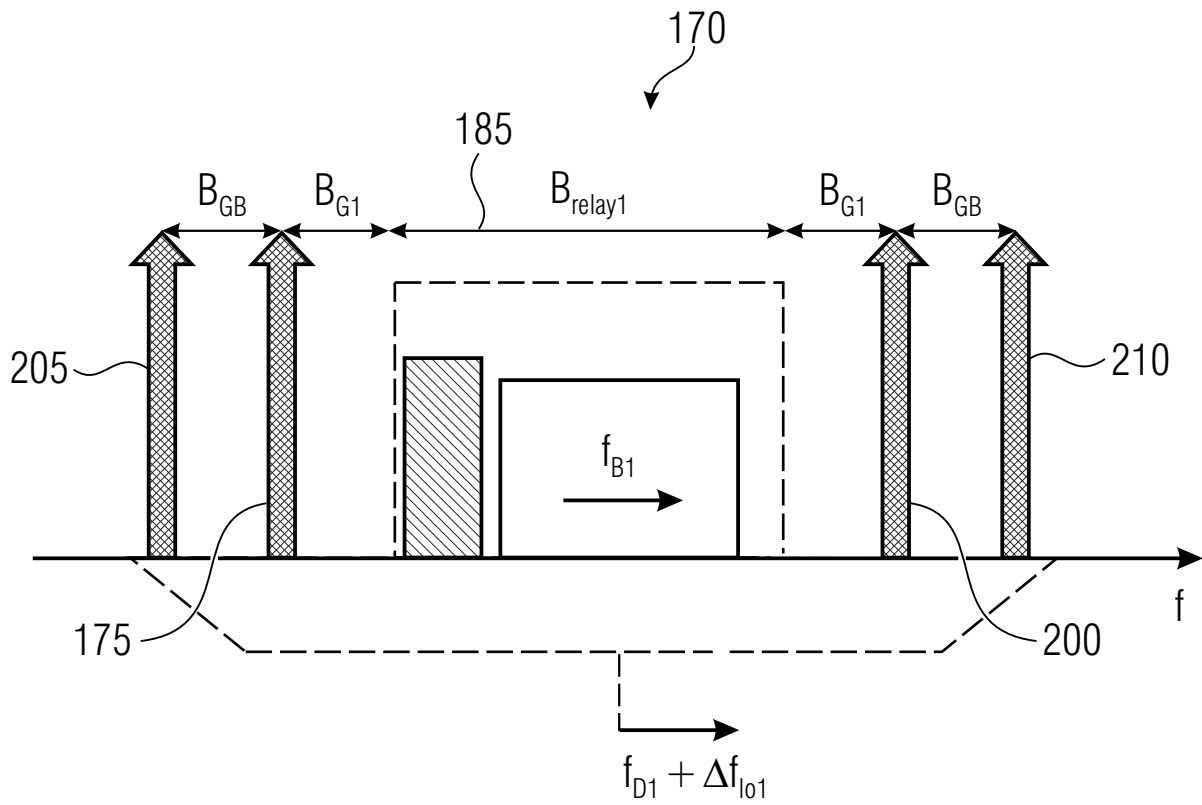


FIG 14

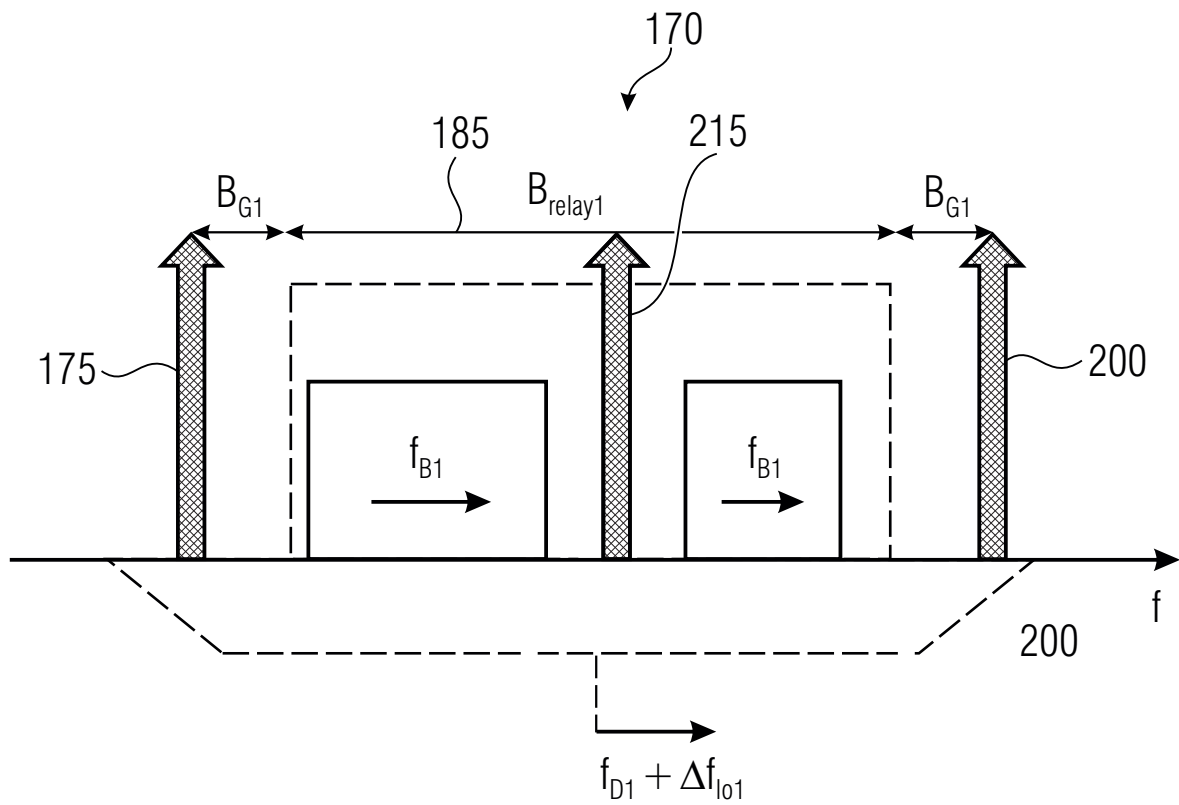


FIG 15

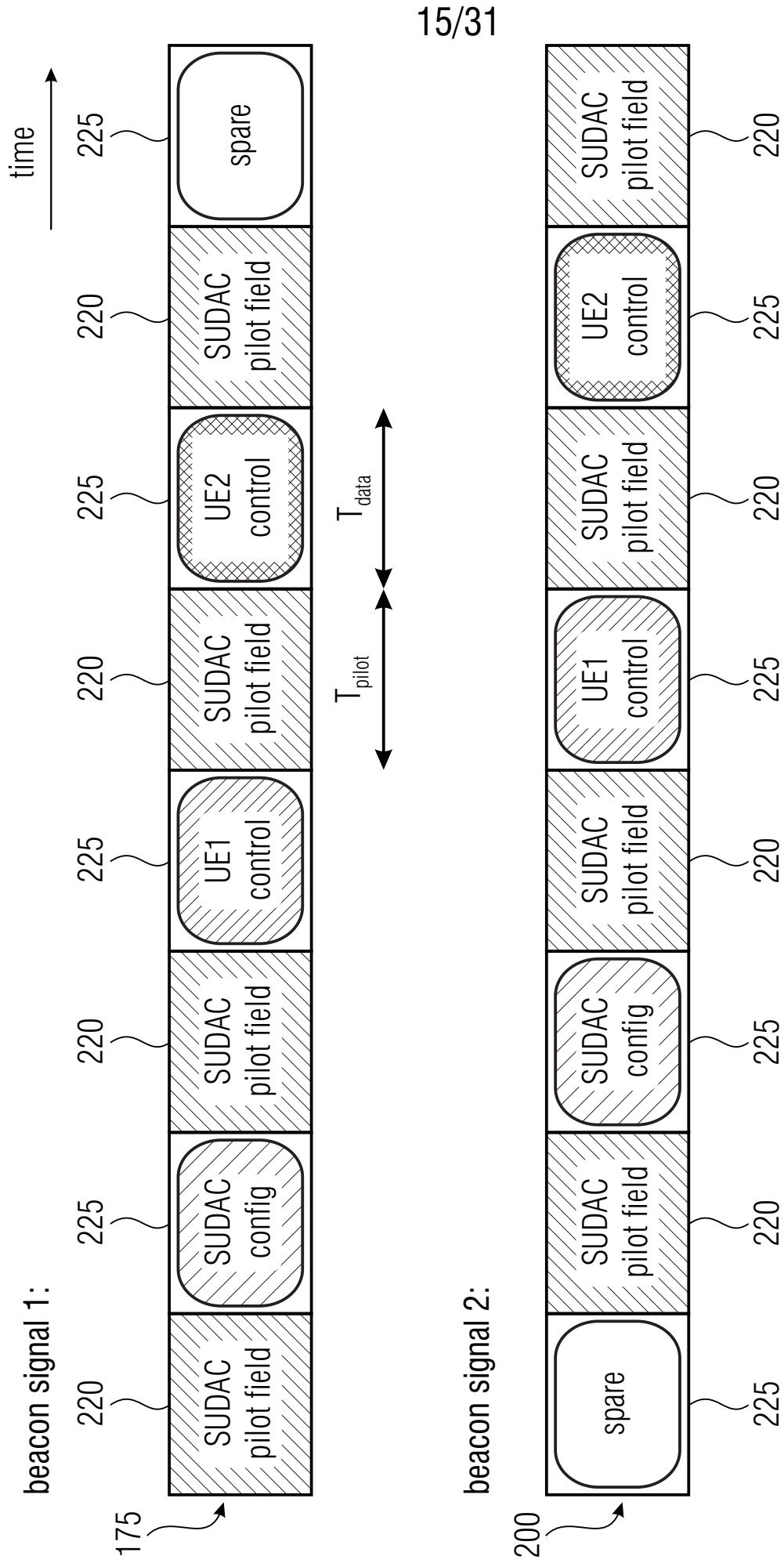


FIG 16

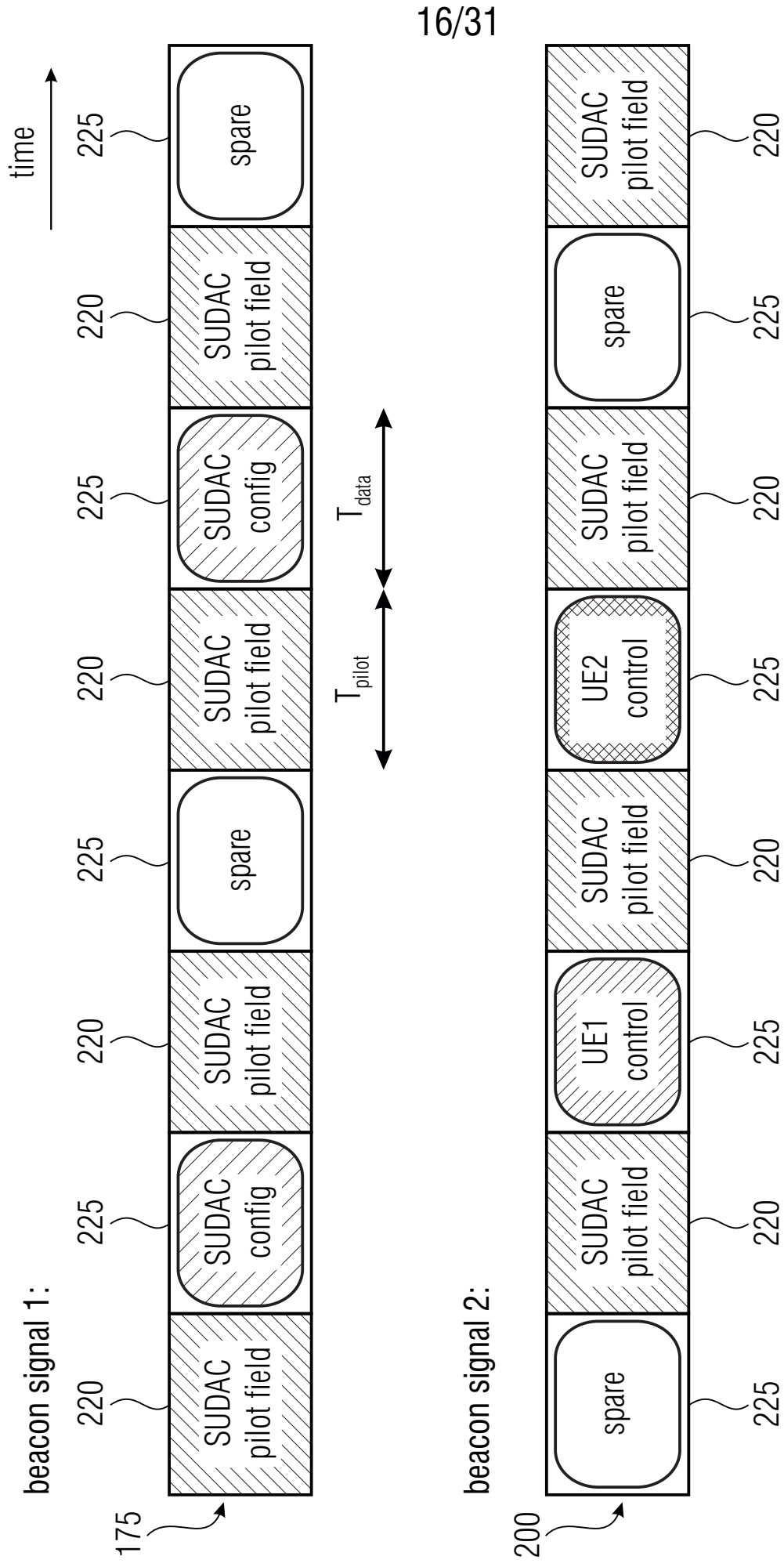


FIG 17

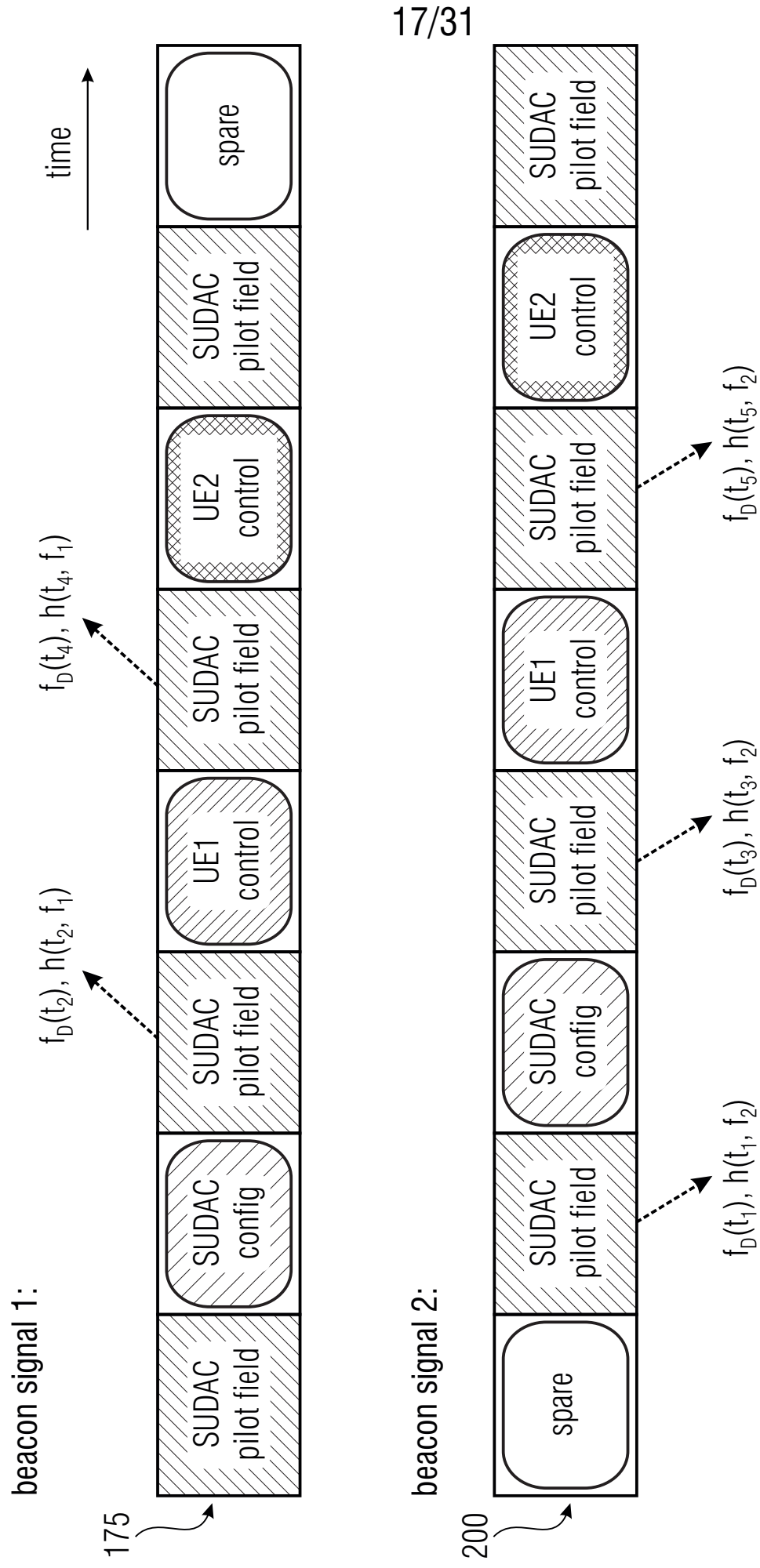


FIG 18

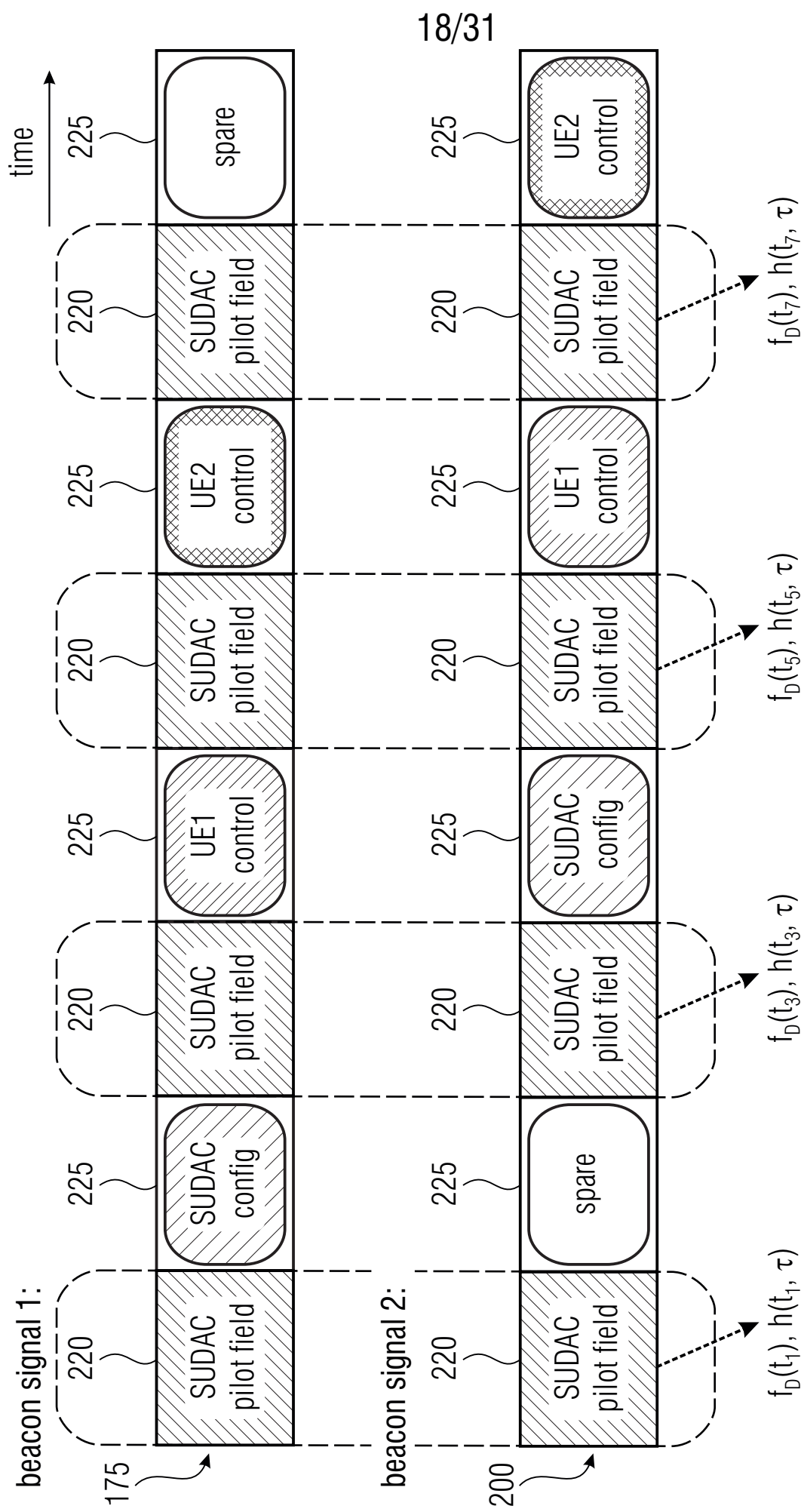


FIG 19

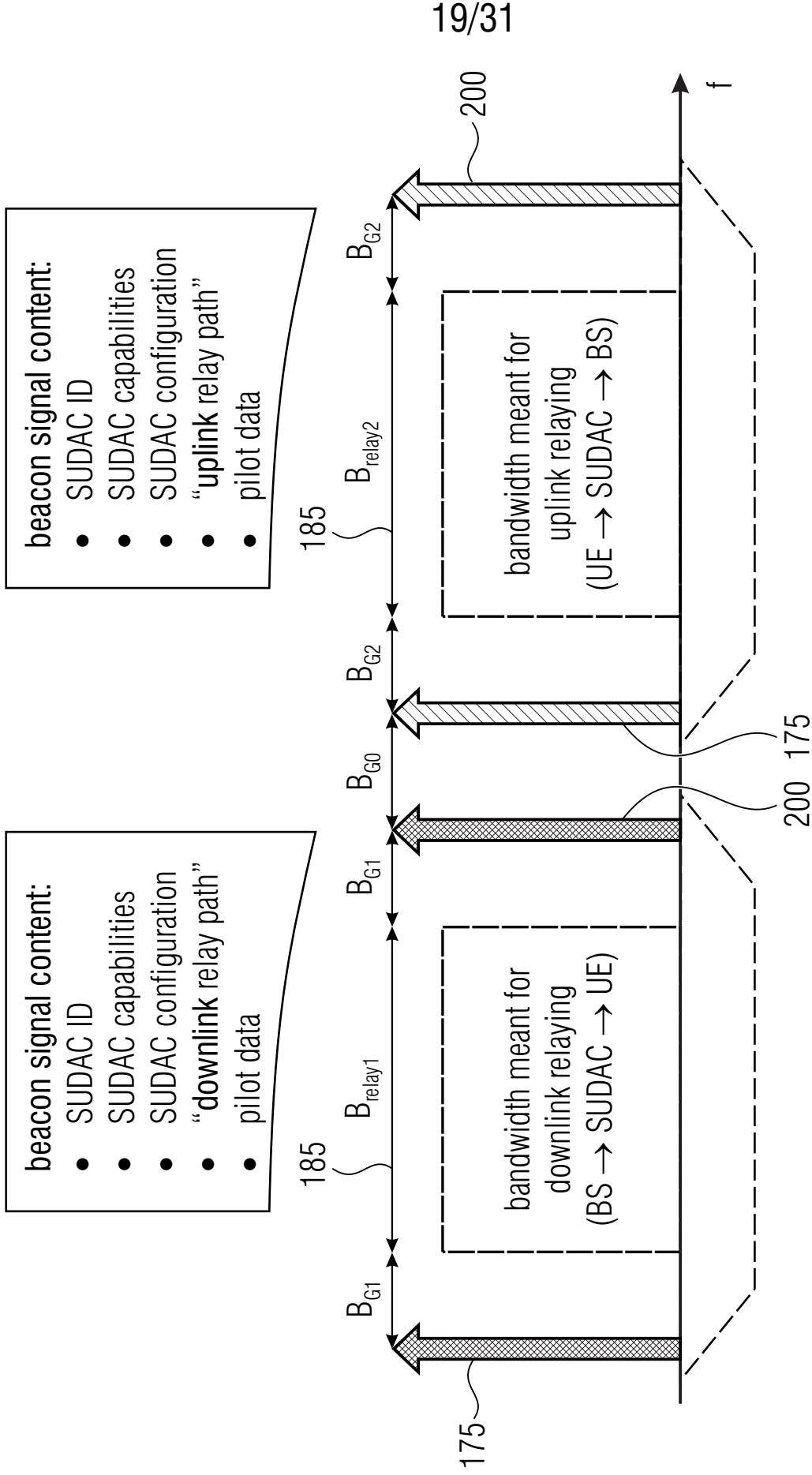


FIG 20

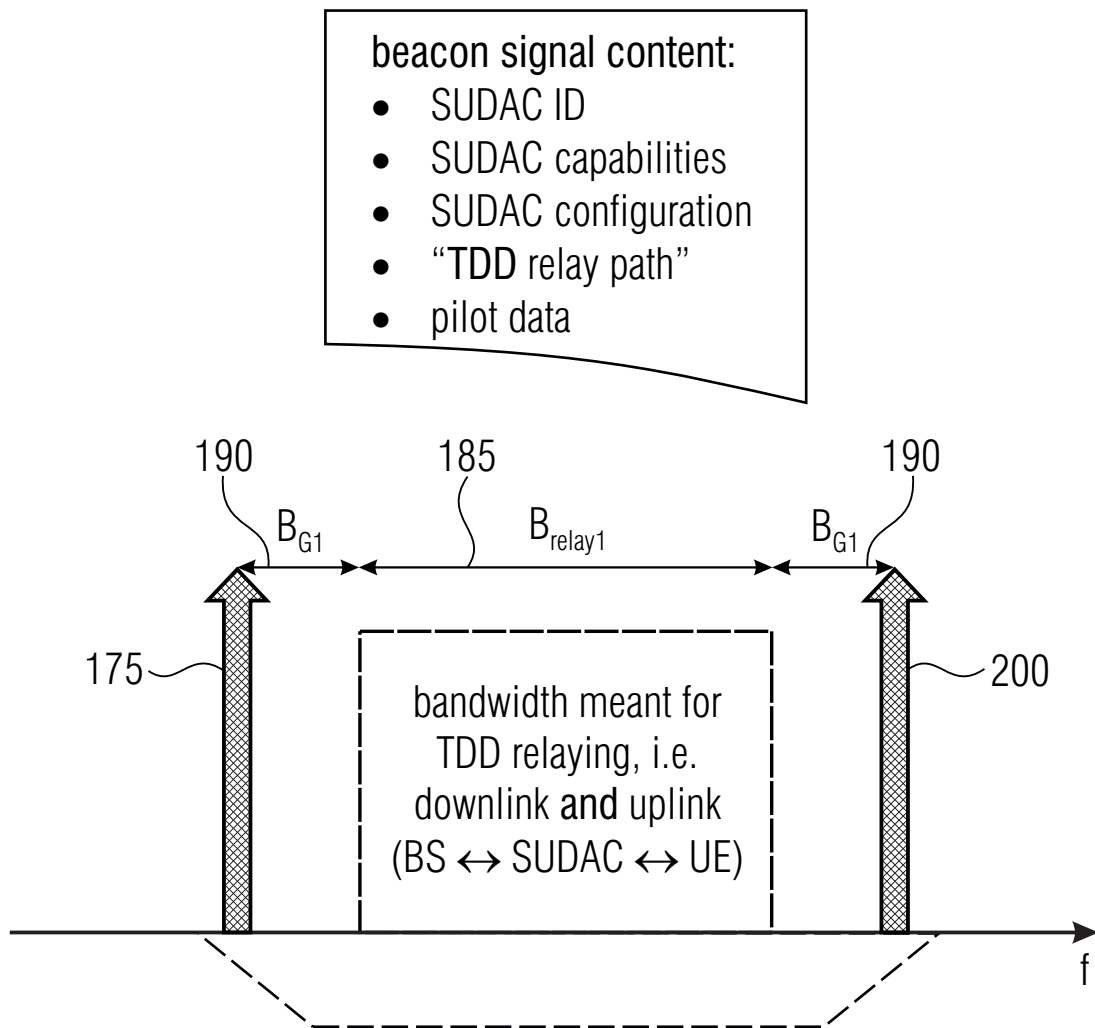


FIG 21

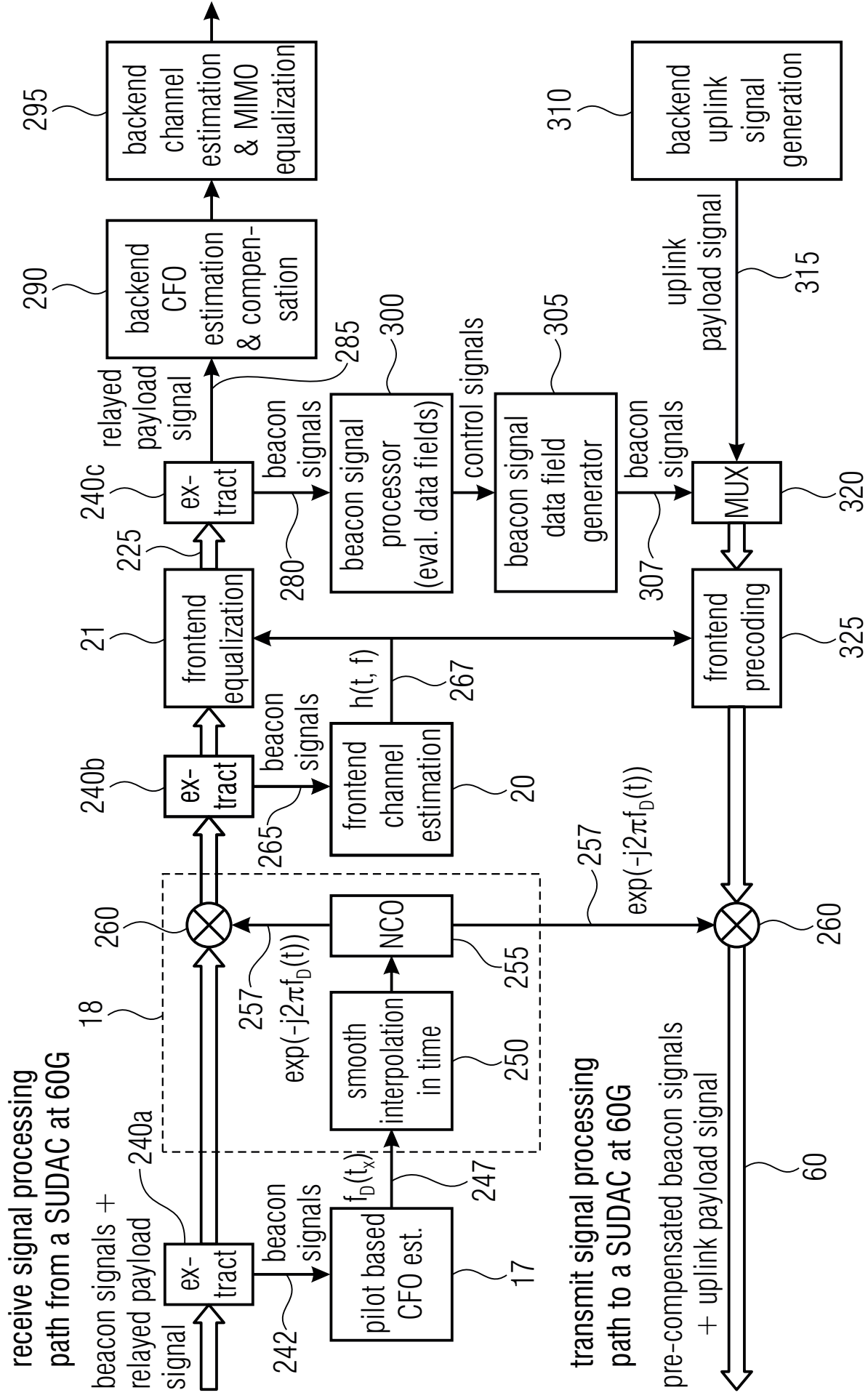
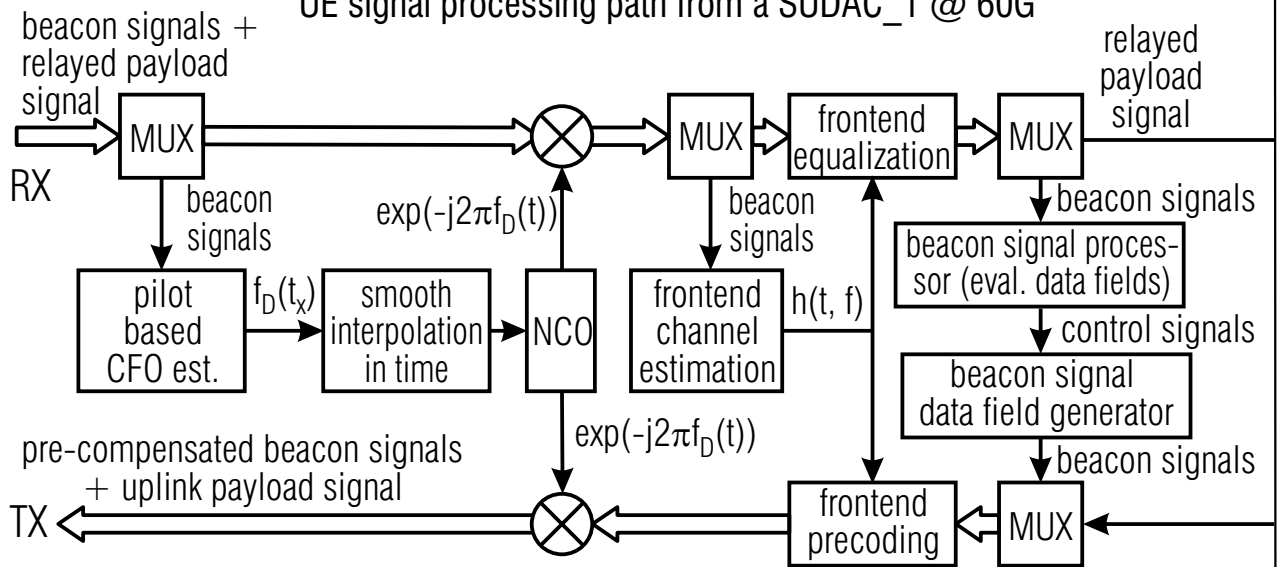
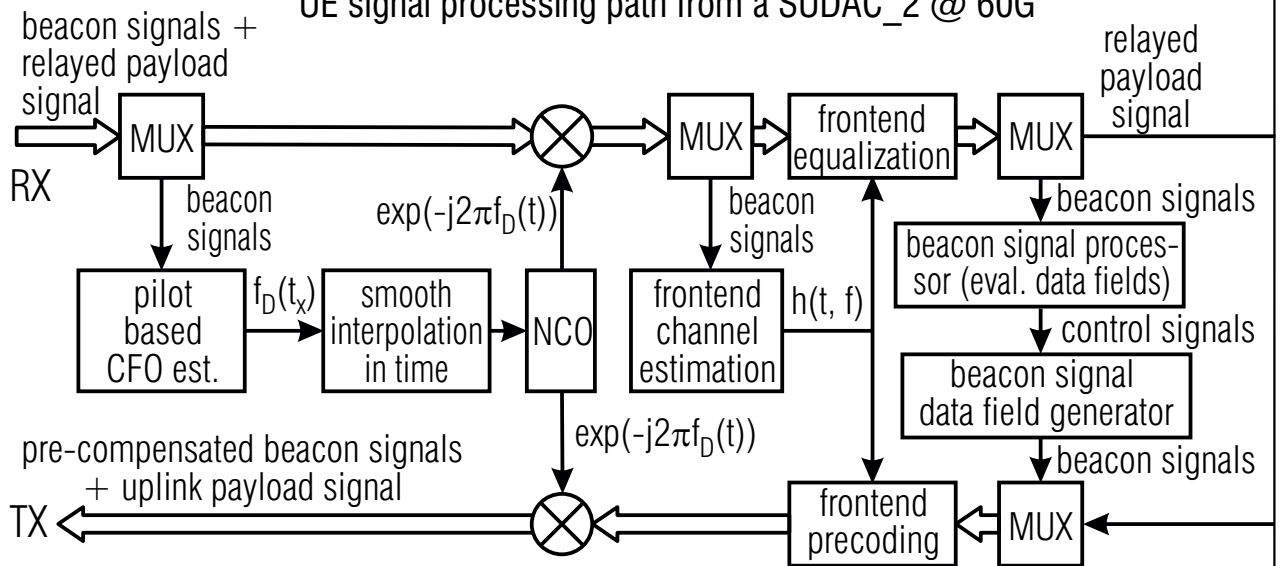


FIG 22

UE signal processing path from a SUDAC_1 @ 60G



UE signal processing path from a SUDAC_2 @ 60G



UE signal processing path from a SUDAC_3 @ 60G

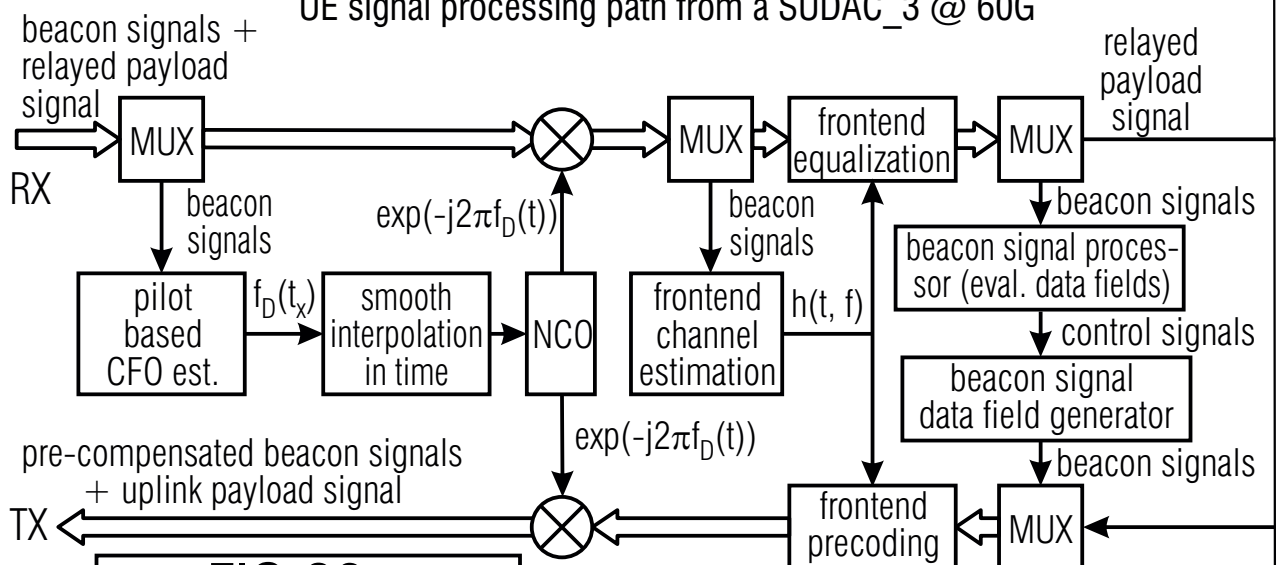


FIG 23
FIG 23A | FIG 23B

FIG 23A

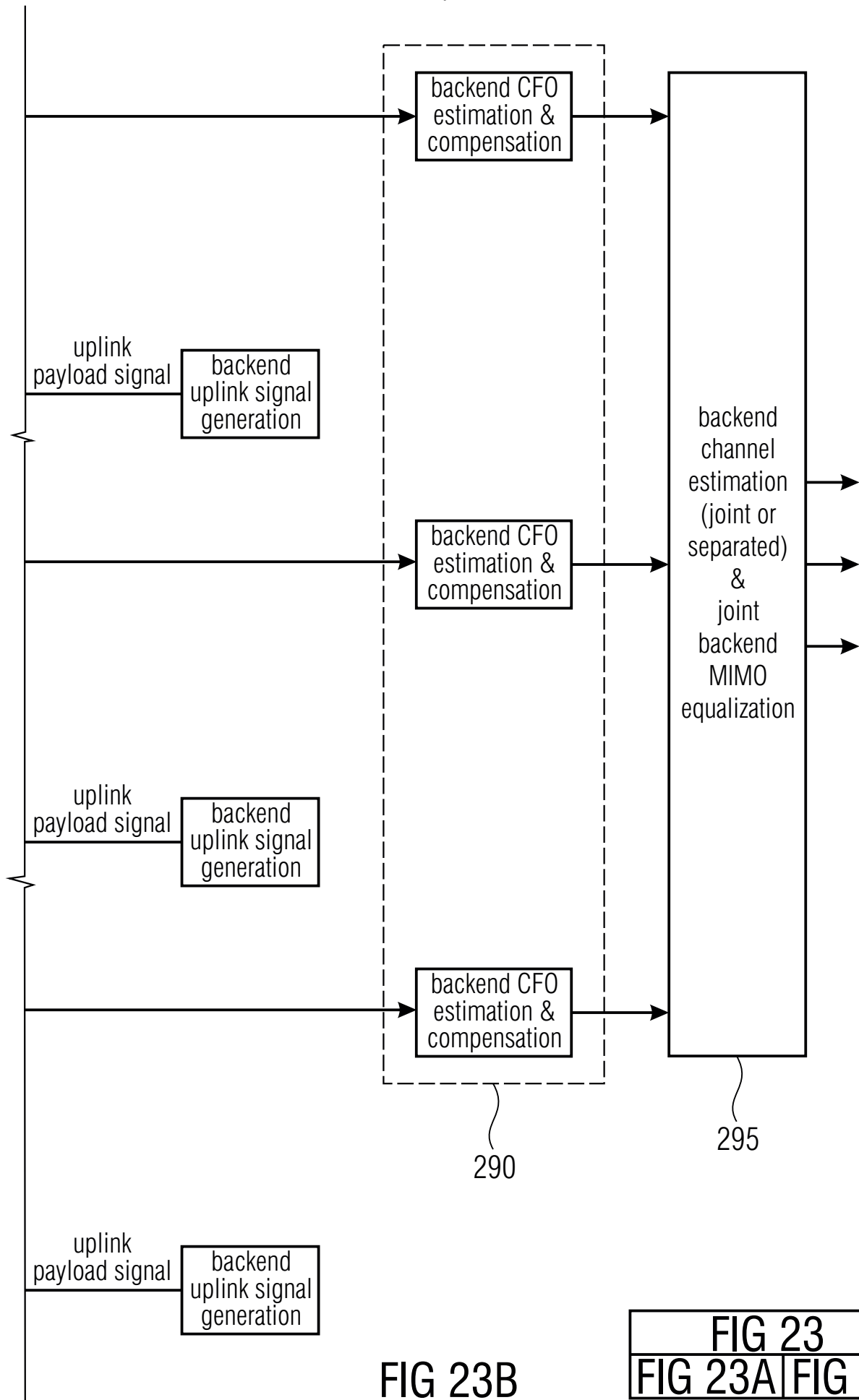


FIG 23	
FIG 23A	FIG 23B

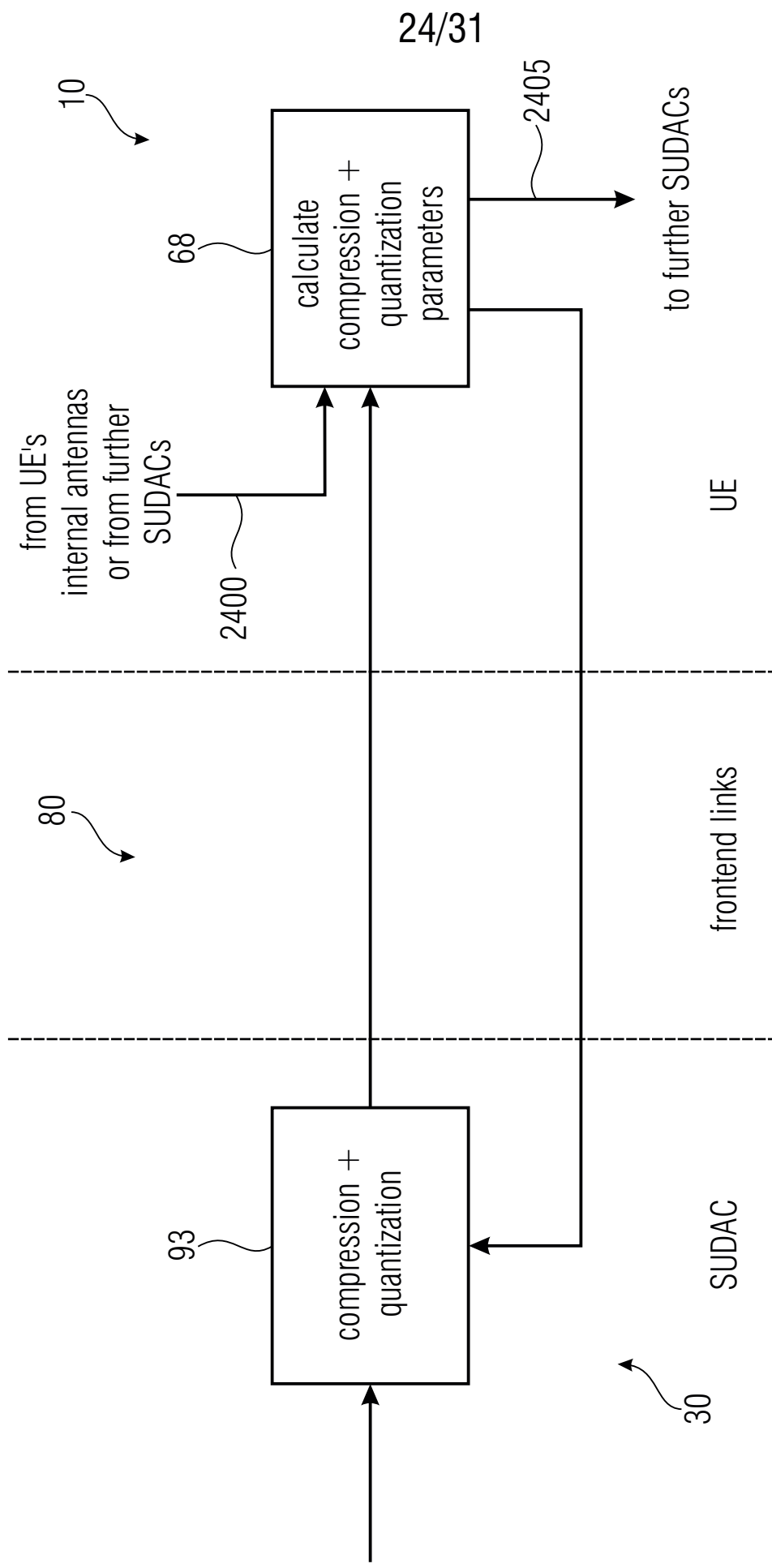


FIG 24A

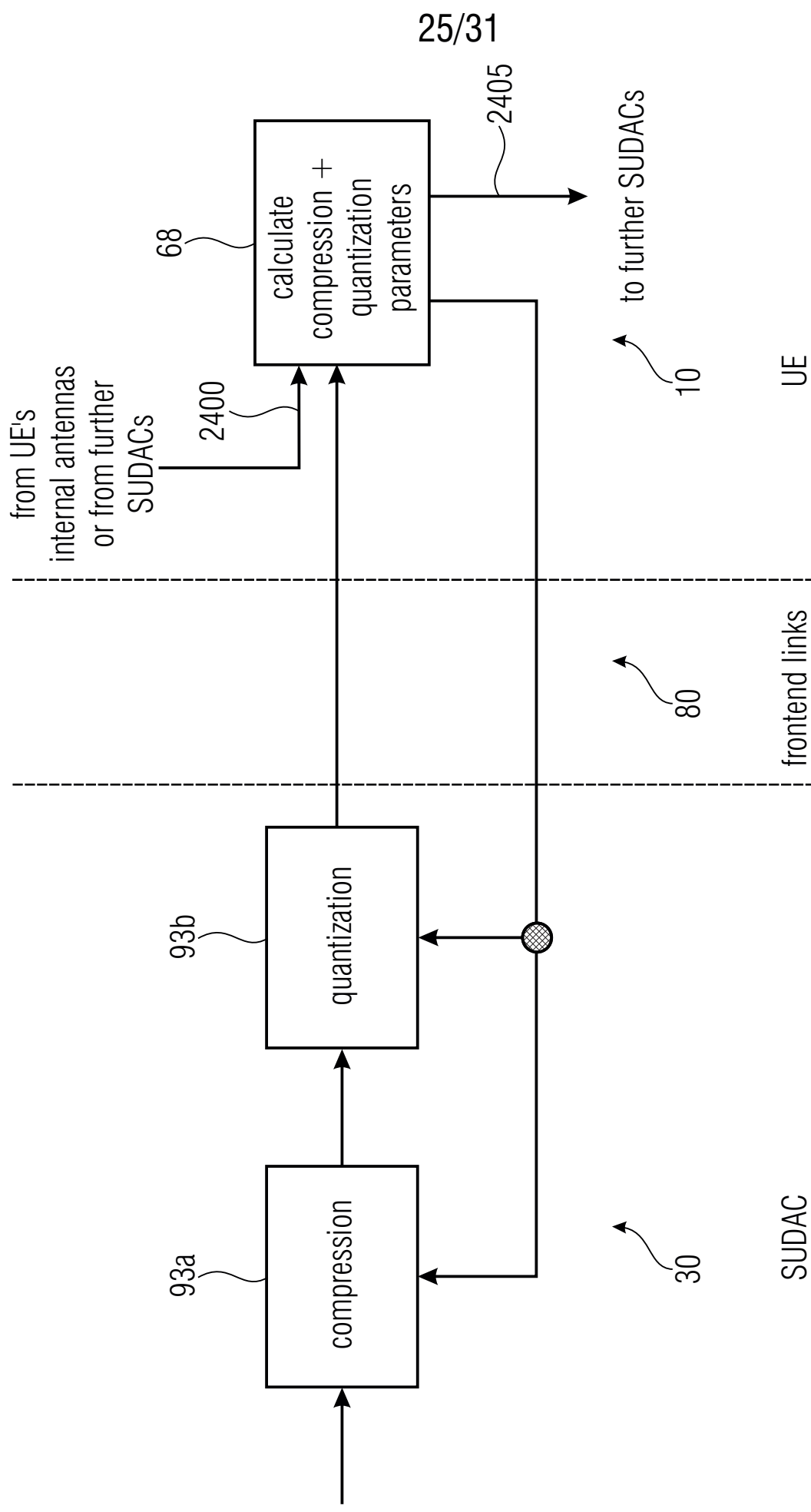


FIG 24B

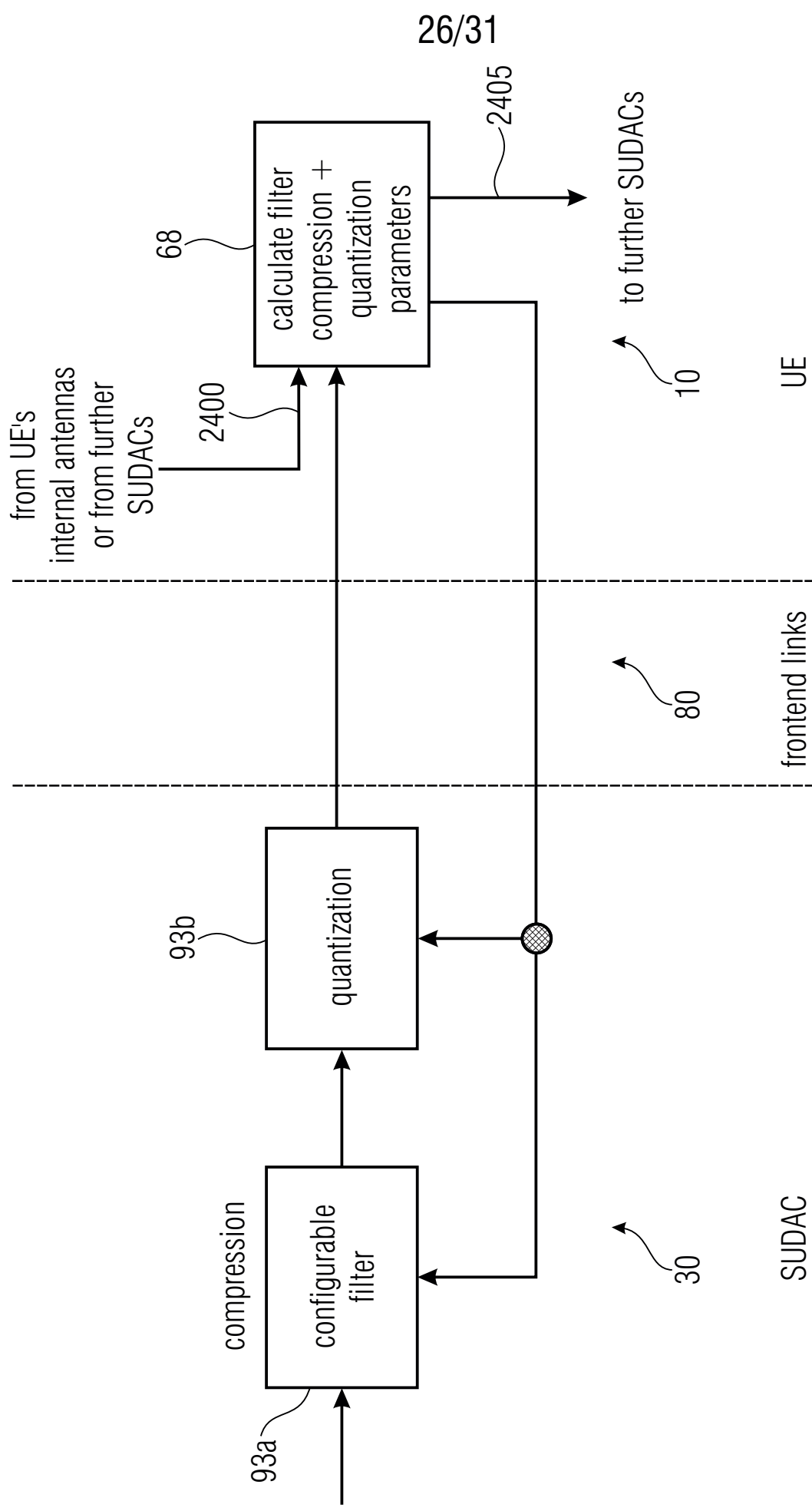


FIG 24C

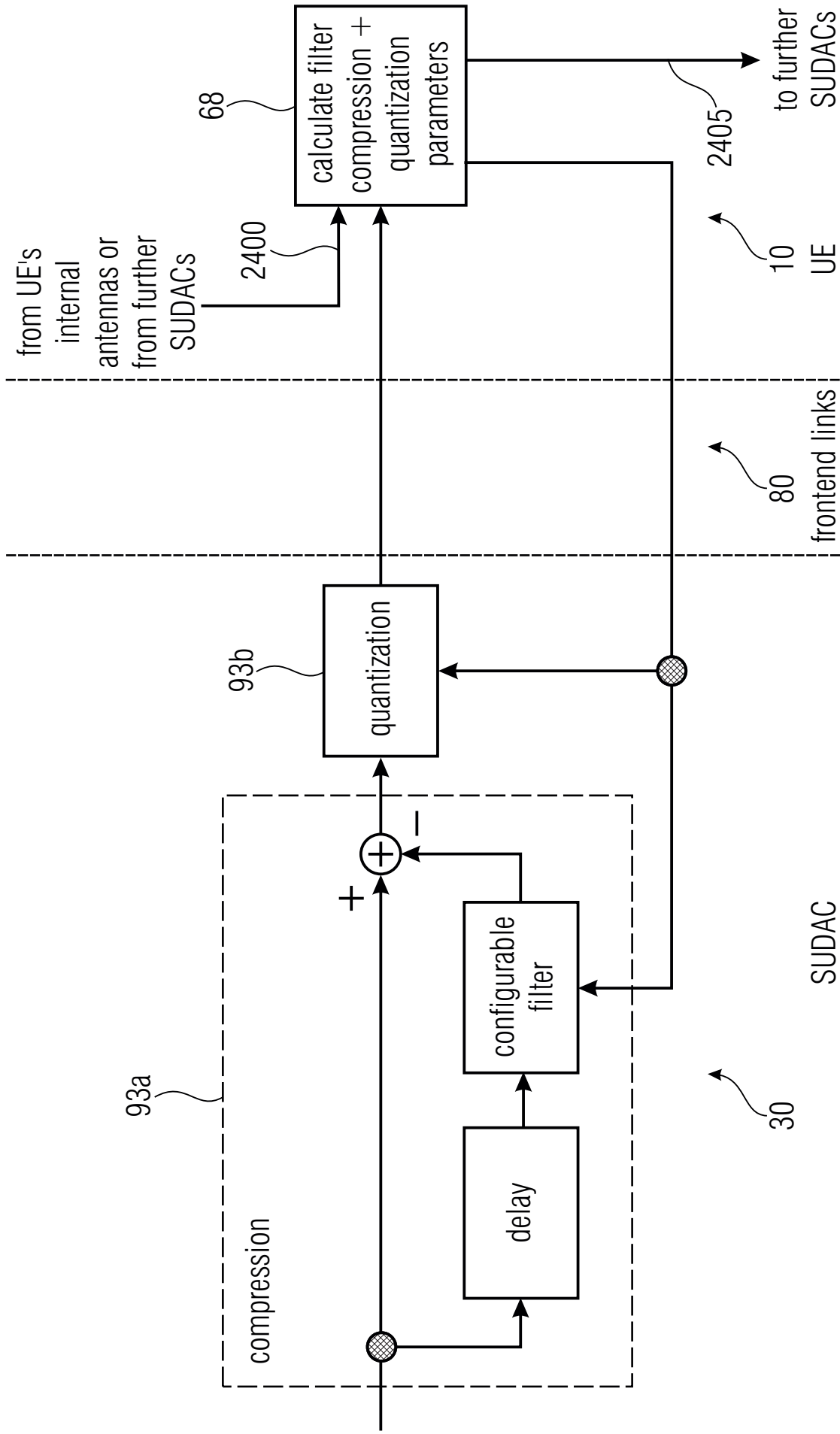


FIG 24D

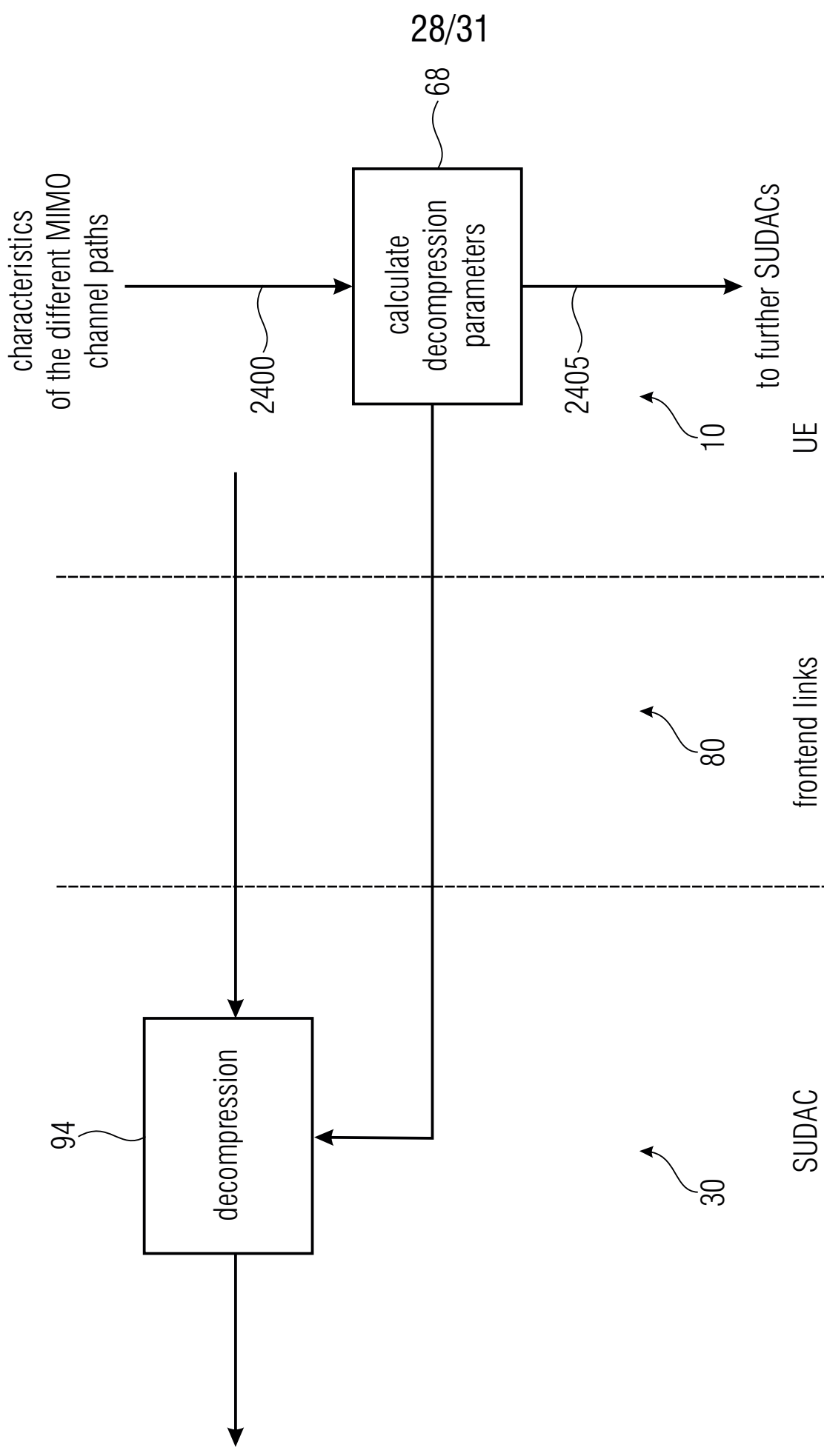


FIG 24E

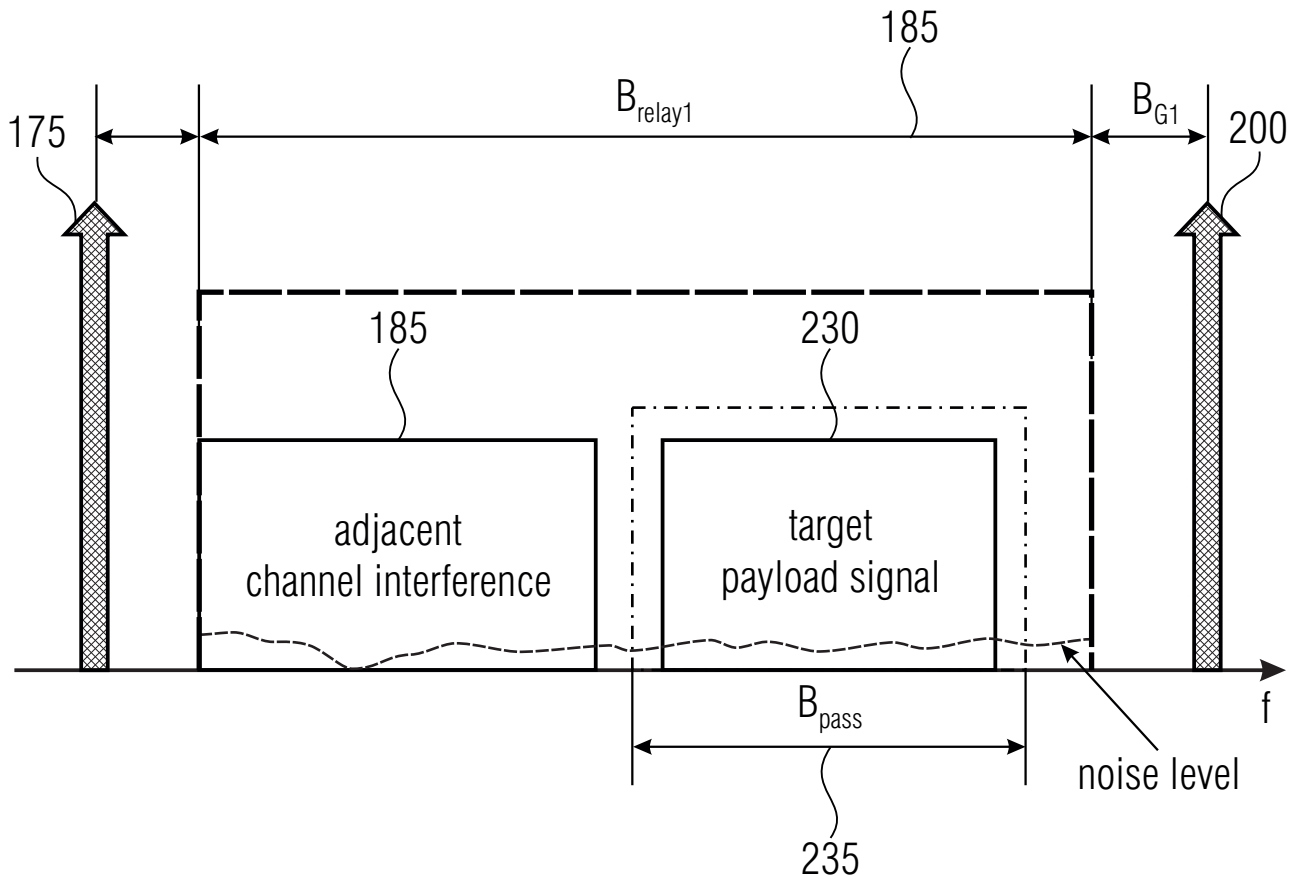


FIG 25

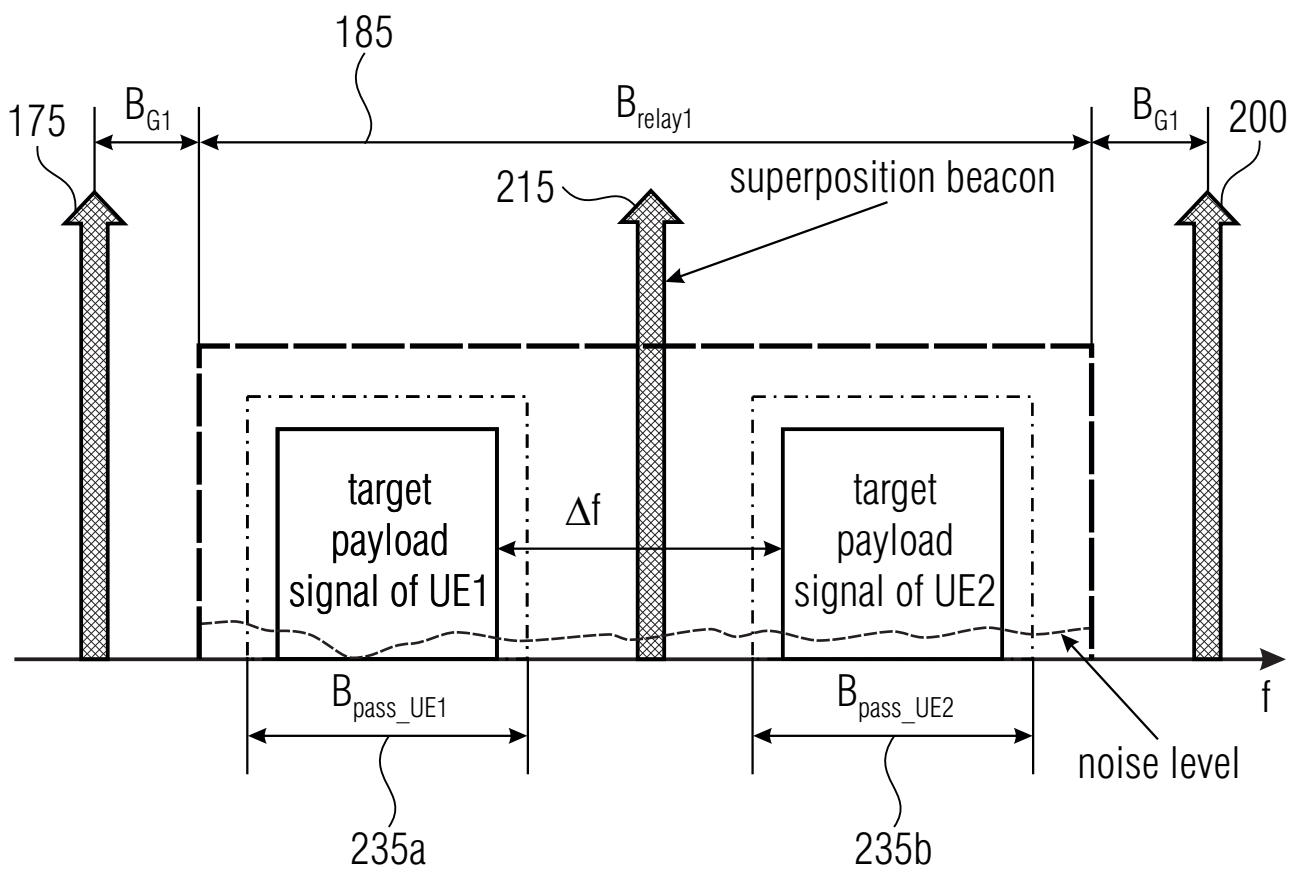


FIG 26

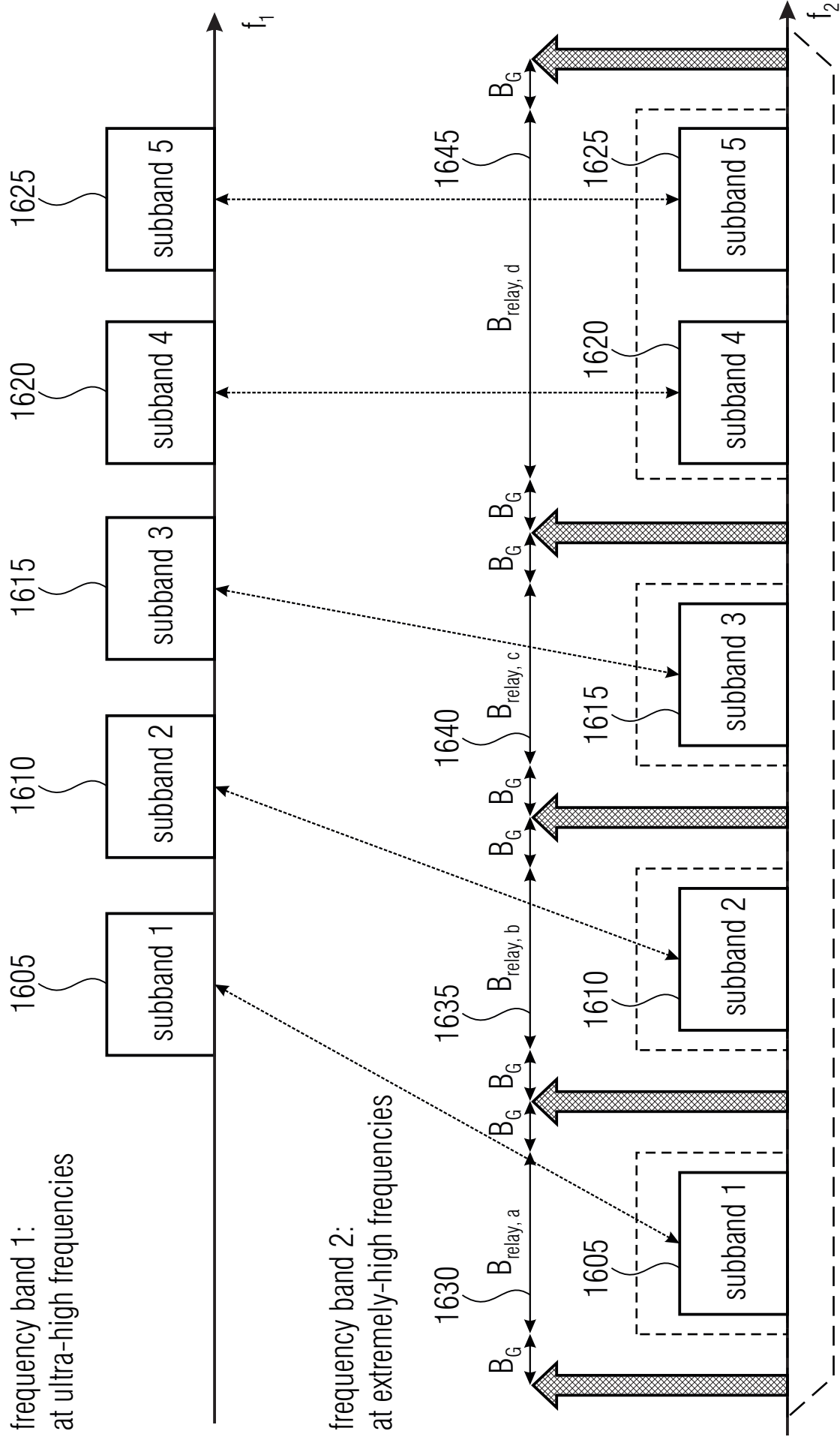


FIG 27

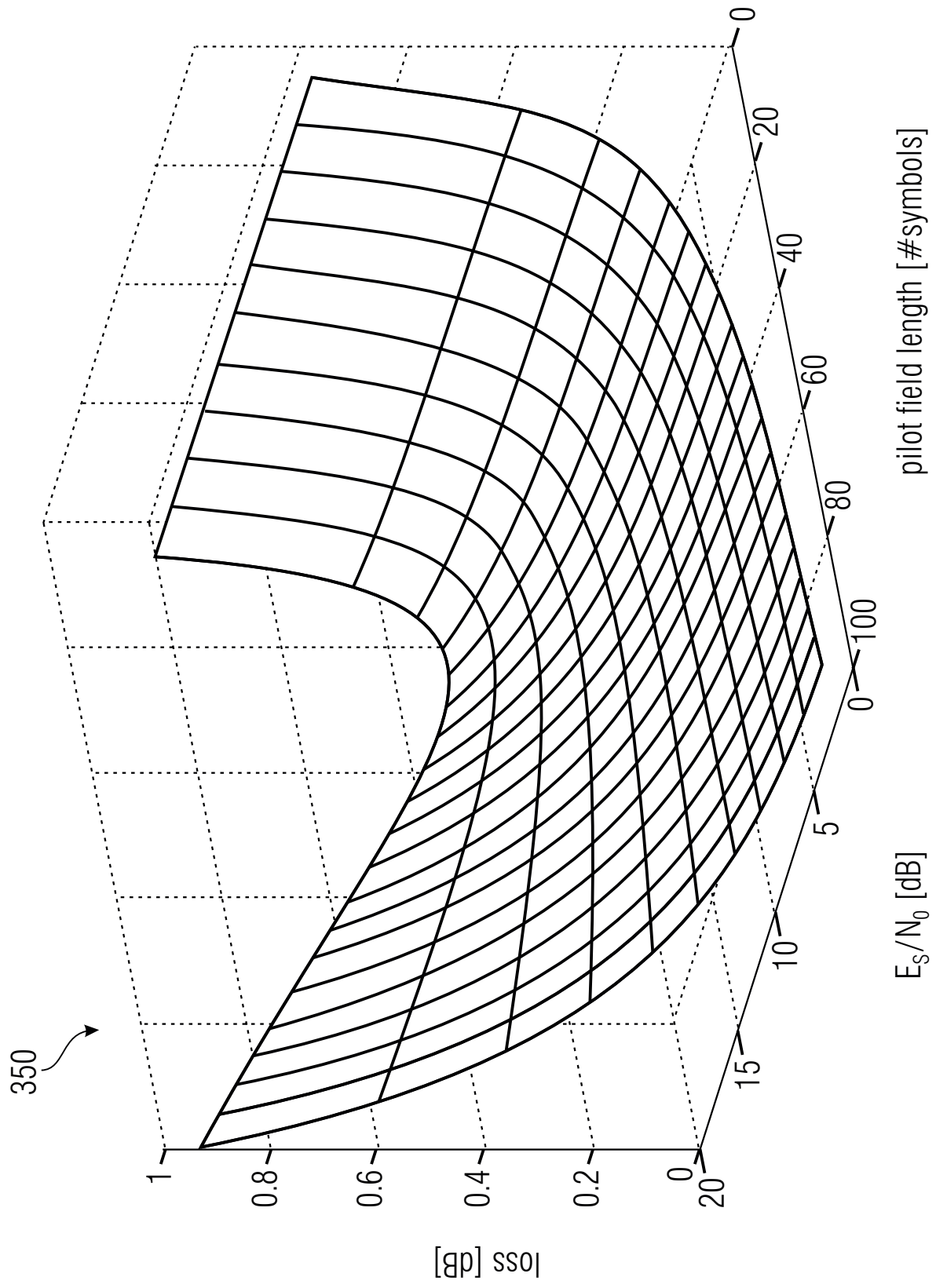


FIG 28

The diagram illustrates a network architecture with the following components and connections:

- Frontend Communication (75b):**
 - 10:** A dashed box representing the overall system boundary.
 - 5:** A solid box within the system boundary.
 - 16:** A dashed box containing a sub-component.
 - 17:** A vertical stack of five rectangular blocks.
 - 18:** A horizontal dashed line passing through the middle of the stack 17.
 - 19:** A dashed box at the bottom of the stack 17.
 - 20:** A dashed box at the top of the stack 17.
 - 21:** A dashed box on the left side of the stack 17.
 - 22:** A dashed box on the right side of the stack 17.
 - 23:** A dashed box on the left side of the stack 17.
 - 24:** A dashed box on the right side of the stack 17.
 - 25:** A dashed box on the right side of the stack 17.
 - 30:** A dashed box on the right side of the stack 17.
 - 35:** A dashed box on the right side of the stack 17.
 - 40:** A dashed box on the right side of the stack 17.
 - 42:** A dashed box on the right side of the stack 17.
 - 45:** A dashed box on the right side of the stack 17.
 - 46:** A dashed box on the right side of the stack 17.
 - 50:** A dashed box on the right side of the stack 17.
 - 55:** A dashed box on the right side of the stack 17.
 - 60:** A dashed box on the right side of the stack 17.
 - 65:** A dashed box on the right side of the stack 17.
 - 66:** A dashed box on the right side of the stack 17.
 - 67:** A dashed box on the right side of the stack 17.
 - 68:** A dashed box on the right side of the stack 17.
- Backend Communication:**
 - 75a:** A dashed box representing the overall system boundary.
 - 75b:** A dashed box representing the overall system boundary.

Arrows indicate data flow and connections between these components, showing a complex interplay between the front-end and back-end communication paths.

FIG 3