

MAINBOARDS FOR SECURITY VERIFICATION, SECURITY VERIFICATION METHODS AND APPARATUSES, AND MEDIA

ABSTRACT

This specification discloses a mainboard for security verification, a security verification
5 method and apparatus, and a medium. A verification unit connected to a channel management unit
and a channel management unit connected to each device to be verified are disposed. After an
electronic device is powered on, the verification unit is controlled to send a verification instruction
for the device to be verified to the channel management unit, configuration information of the
device to be verified is obtained according to the received verification instruction by using the
10 channel management unit, and the configuration information is returned to the verification unit.
Then the verification unit verifies the received configuration information according to stored
verification information. After verification succeeds, the electronic device is controlled to start.
The configuration information of the device to be verified is sent by using the channel management
unit to the verification unit for verification, so as to implement security verification on a device to
15 be verified disposed inside the electronic device and/or a device to be verified externally connected
to the electronic device in a start process of the electronic device, thereby ensuring information
security.

FIG. 1

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