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(54) Title: BLOCK FAMILY-BASED ERROR AVOIDANCE FOR MEMORY DEVICES

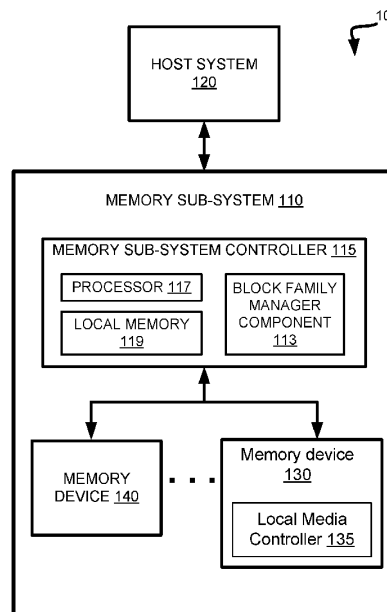


FIG. 1

(57) Abstract: An example memory sub-system includes a memory device and a processing device, operatively coupled to the memory device. The processing device is configured to initialize a block family associated with a memory device; initialize a timeout associated with the block family; initializing a low temperature and a high temperature using a reference temperature at the memory device; responsive to programming a block residing on the memory device, associate the block with the block family; and responsive to at least one of: detecting expiration of the timeout or determining that a difference between the high temperature and the low temperature is greater than or equal to a specified threshold temperature value, close the block family.

[Continued on next page]

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- *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))*

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