

READ-WRITE CONVERSION CIRCUIT AND MEMORY

ABSTRACT

Some of embodiments of the present application relate to the technical field of semiconductors, and disclosed are a read-write conversion circuit and a memory. The read-write conversion circuit comprises: a read-write conversion module (101), responding to a read-write control signal to perform a read-write operation, such that data transmission is performed between a local data line and a global data line as well as between a local complementary data line and the global data line, and during the read-write operation, data signal phases of the local data line and the local complementary data line are opposite; and a control module (102), responding to a read-write speed configuration signal to output a variable read-write control signal so as to control the speed of the read-write operation of the read-write conversion module (101) to be variable.

[Fig. 2]