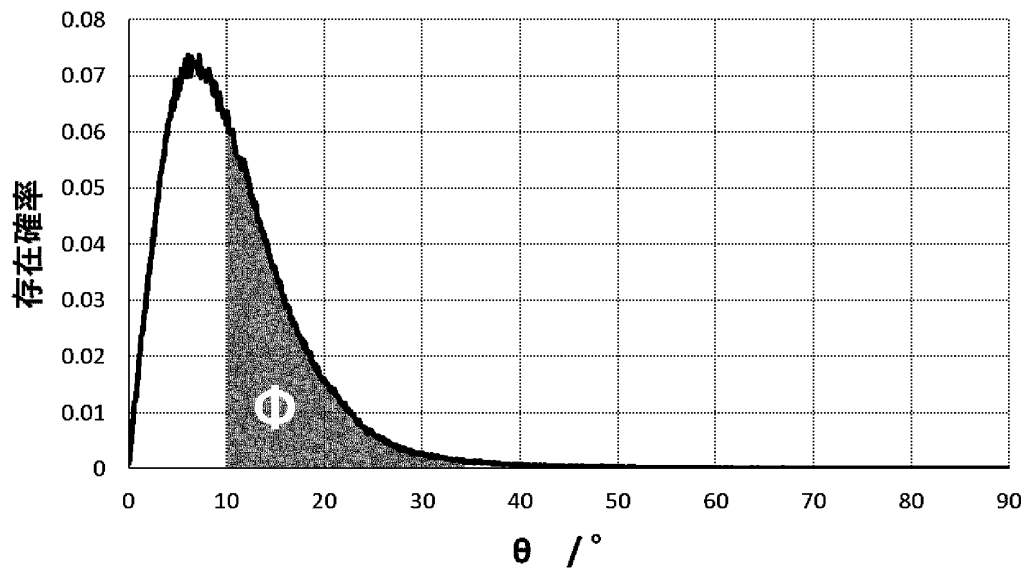
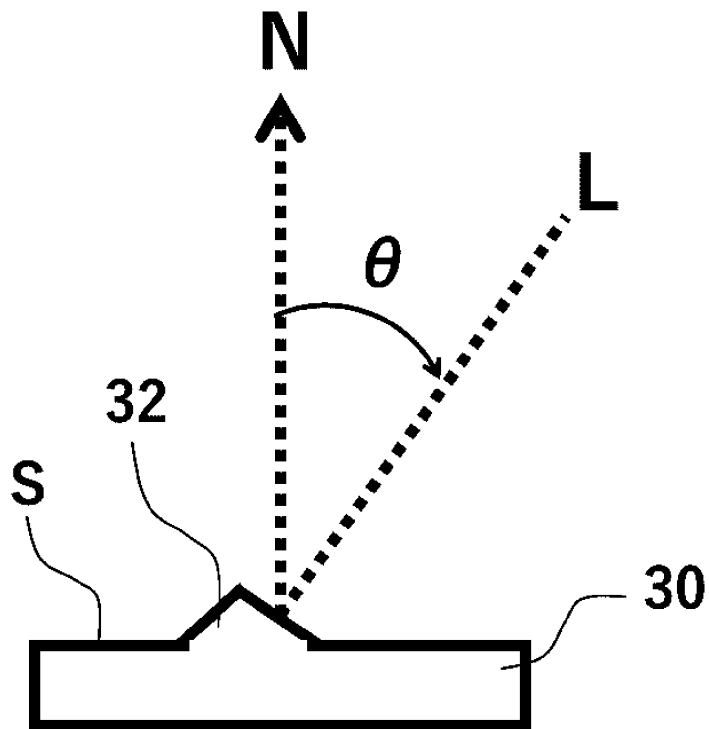


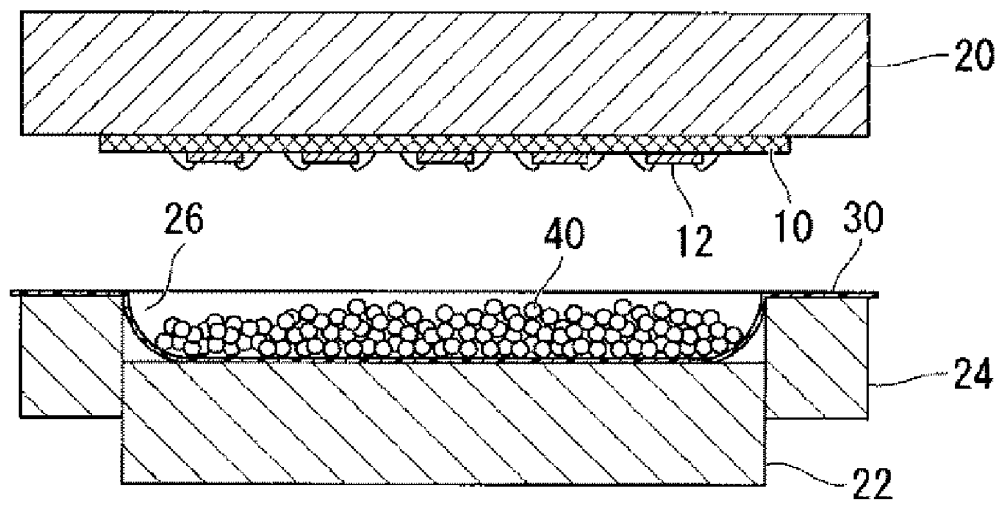
[図1]



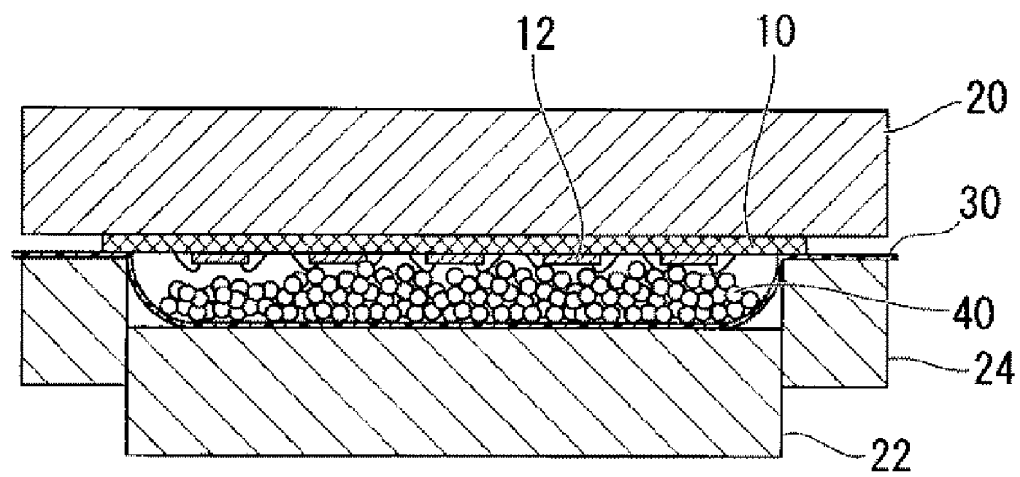
[図2]



[図3]



[図4]



[図5]

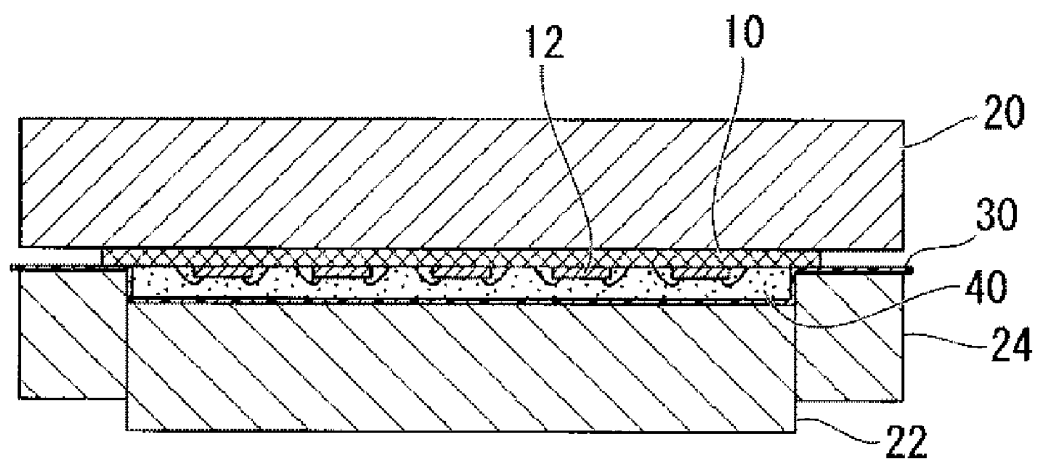
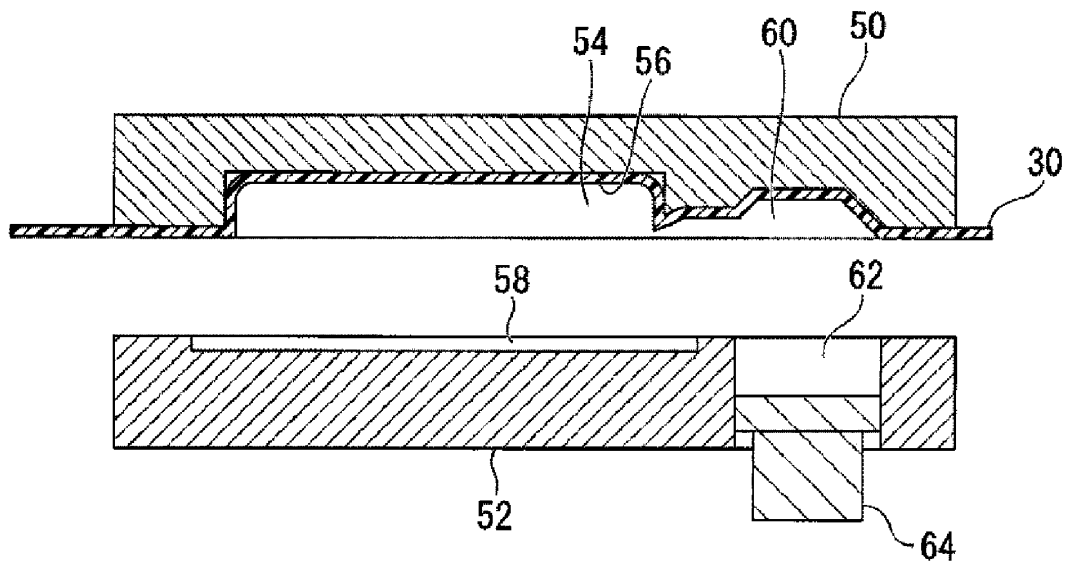
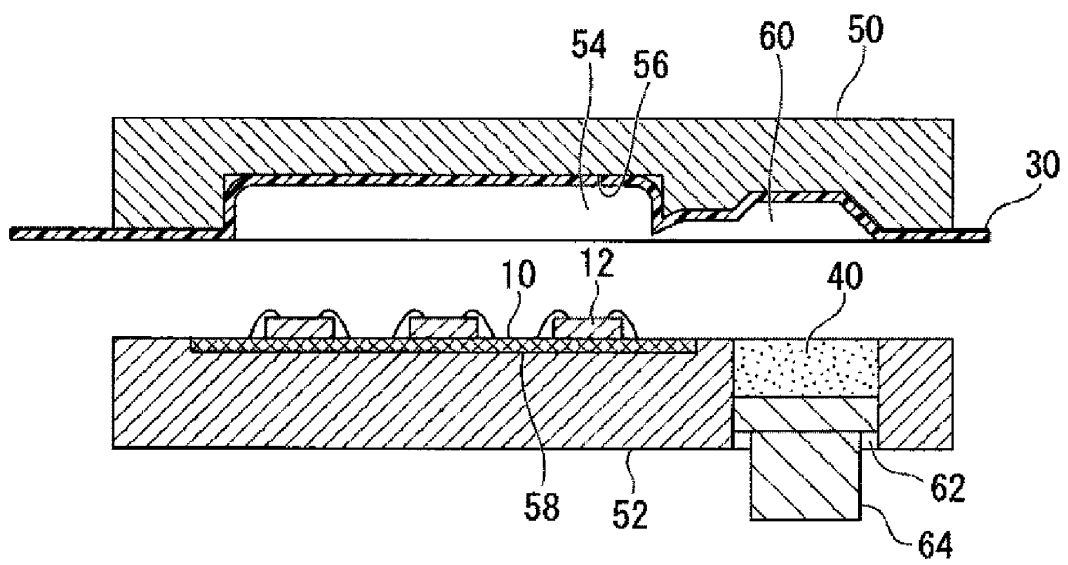


Figure 1 shows two cross-sectional views of a semiconductor device. The top view shows a substrate 50 with a layer 54 on top. A layer 56 is formed on top of layer 54, and a layer 60 is formed on top of layer 56. The bottom view shows a substrate 52 with a layer 58 on top. A layer 62 is formed on top of layer 58, and a layer 64 is formed on top of layer 62.

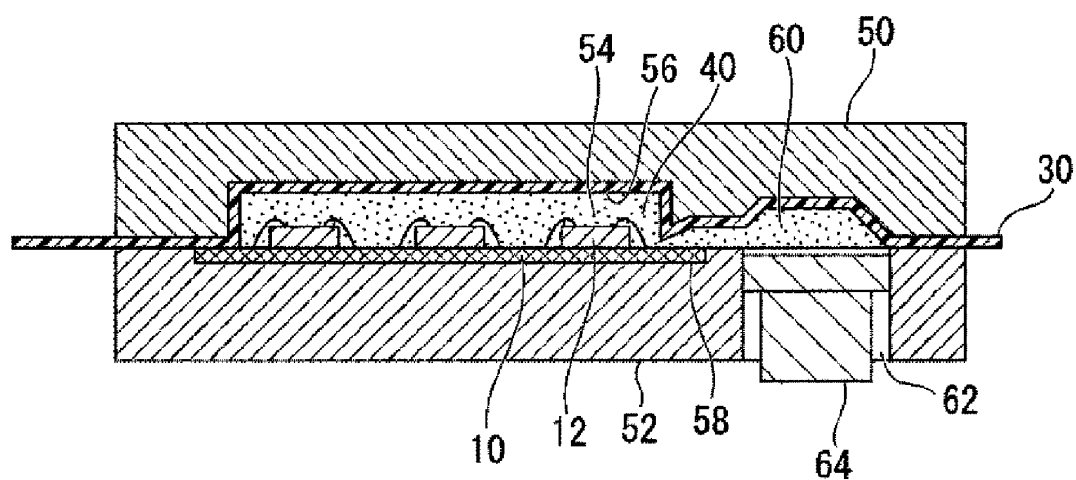
[図8]



[図9]



[図10]



[図11]

