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NL-5656 AA Eindhoven(NL)**54 **Amplifier arrangement.**

57 A driver stage (S) comprising a differential pair (T3, T4) is coupled to an output stage (U) comprising a first output transistor (T1) and a second output transistor (T2), which driver stage is capable of providing class AB operation of said output transistors (T1, T2) at a comparatively low supply voltage. In order to obtain such operation a translinear network (T1, T3, T4, T5) comprising the differential pair (T3, T4) is coupled directly to a current mirror (T2, T6), which network and which current mirror comprise, and dictate the setting of the first (T1) and the second (T2) output transistor respectively.

