

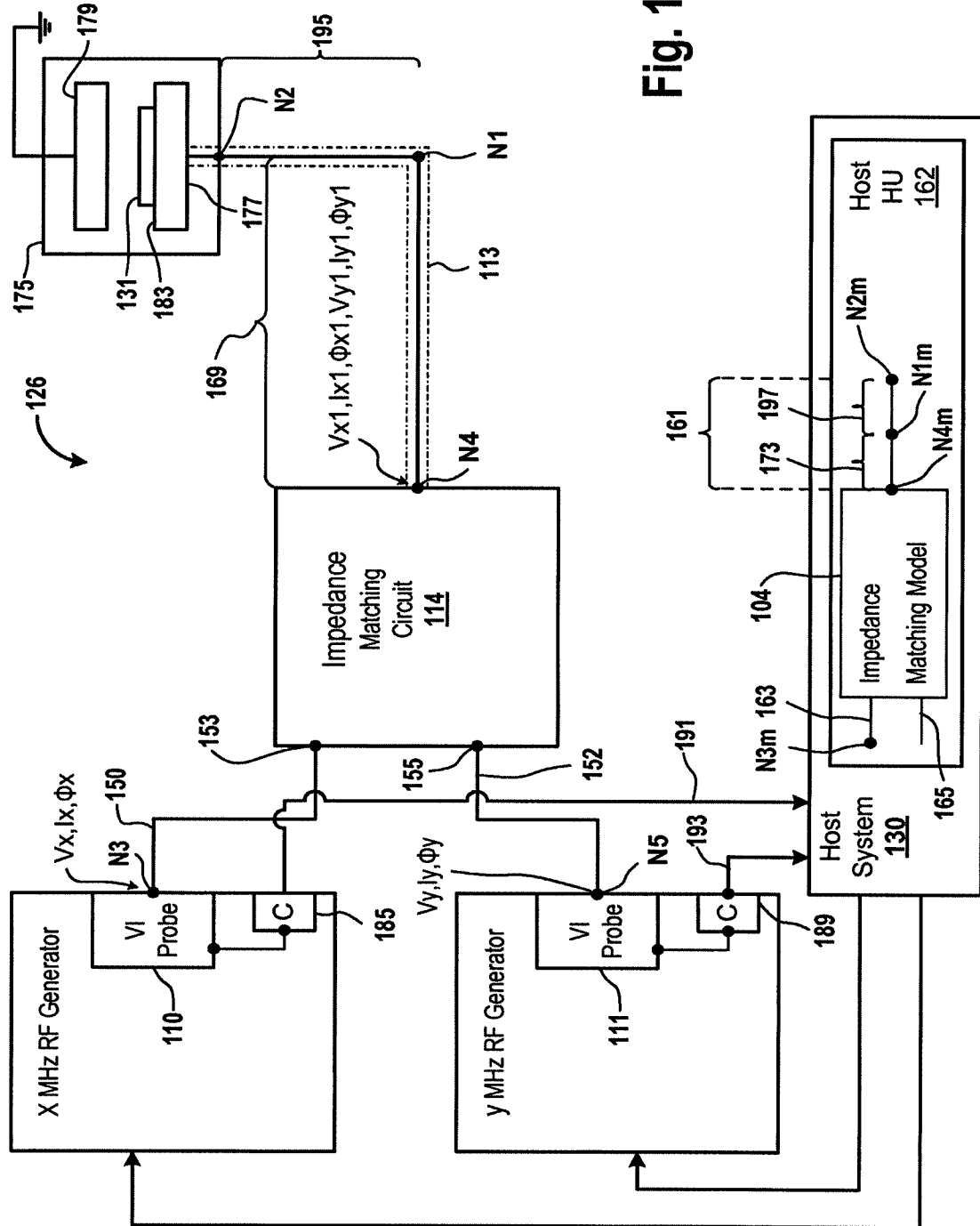
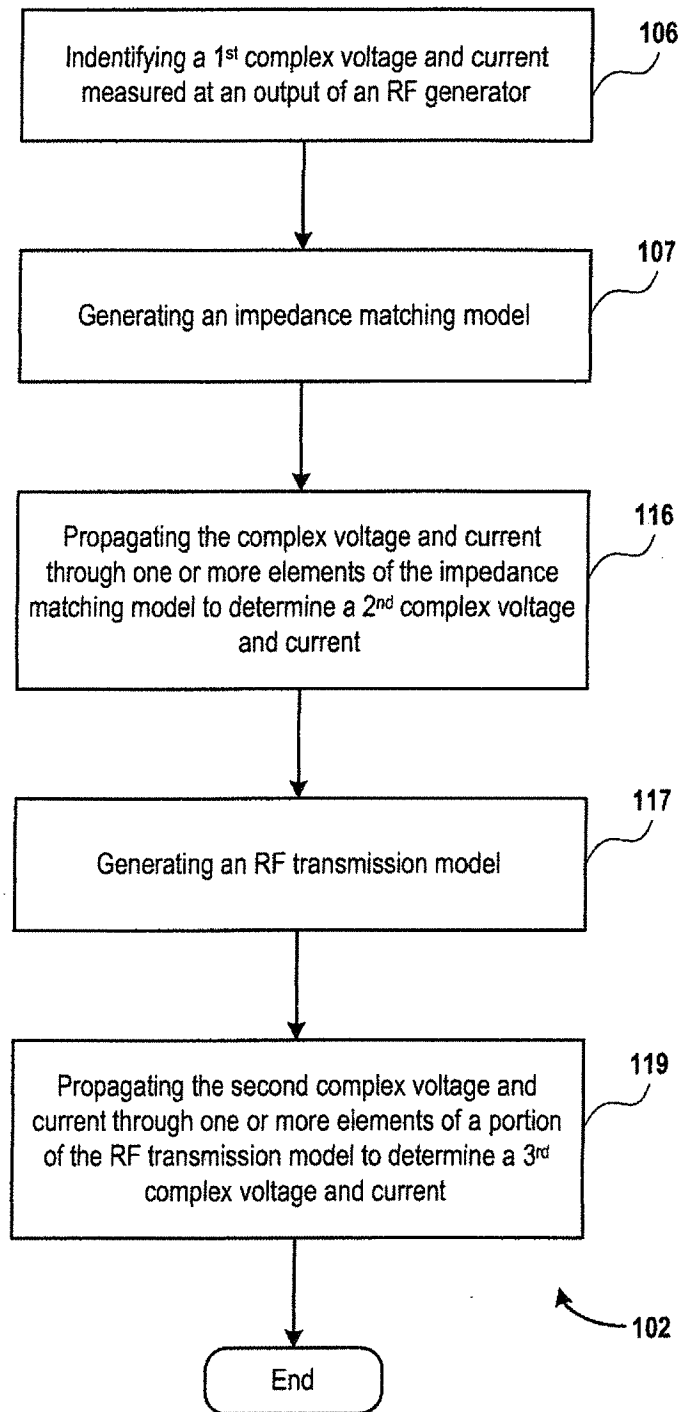


Fig. 1

**Fig. 2**

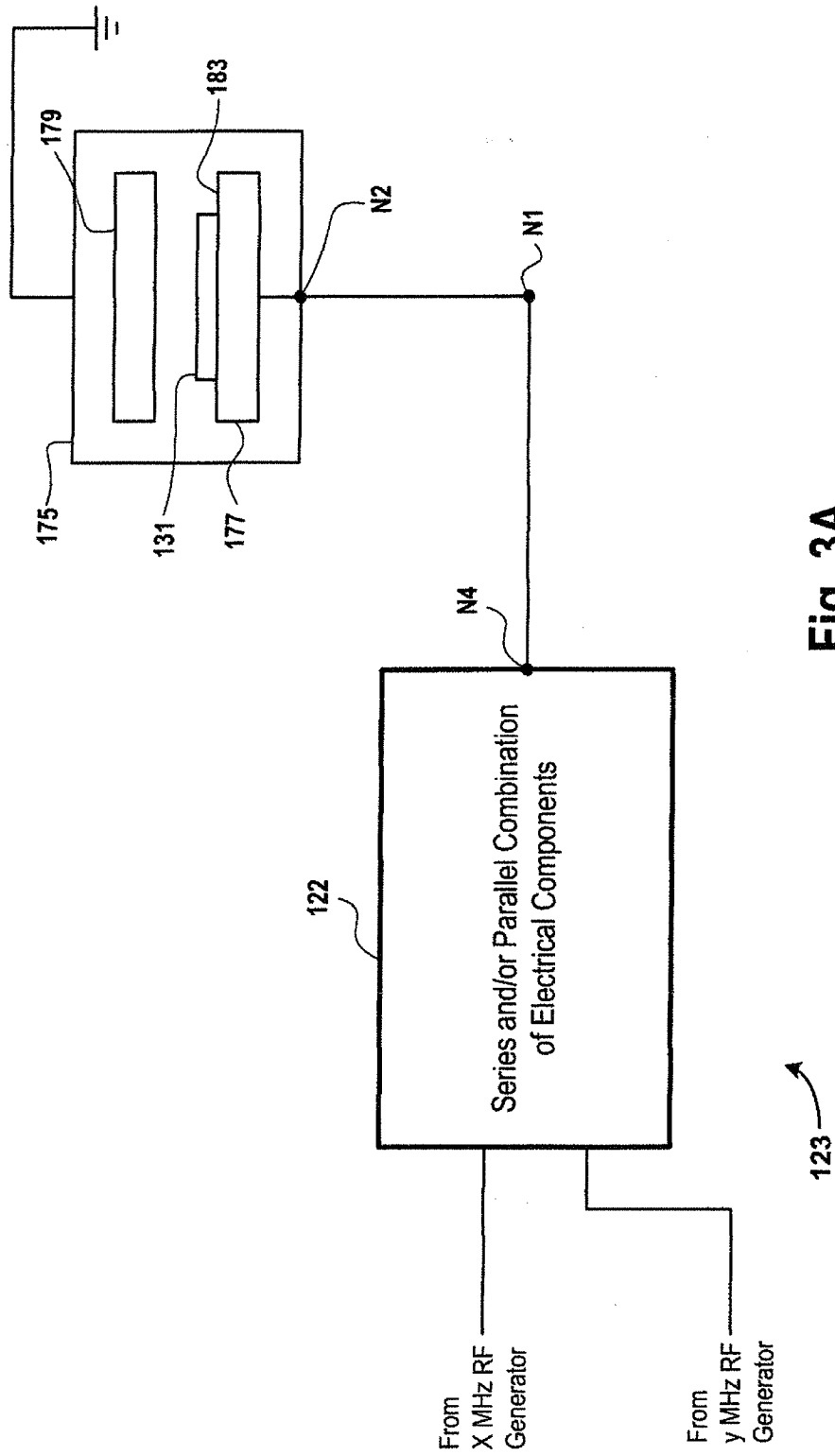


Fig. 3A

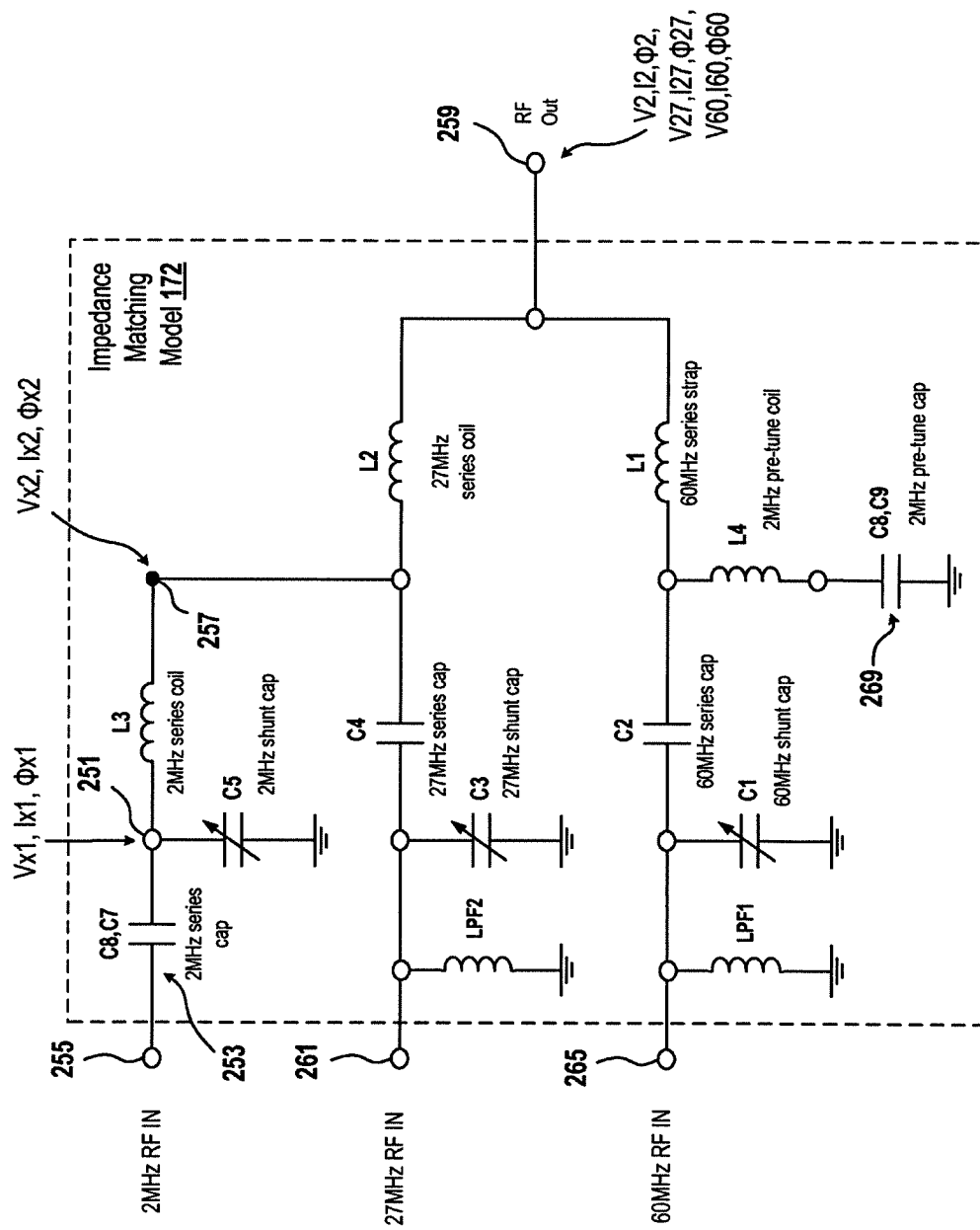


Fig. 3B

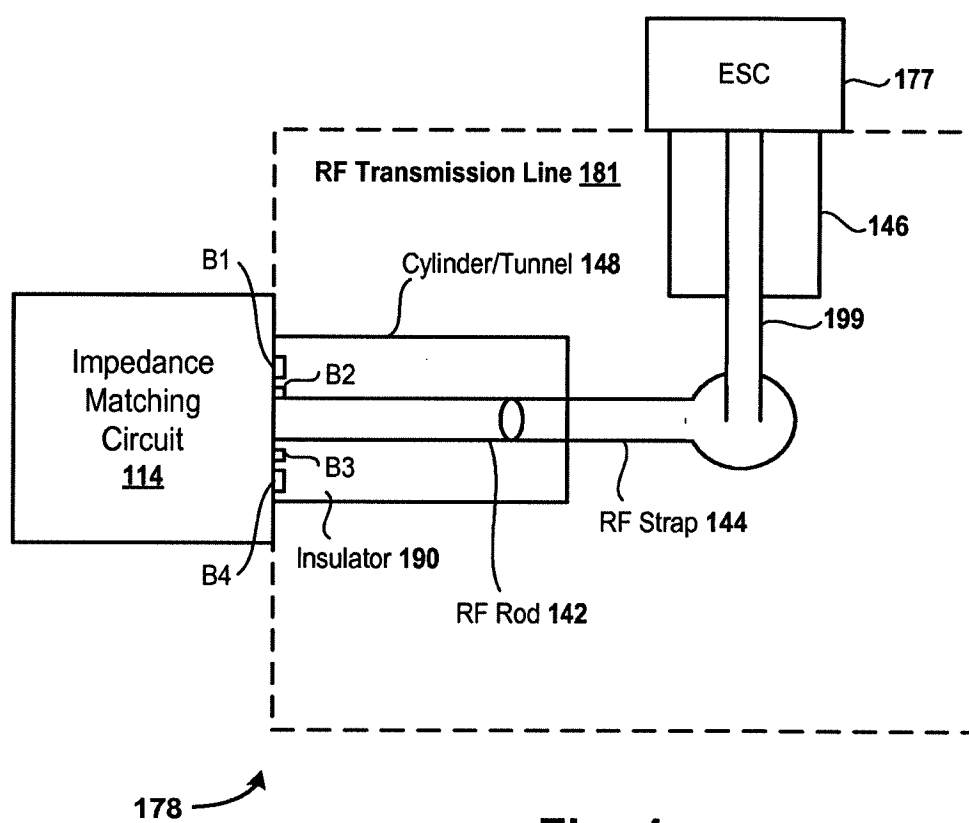
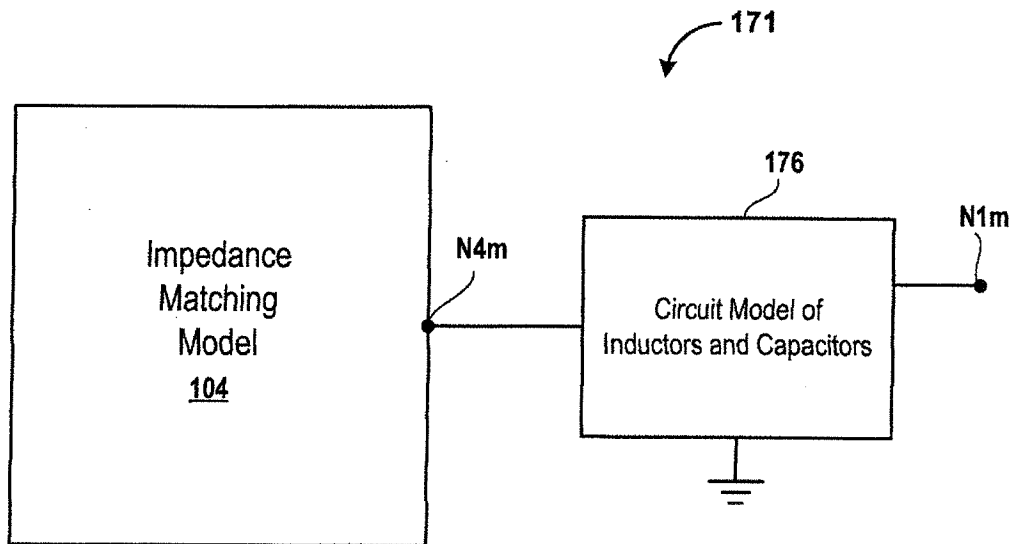


Fig. 4

**Fig. 5A**

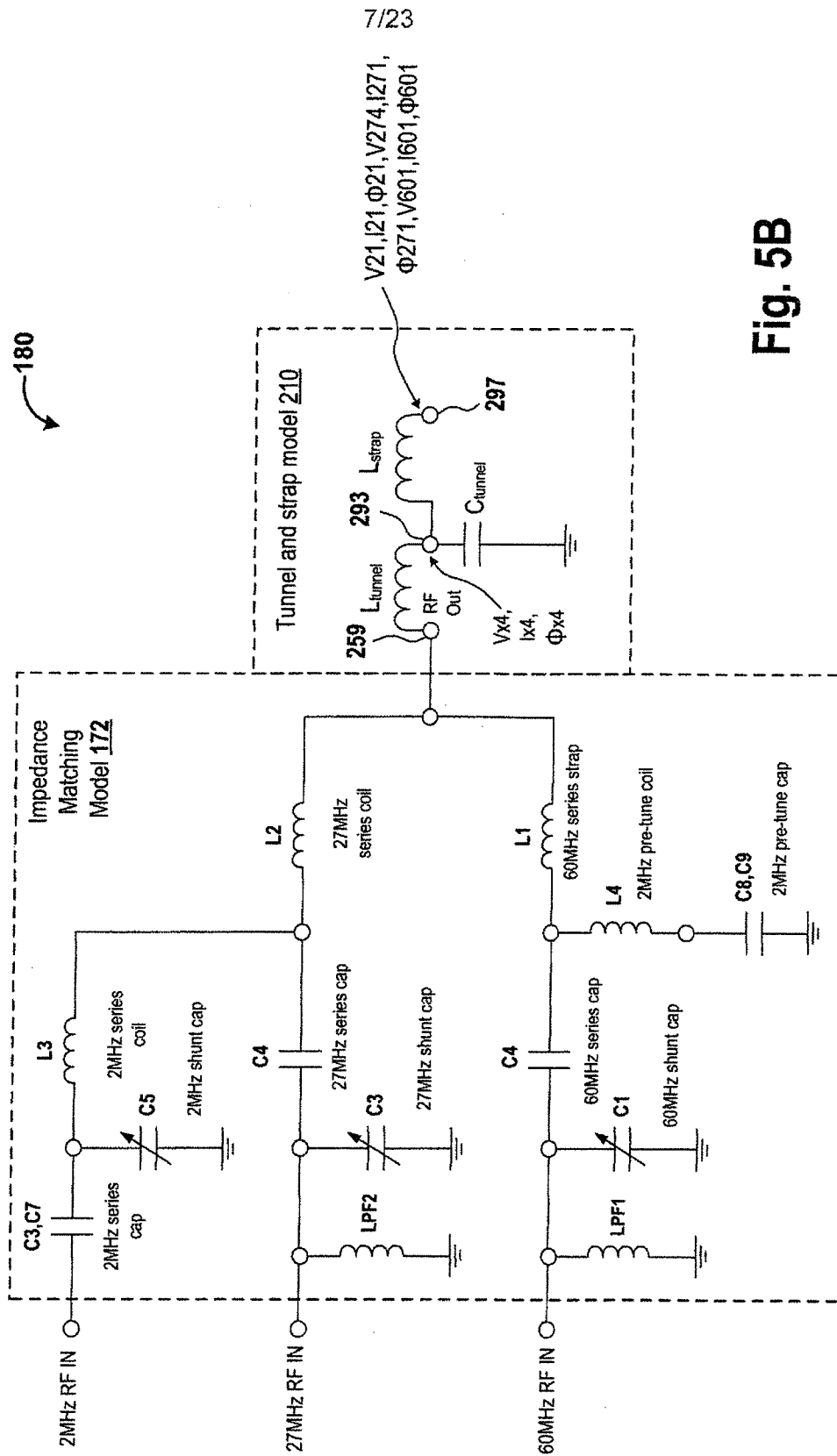


Fig. 5B

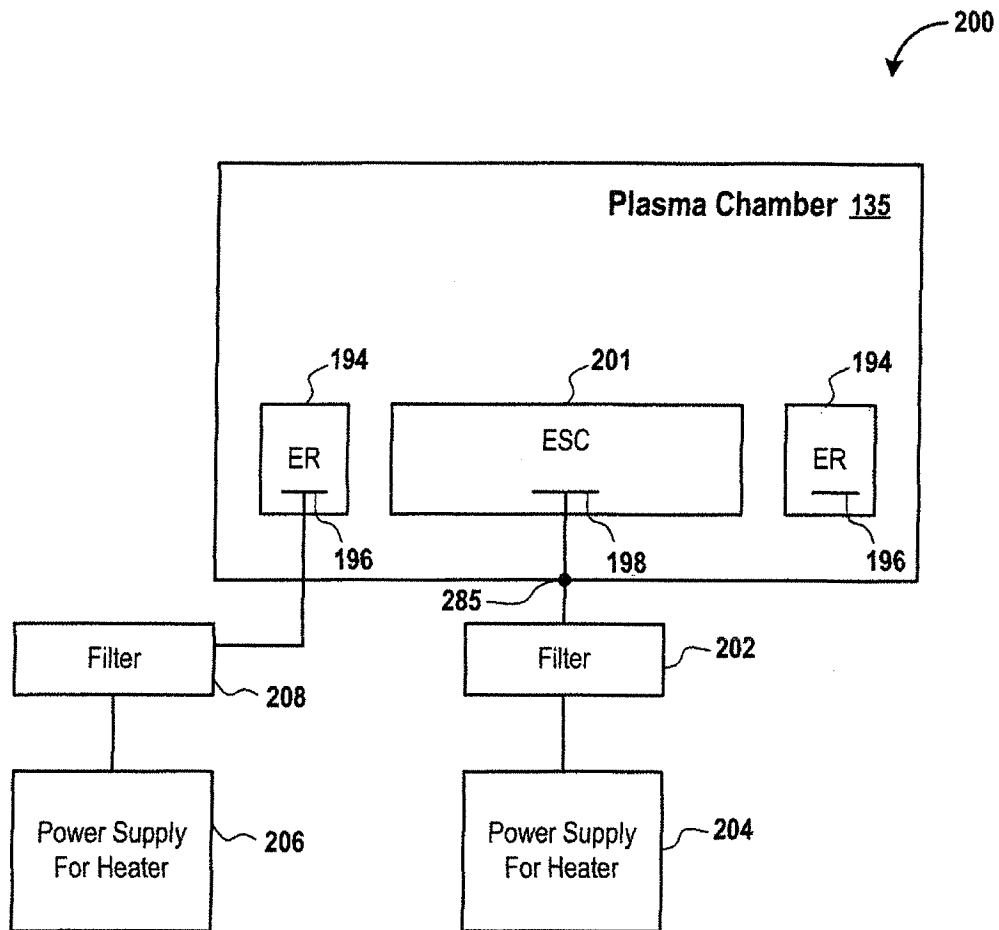
**Fig. 6**

Fig. 7A

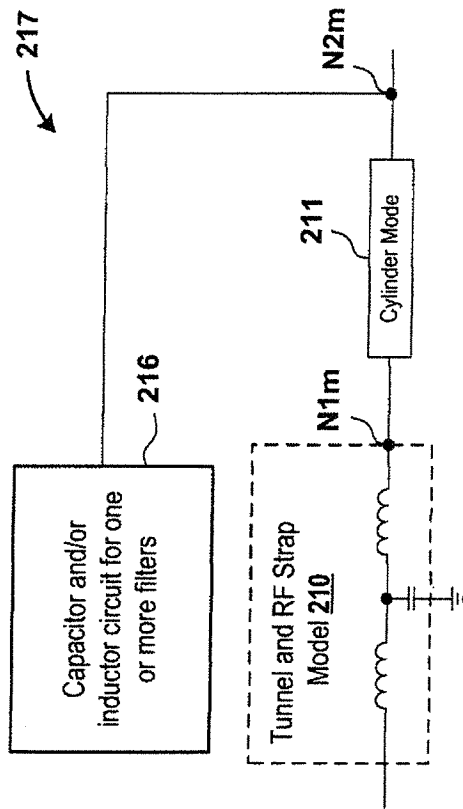
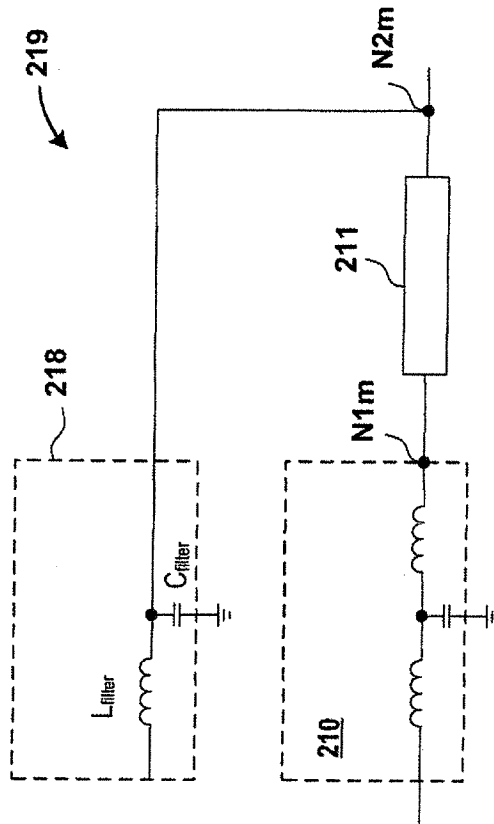


Fig. 7B



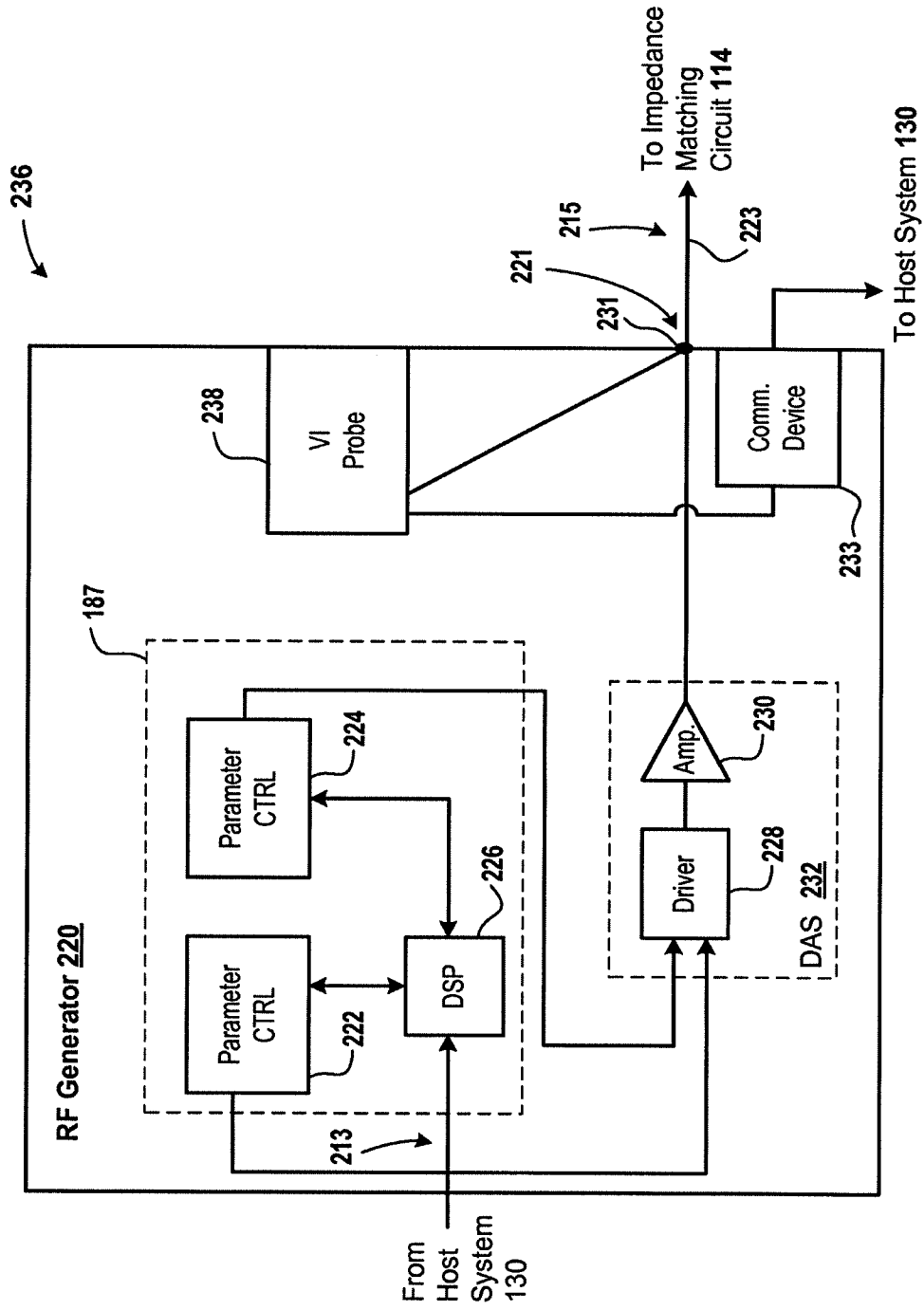


Fig. 8

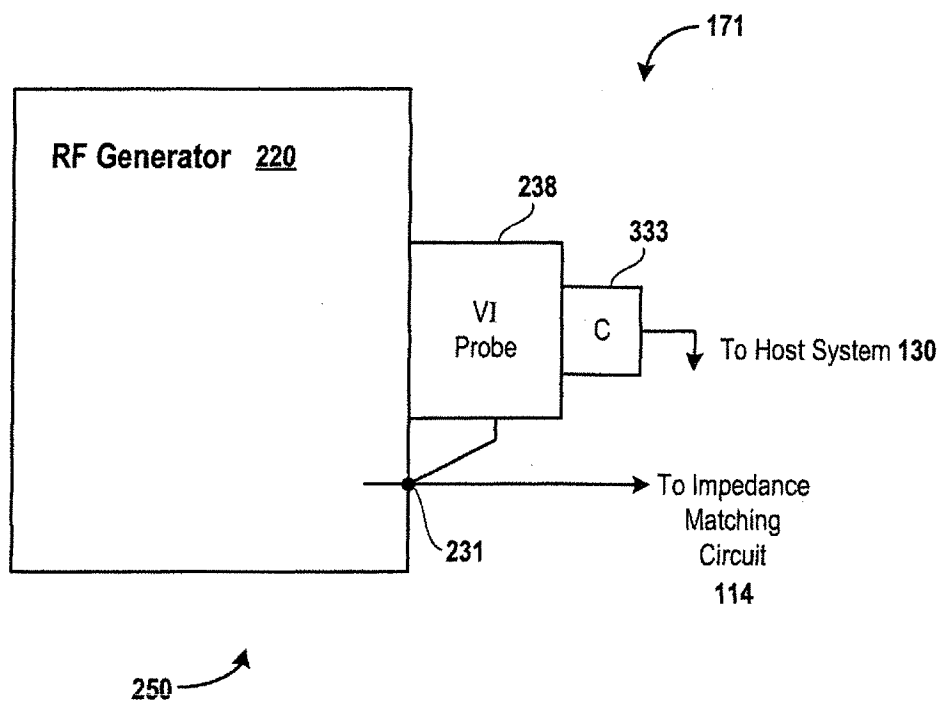
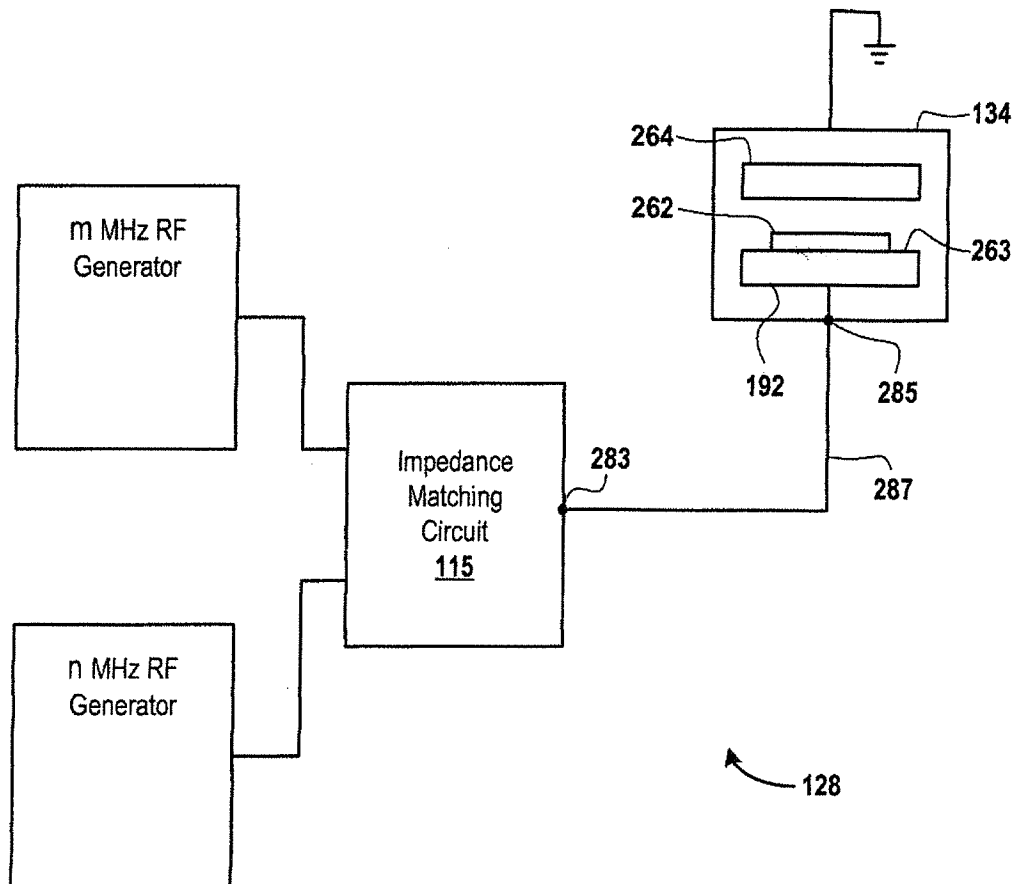


Fig. 9

**Fig. 10**

Compare Model Vpeak vs Vprobe for ALL 2MHz Combinations Pwr/Pres/Gap/Match/Filter Scans

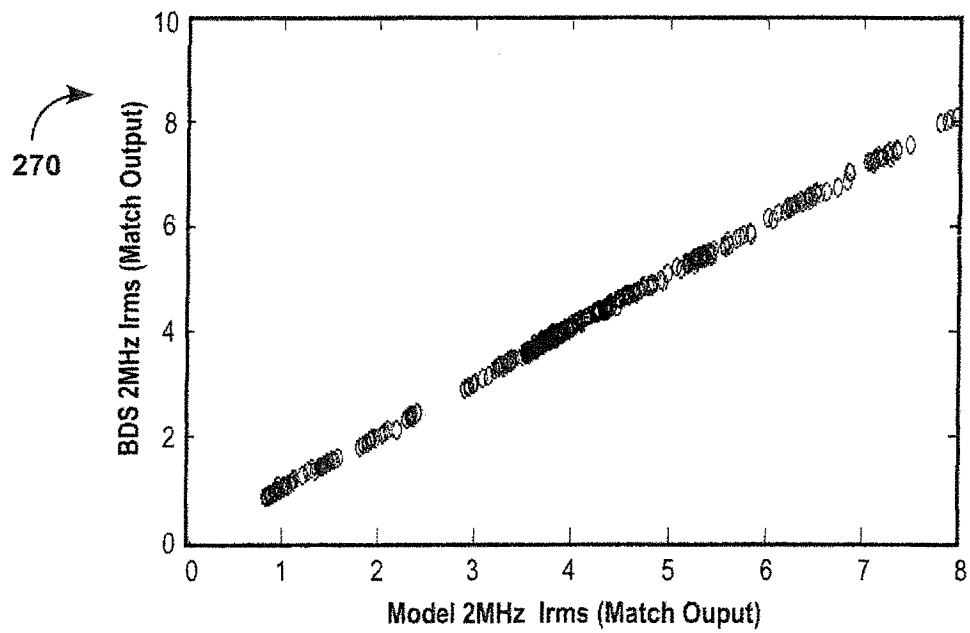
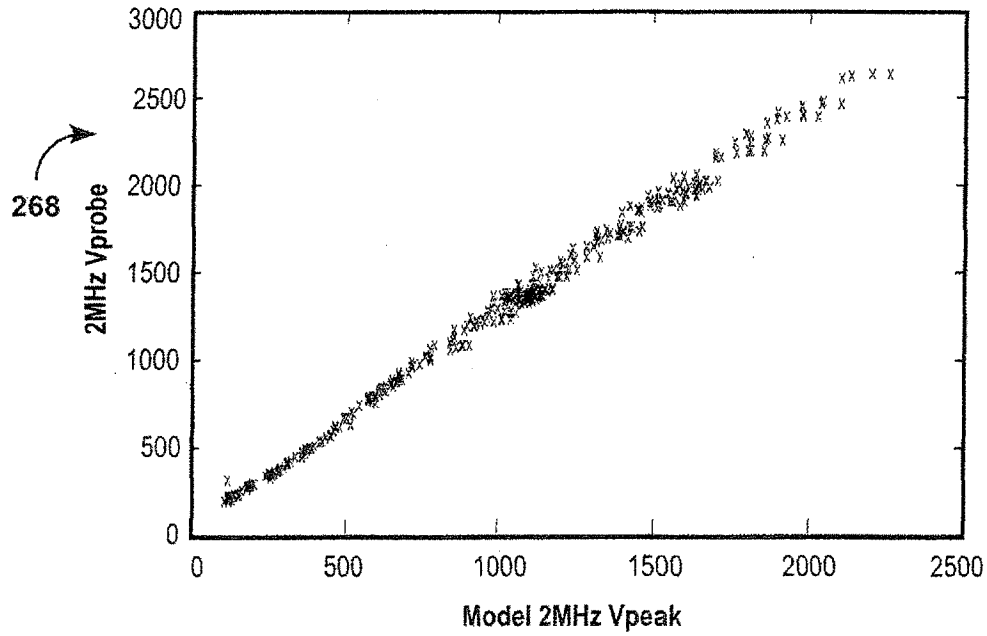


Fig. 11A

Compare Model Vpeak vs Vprobe for ALL 27MHz Combinations Pwr/Pres/Gap/Match/Filter Scans

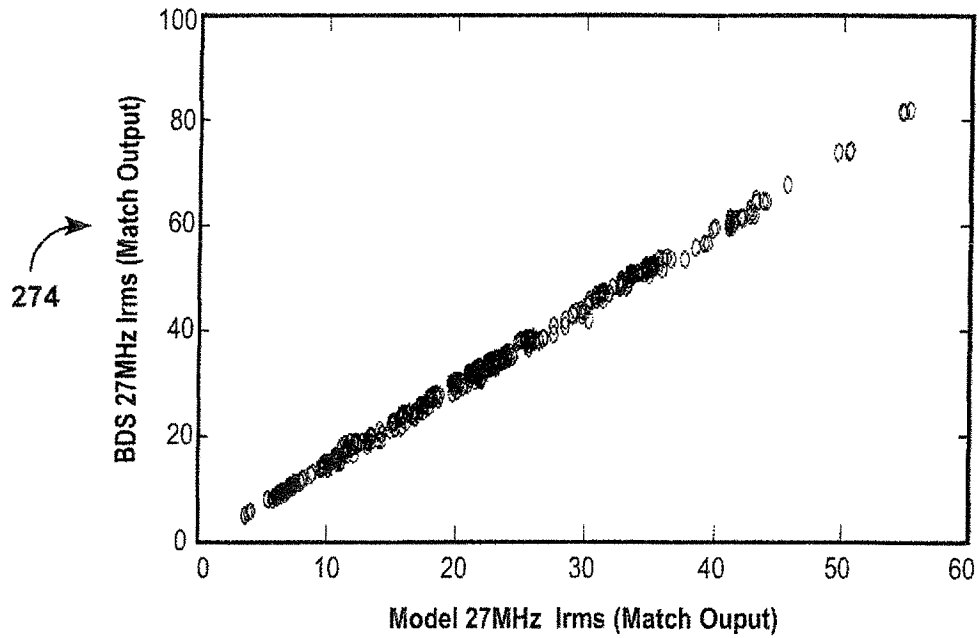
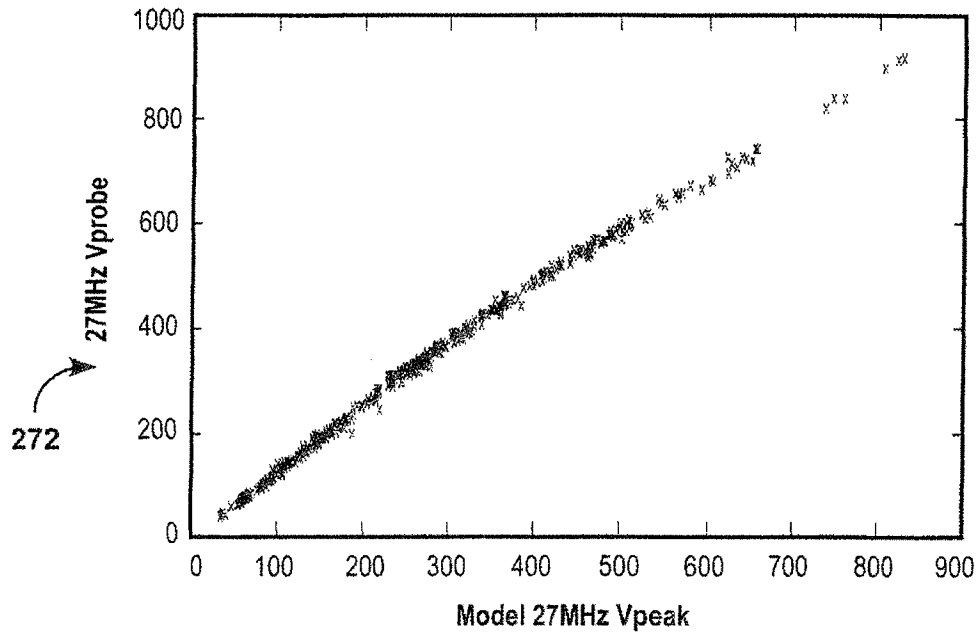


Fig. 11B

Compare Model Vpeak vs Vprobe for ALL 60MHz Combinations Pwr/Pres/Gap/Match/Filter Scans

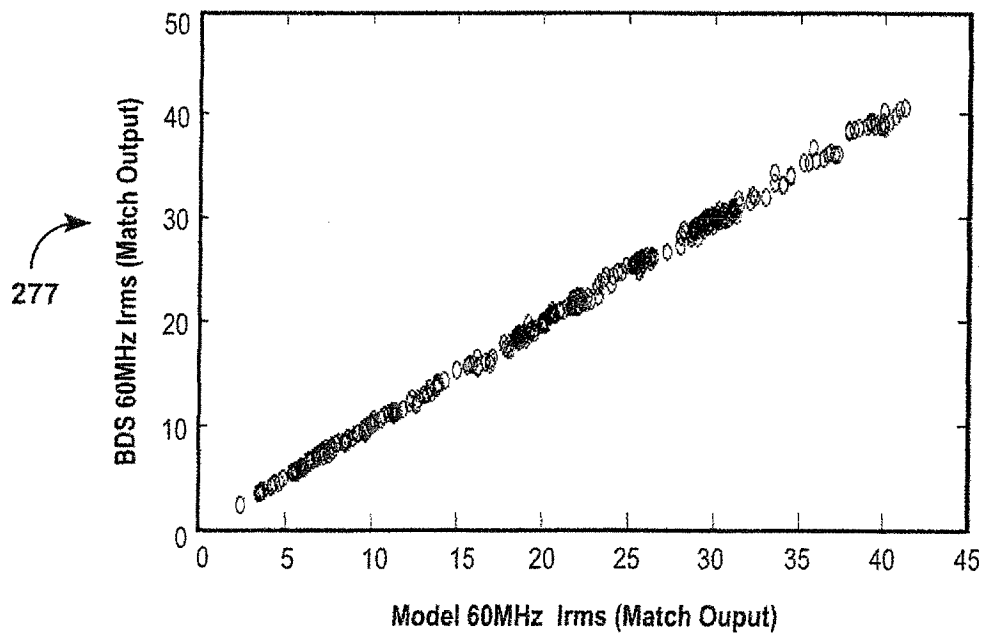
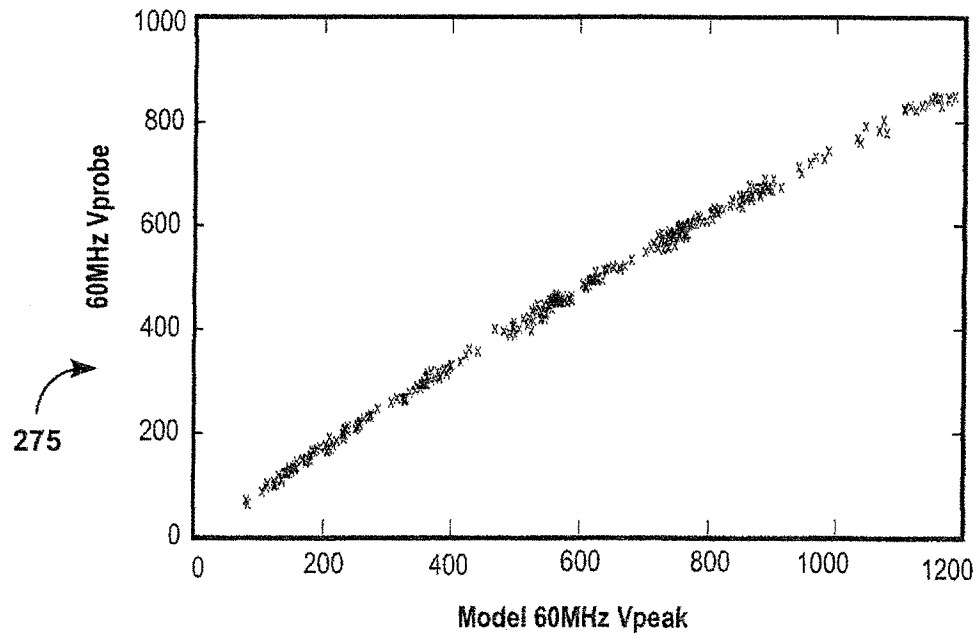


Fig. 11C

Compare 2MHz Only Bias Models vs WW Vdc for 2MHz ONLY Pwr/Pres/Gap/Tap/Match/Filter Scans

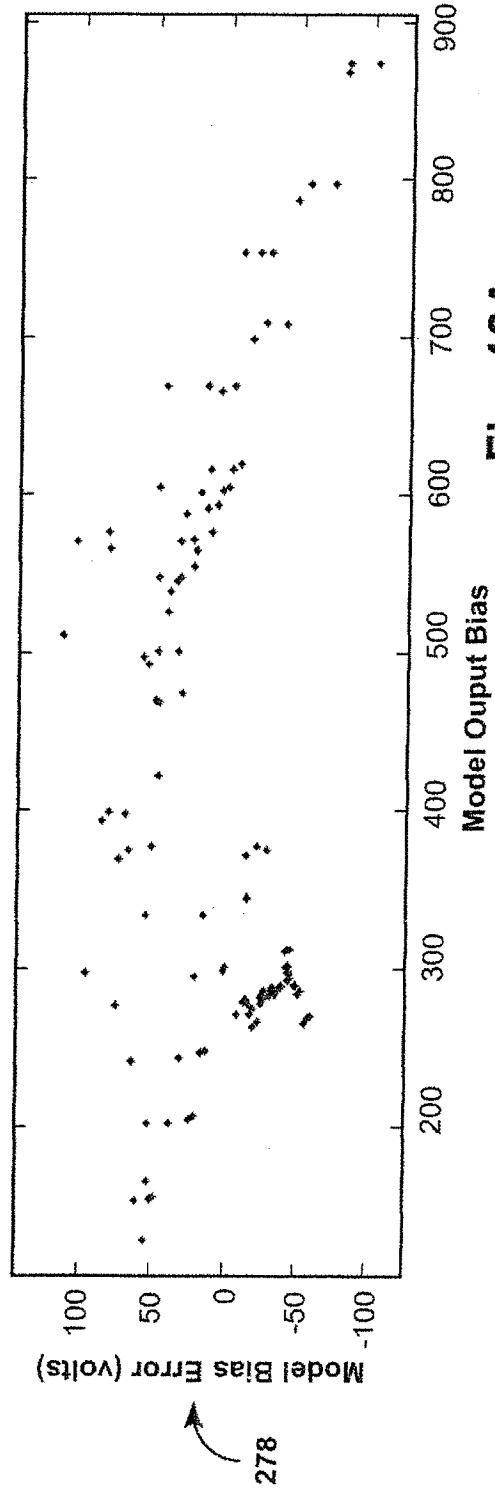
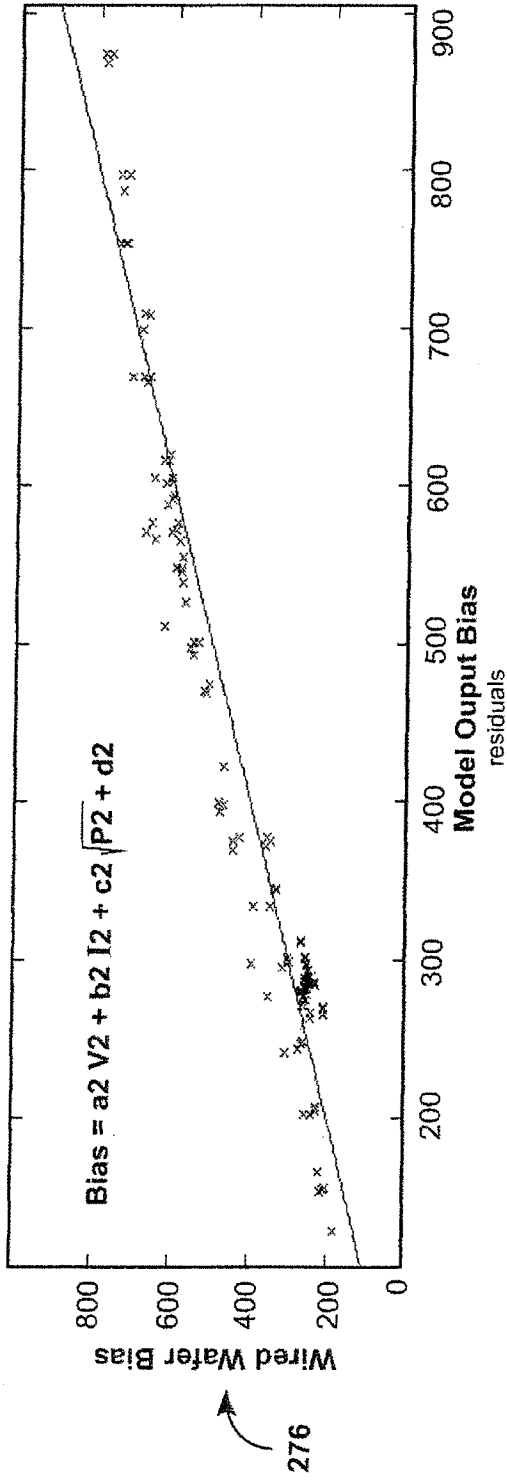


Fig. 12A

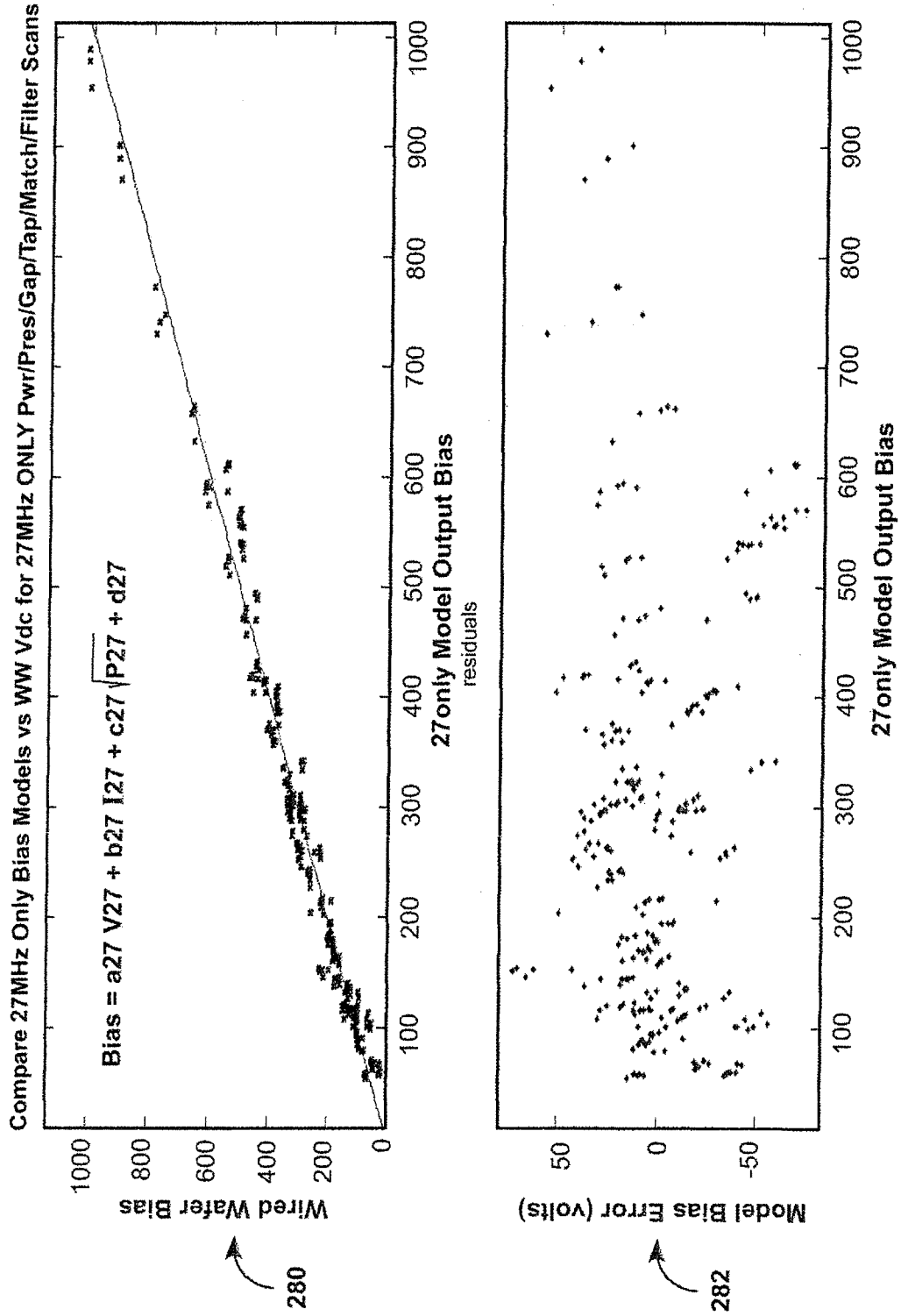


Fig. 12B

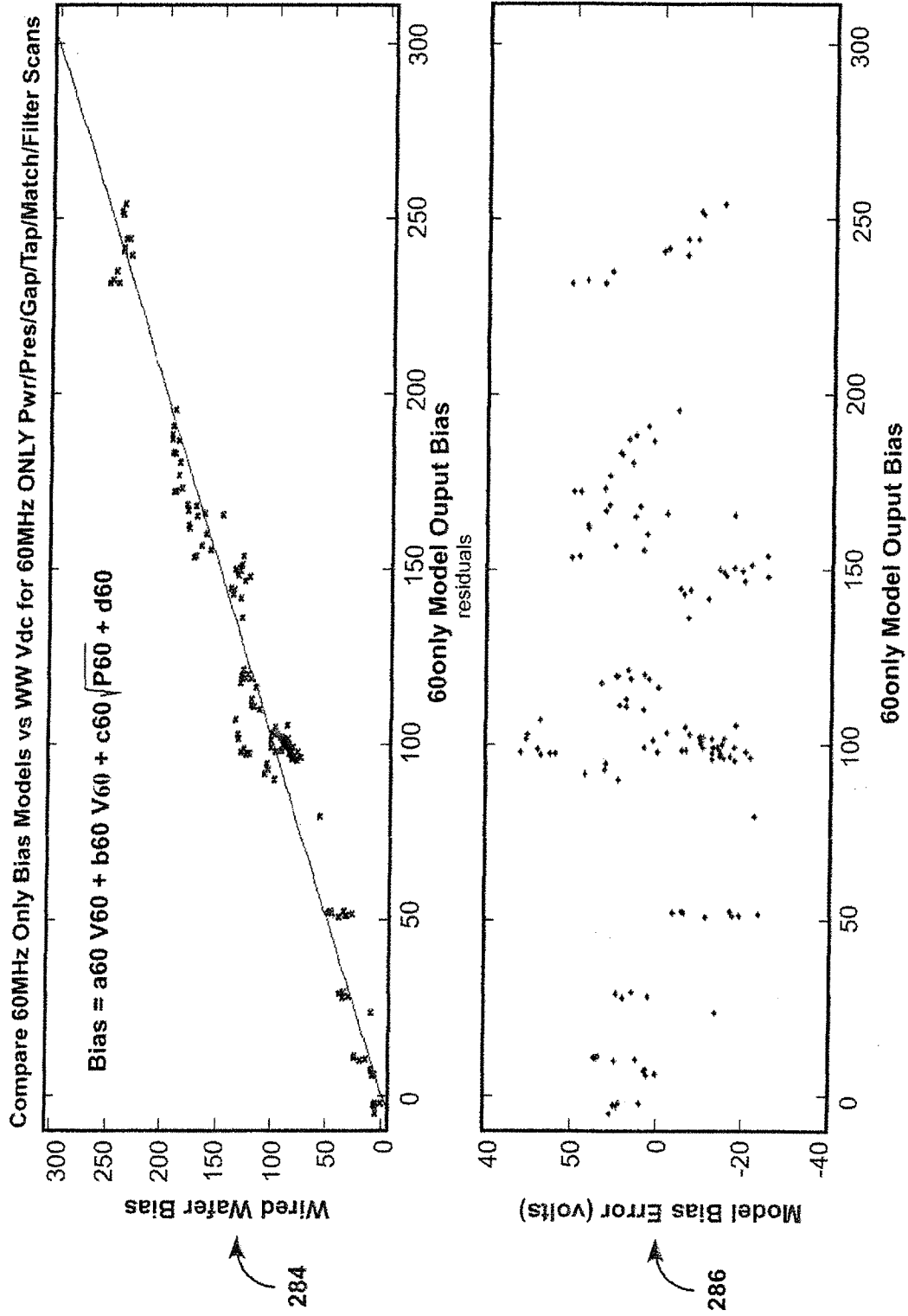
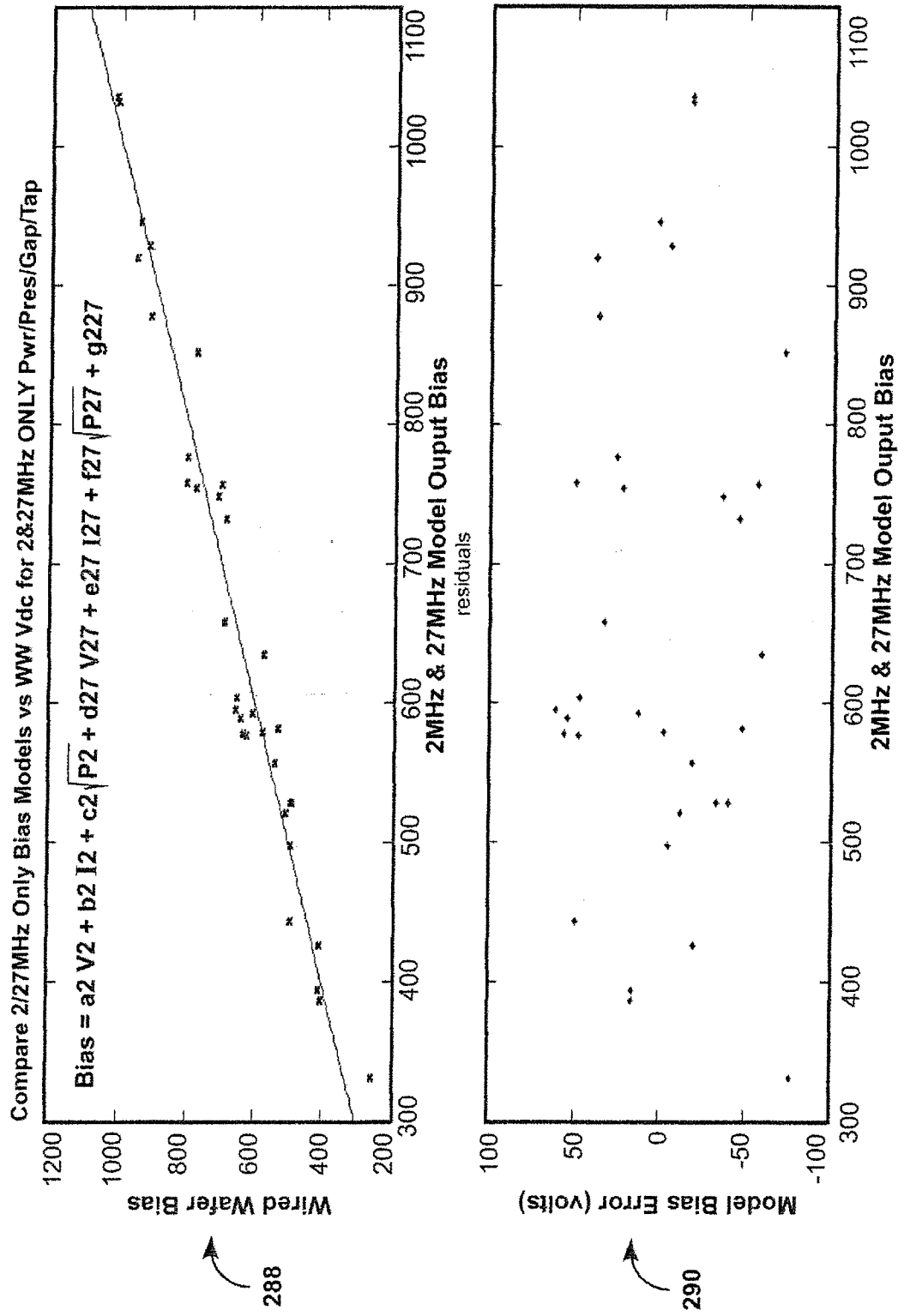


Fig. 12C



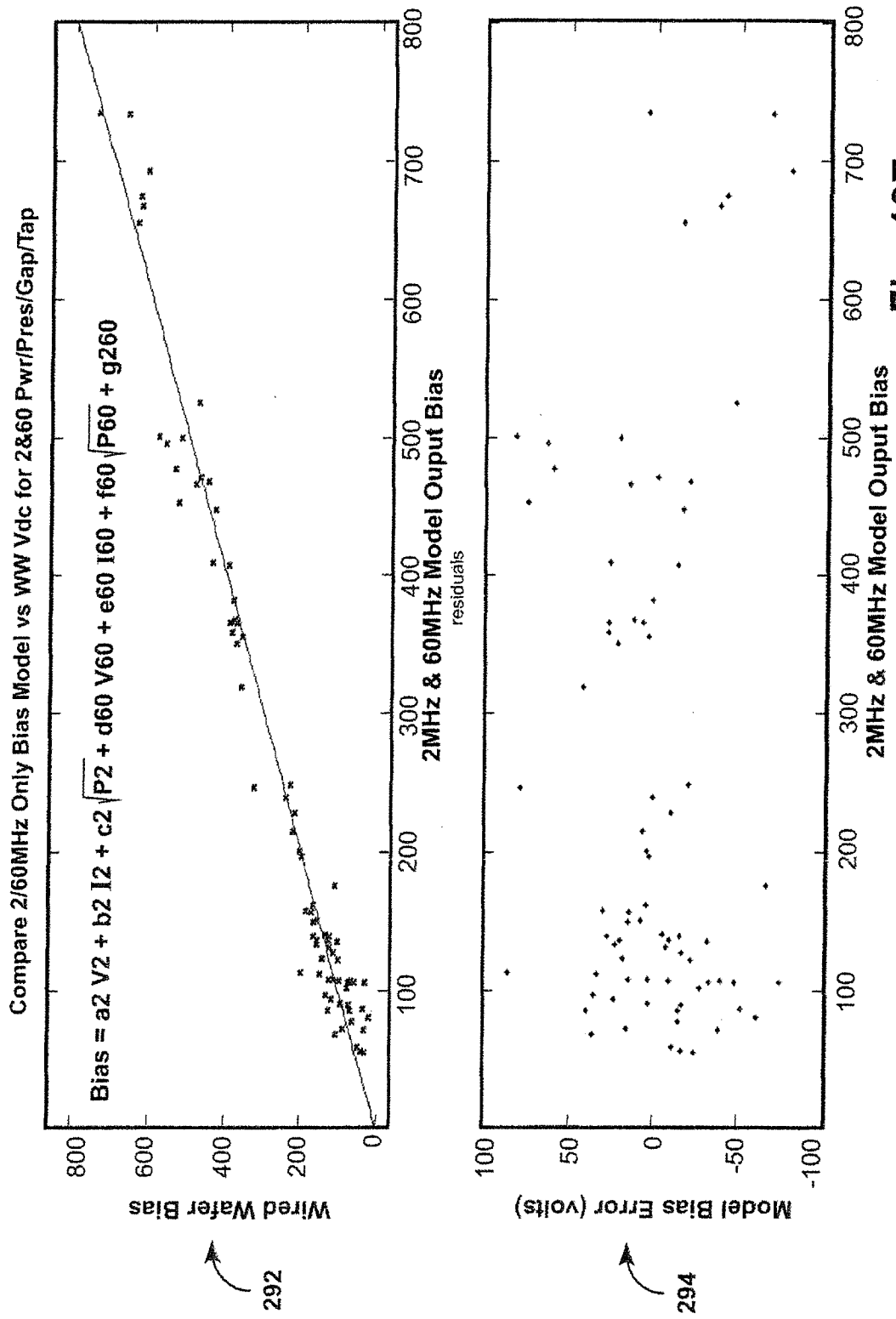
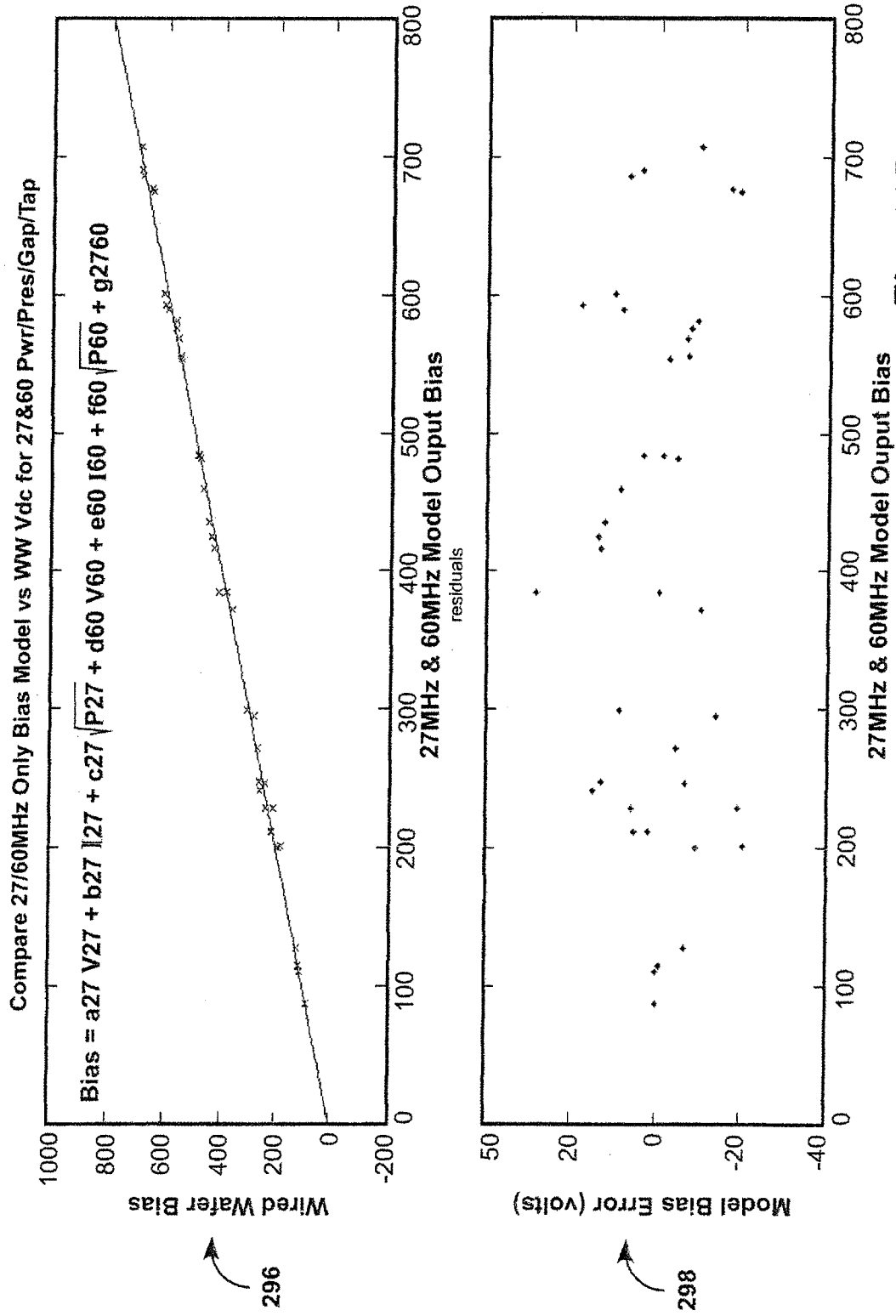
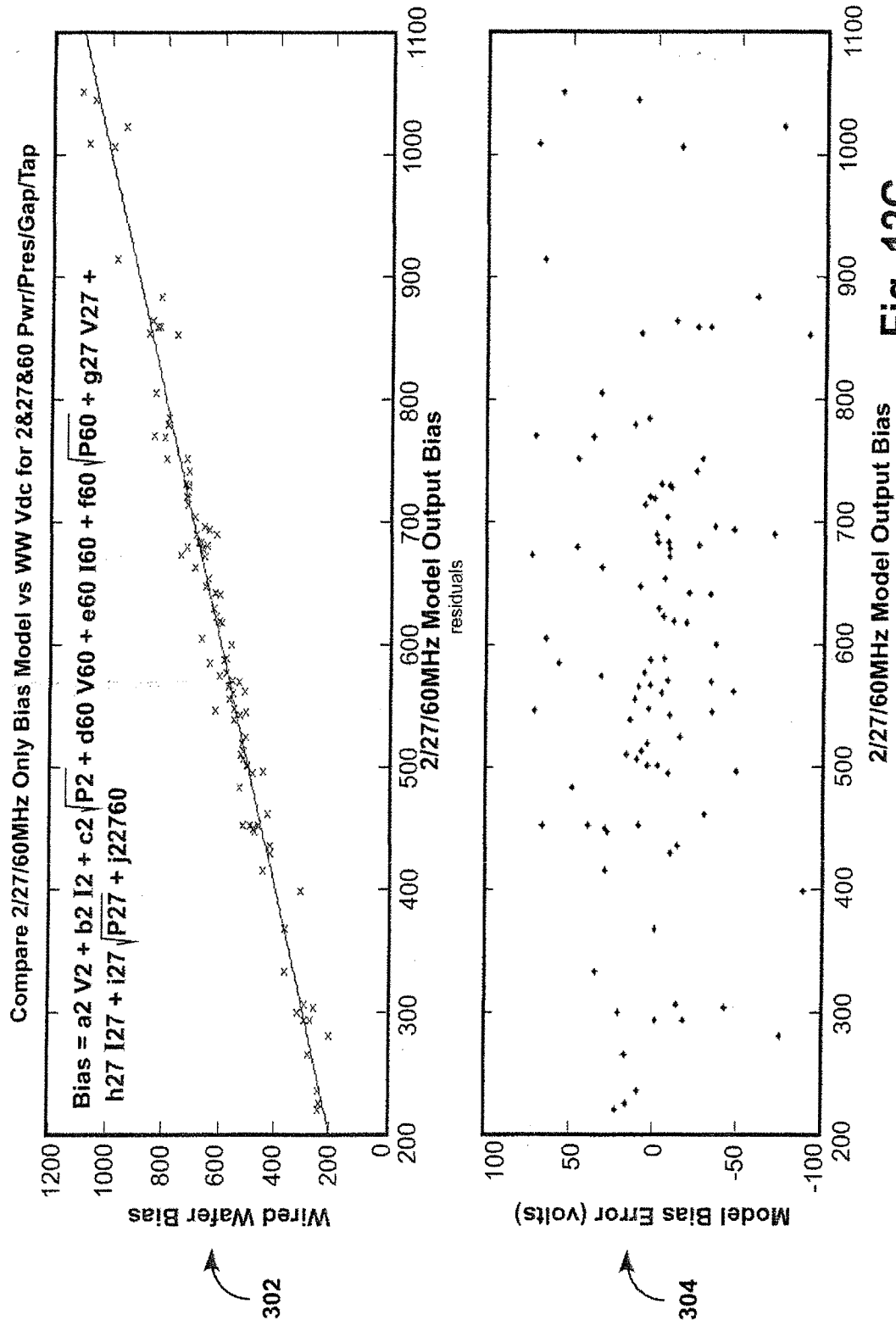


Fig. 12E





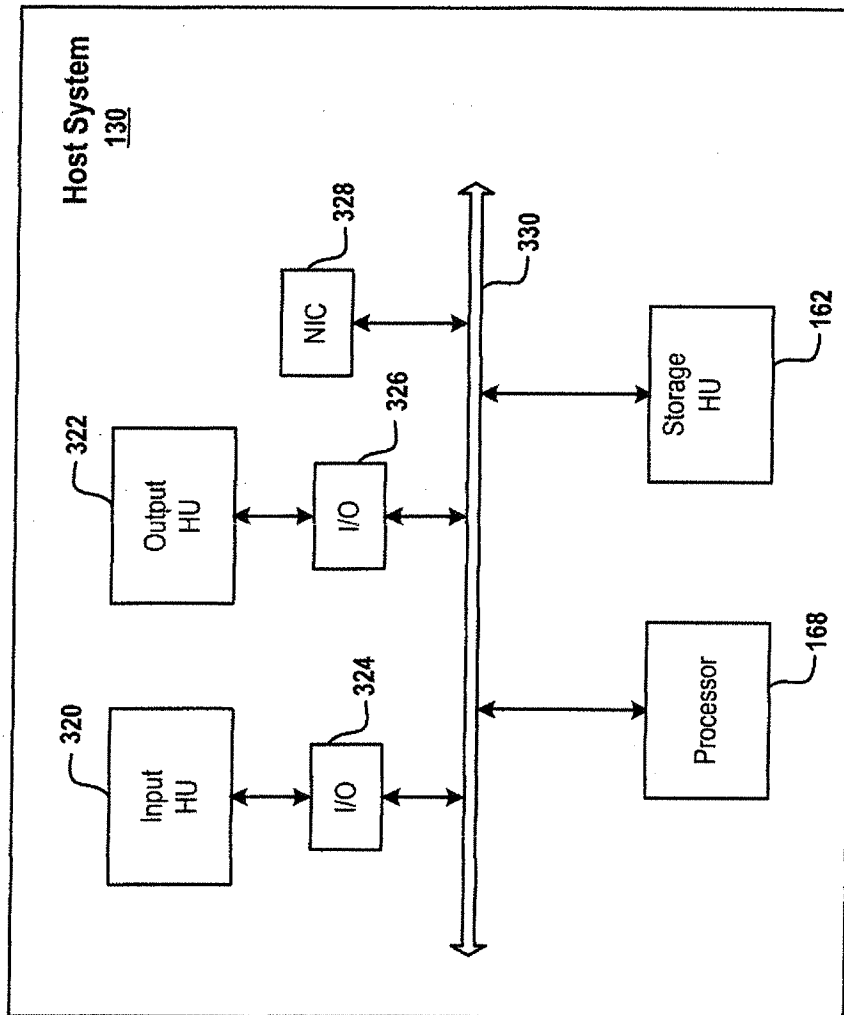


Fig. 13