

ABSTRACT

A memory device for performing a data write operation based on a multiple write command, an operating method thereof, and an operating method of a memory controller are provided. An operating method of a memory device including a plurality of banks includes receiving a write command, and data and an address corresponding to the write command, decoding the received write command, and responsive to a result of the decoding indicating that the write command corresponds to a multiple write command, together writing the same data in two or more banks using an internal address generating operation that is based on the received address.

FIG. 1