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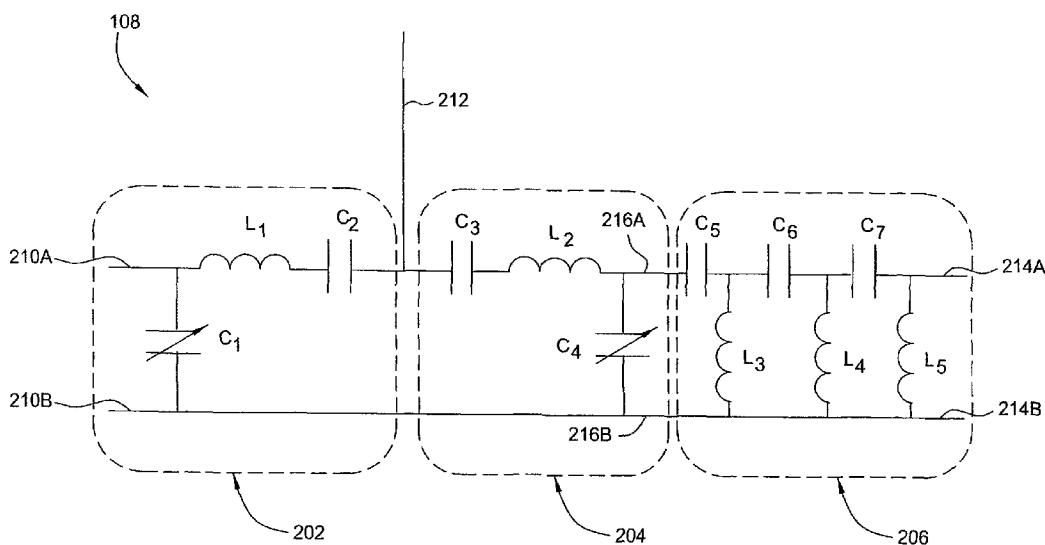
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(54) Title: DUAL FREQUENCY RF MATCH



(57) Abstract: A dual frequency matching circuit (108) for plasma enhanced semiconductor processing chambers having dual frequency cathodes is provided. The matching circuit includes two matching circuits (202-204) with variable shunts (C1, C4) combined to a common output (212). The matching circuit balances the load of the independent RF sources to that of the plasma in the processing chamber during operation.

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