# A DATA INTERFACE AND METHOD THEREFOR

## Abstract of the Invention

A receive bit counter (107) and a store multiplexer (109) stores a received sequence of data packets alternately in a first memory (111) and a second memory (113) at a first data rate. A retrieve multiplexer (115) and a retrieve bit counter (117) selectively retrieve the stored data packets at a second data rate which is greater than the first data rate. The selection of which of the first or second memory (11 or 113) to retrieve each data packet is determined by the controller (125) using the outputs received from the store time calculator (120), the retrieve time calculator (130), store multiplexer (109), the retrieve multiplexer (115), and the retrieve bit counter (117).

(FIG. 1 accompanies this abstract)