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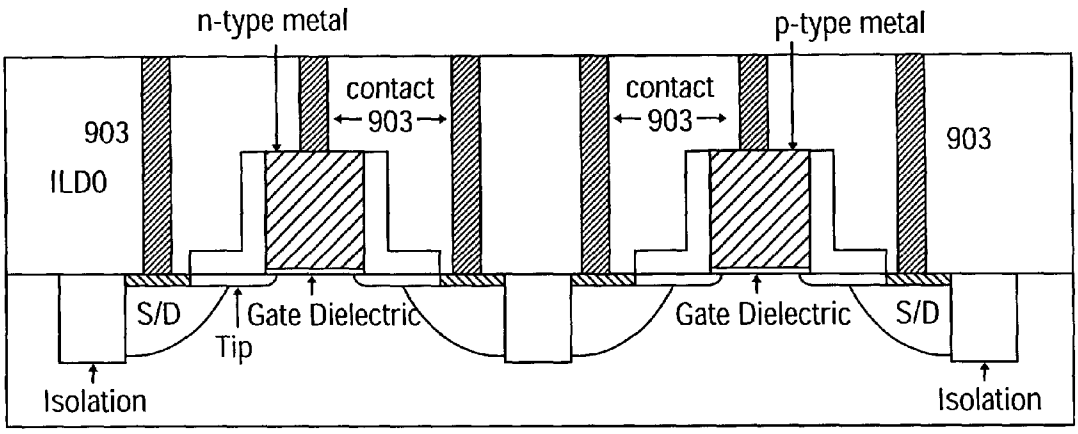
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(54) Title: INTEGRATING N-TYPE AND P-TYPE METAL GATE TRANSISTORS



(57) Abstract: At least a p-type and n-type semiconductor device deposited upon a semiconductor wafer containing metal or metal alloy gates. More particularly, a complementary metal-oxide-semiconductor (CMOS) device is formed on a semiconductor wafer having n-type and p-type metal gates.

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