

ABSTRACT

The invention refers to a DRAM operation frequency adjusting system and a method thereof. The DRAM operation frequency adjusting system includes a statistic module 11 counting effective operation statuses of DRAM to obtain a bandwidth utilization rate of DRAM at a present operation frequency, a parameter configuration module including a target frequency configuration sub-module configured to generate a target operation frequency, and a frequency switch controller for switching a present operation frequency of DRAM to the target operation frequency. The invention benefits adjusting the balance between performance and power consumption of DRAM, so as to improve operation speed of systems on chip and decrease the power consumption.