

FIG. 1

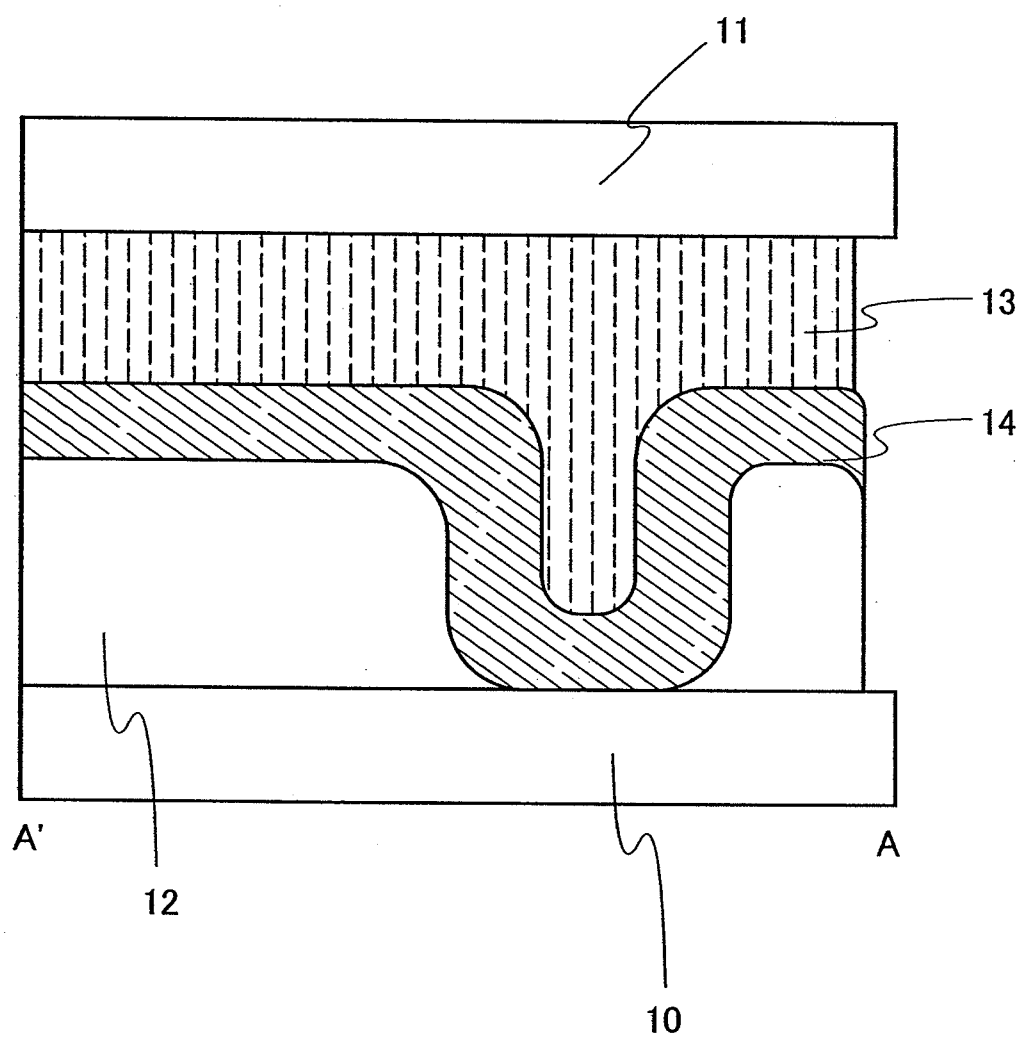


FIG. 2

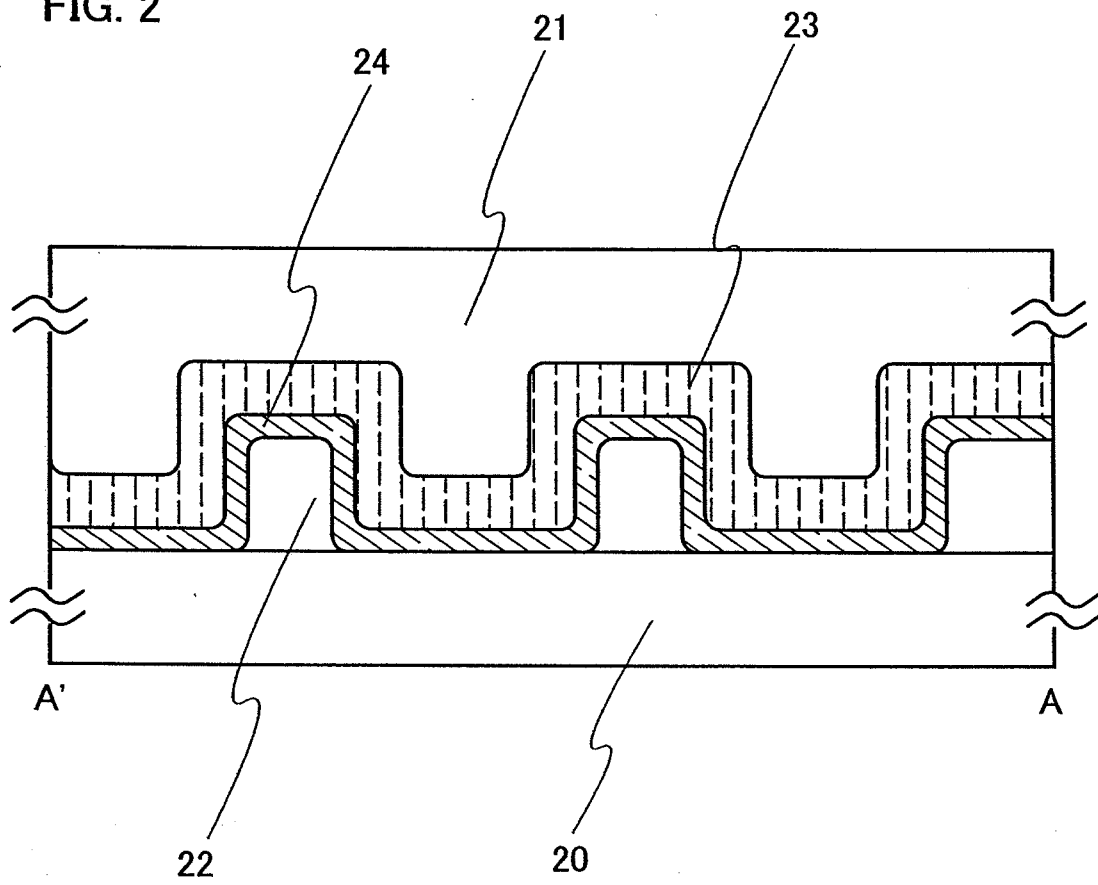


FIG. 3

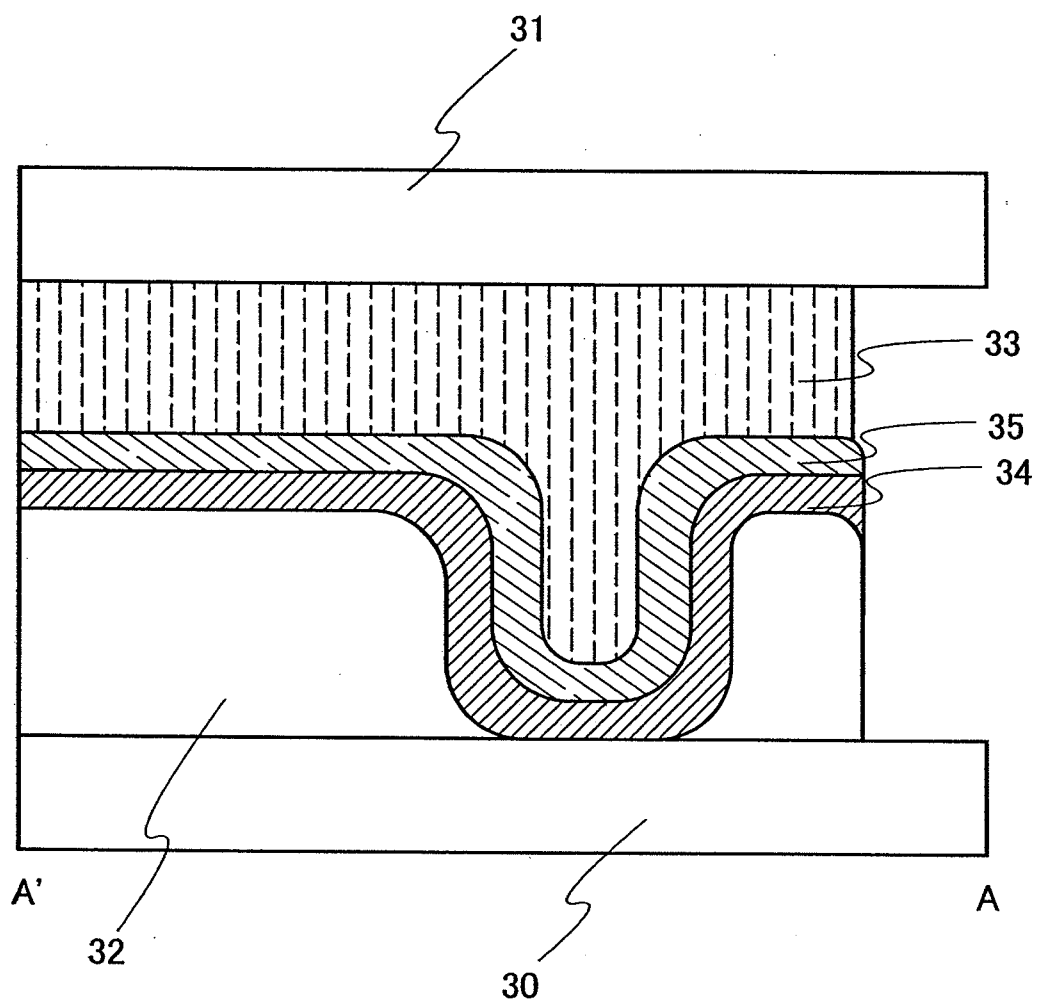


FIG. 4

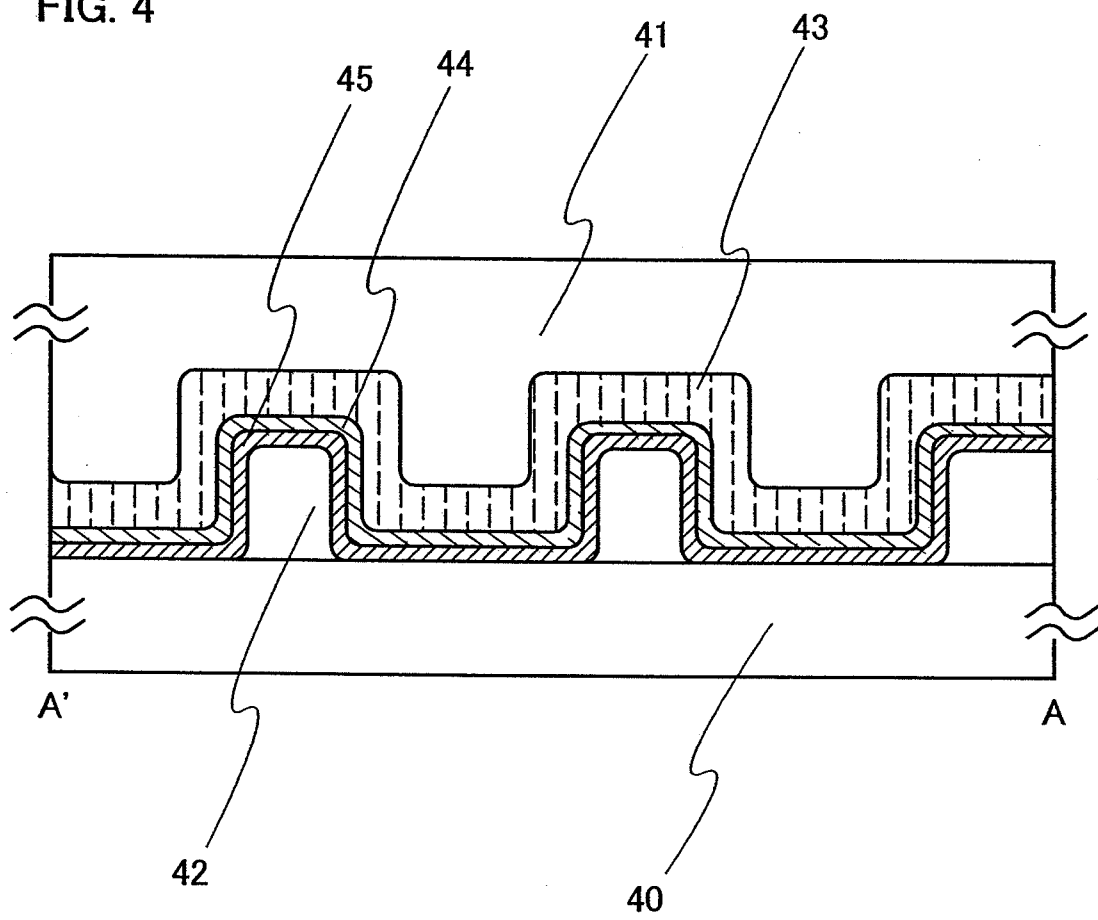


FIG. 5

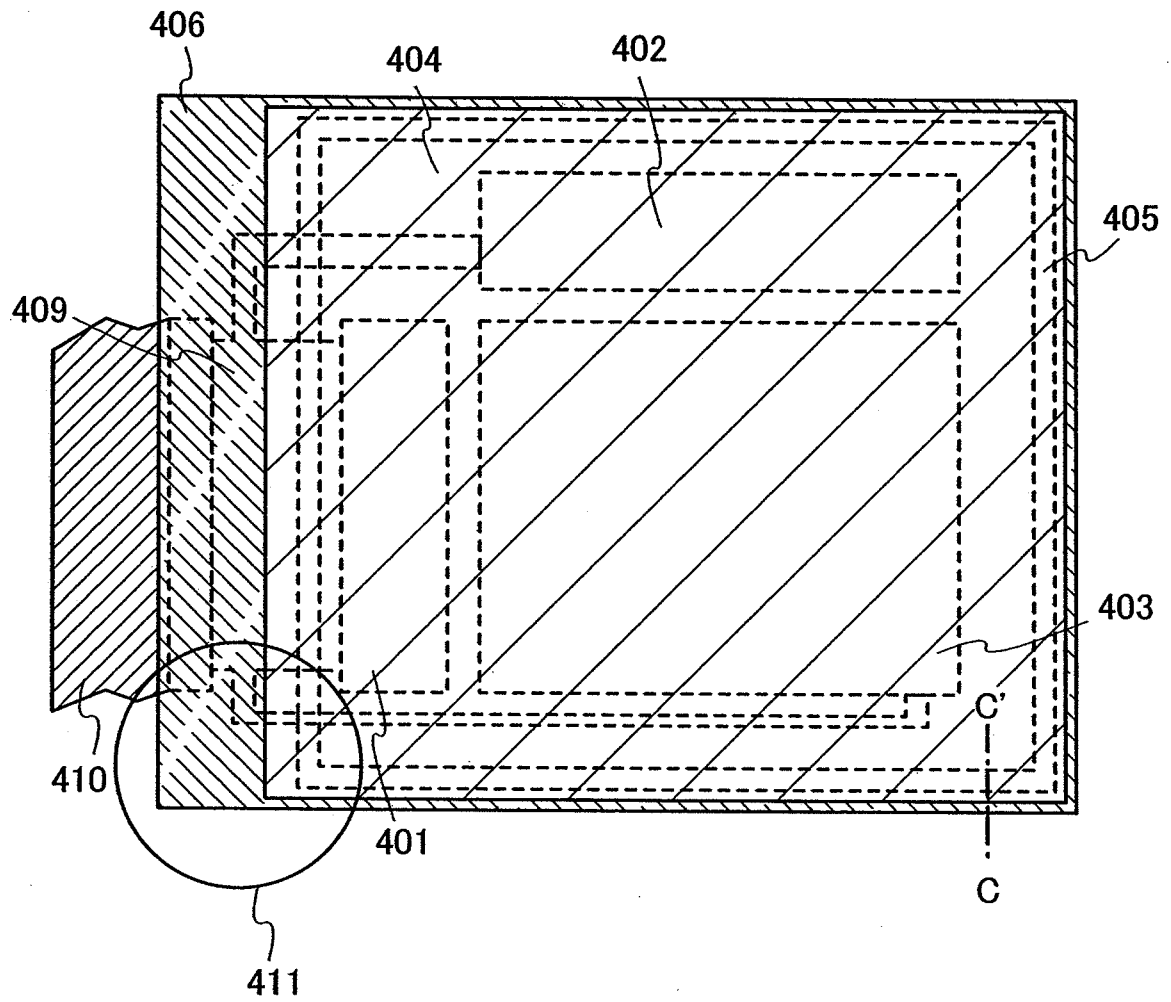


FIG. 6

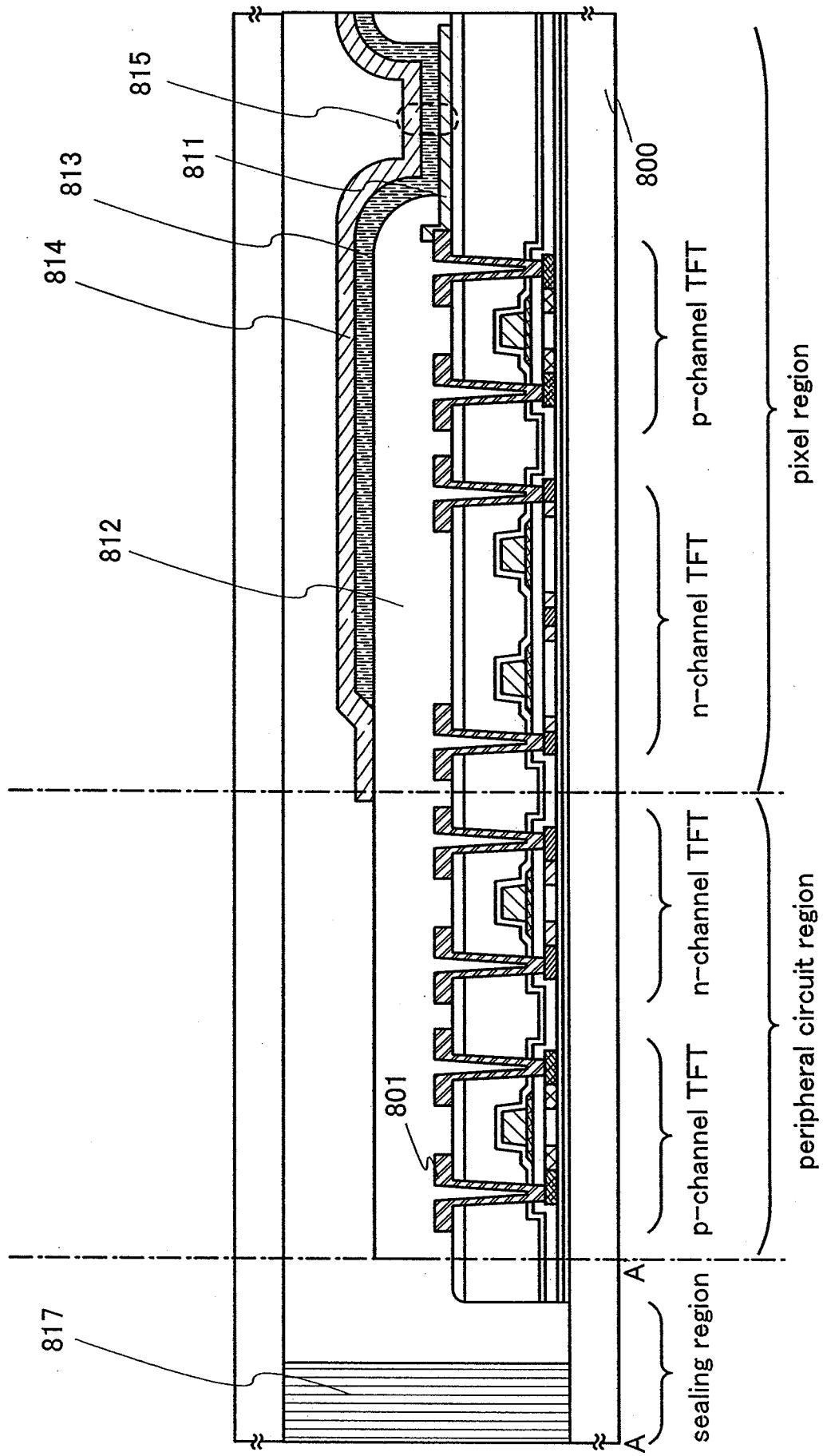


FIG. 7

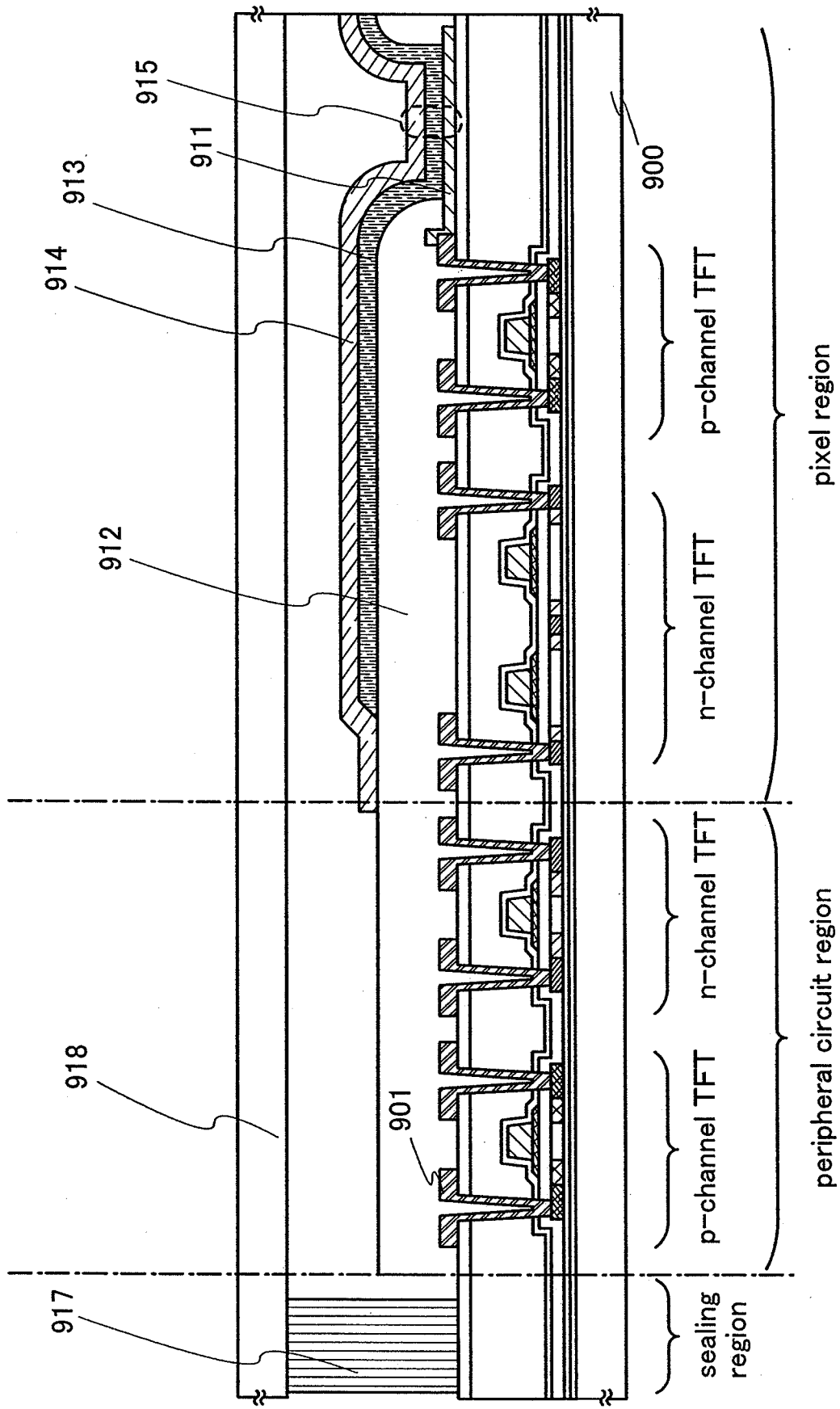


FIG. 8A

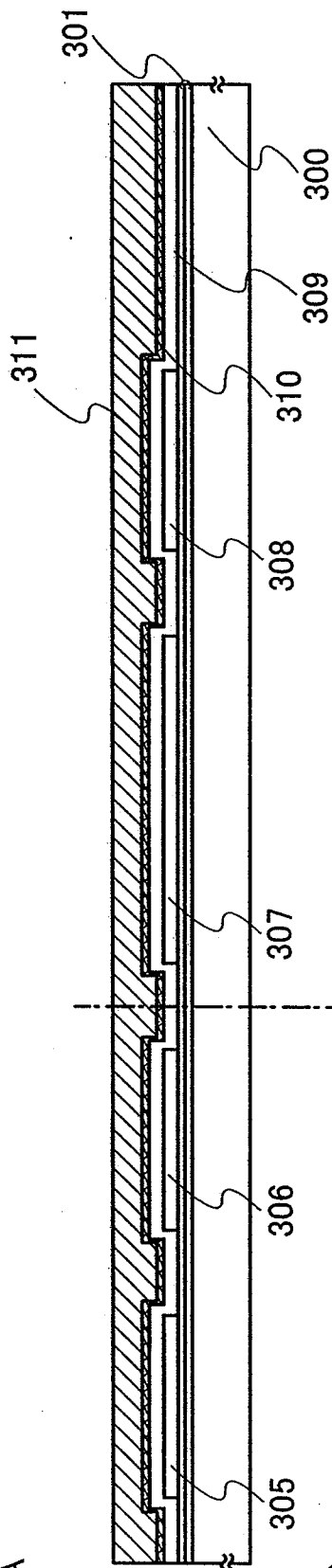


FIG. 8B

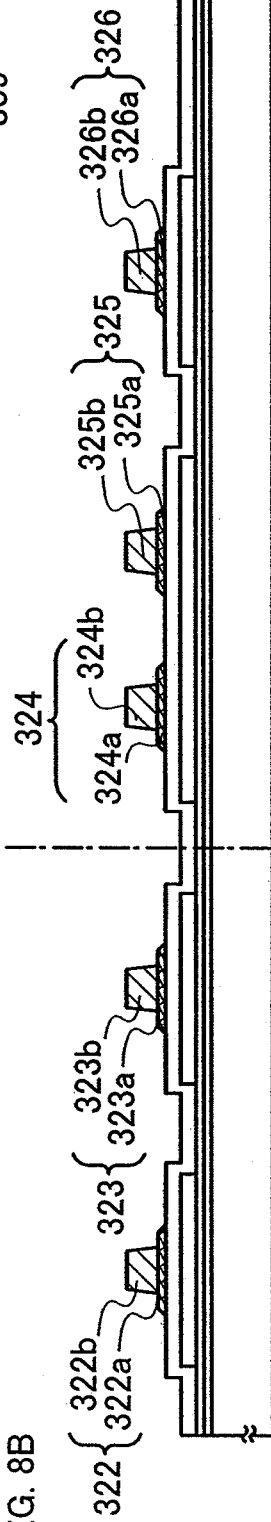


FIG. 8C

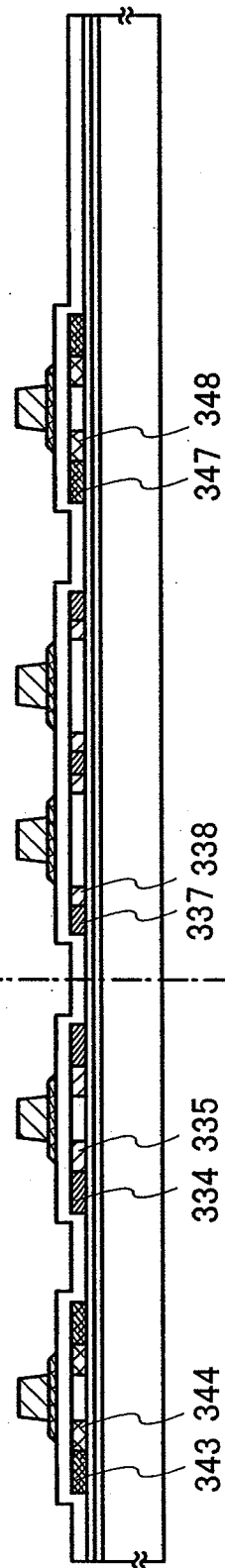


FIG. 9A

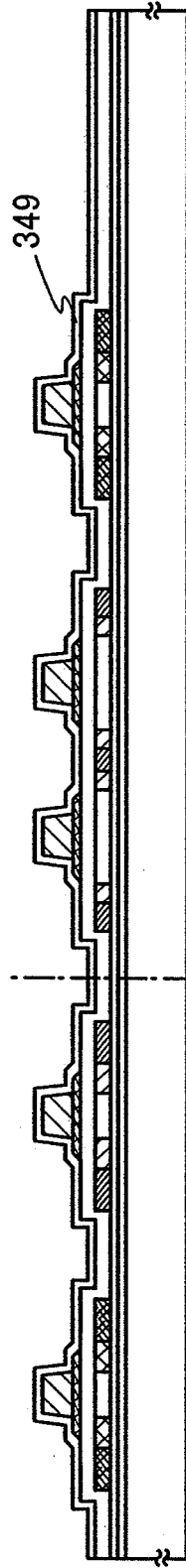


FIG. 9B

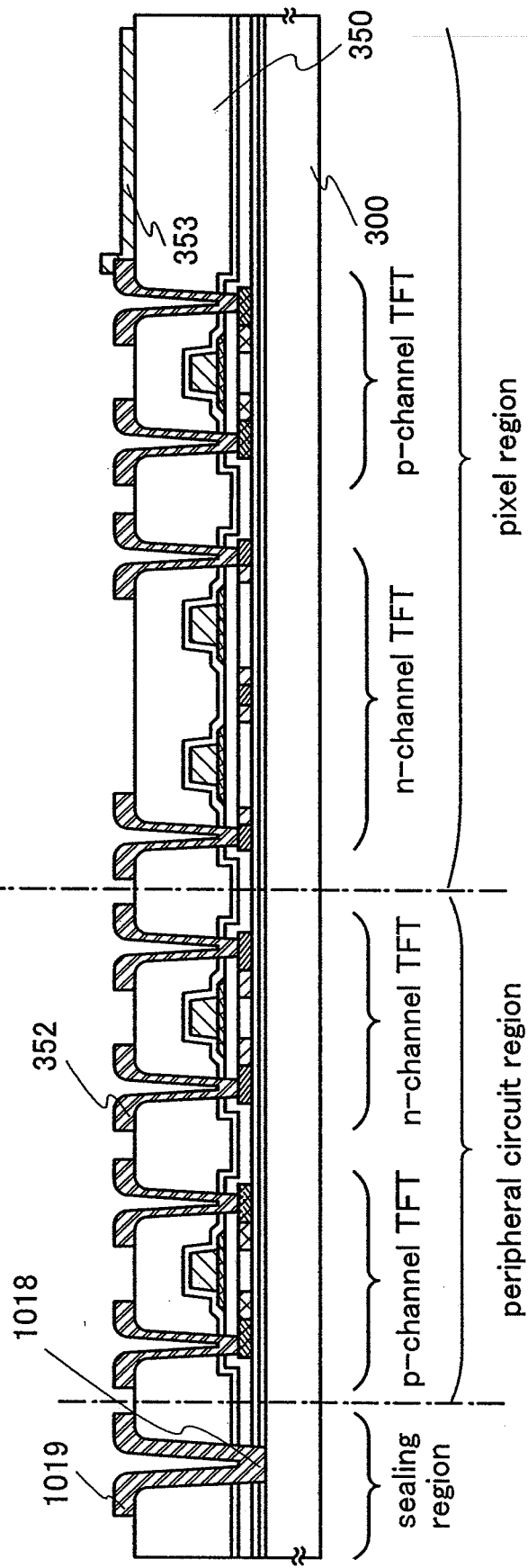


FIG. 10

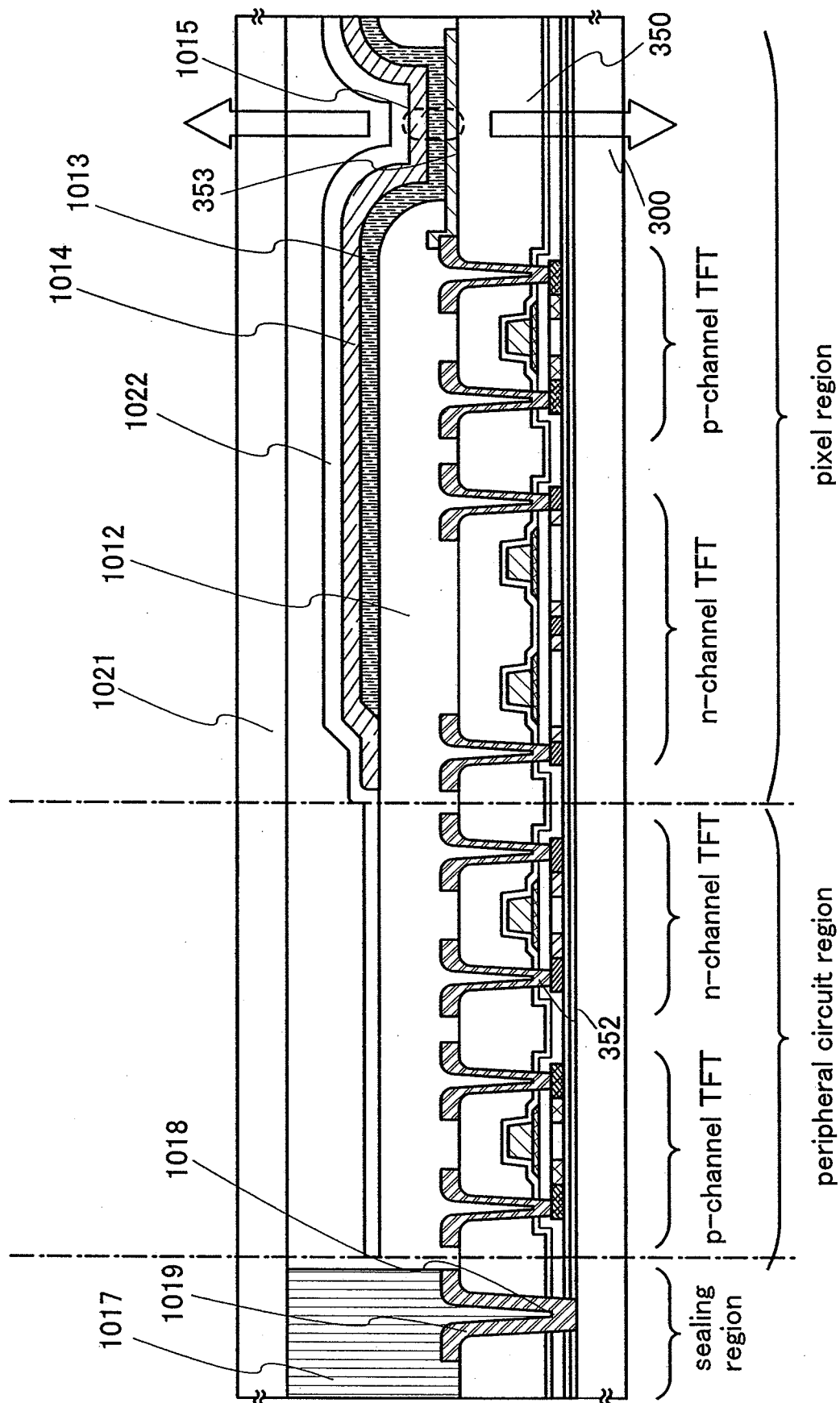


FIG. 11

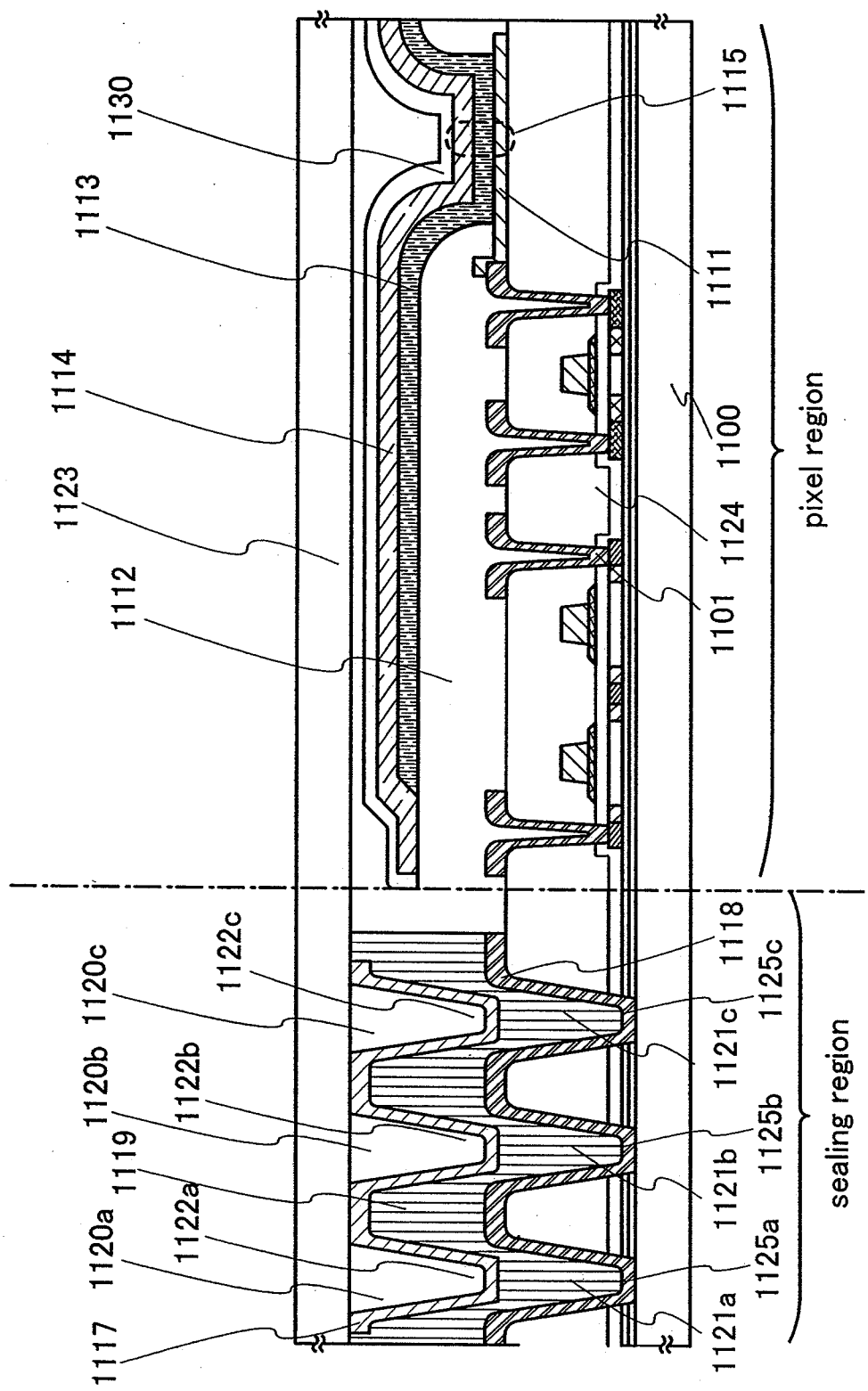


FIG. 12A

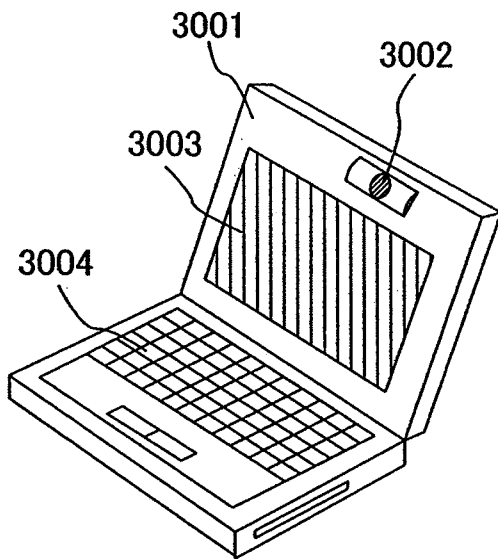


FIG. 12B

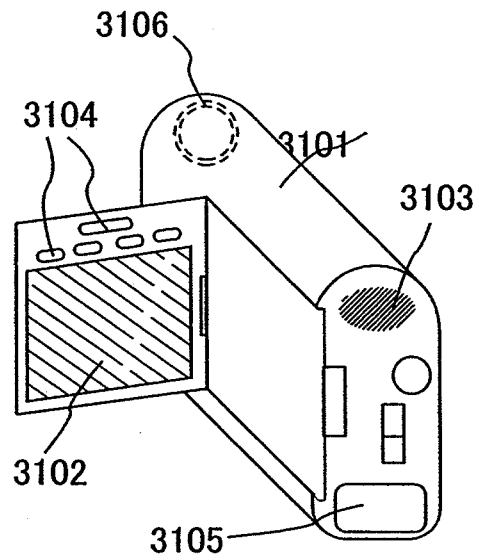


FIG. 12C

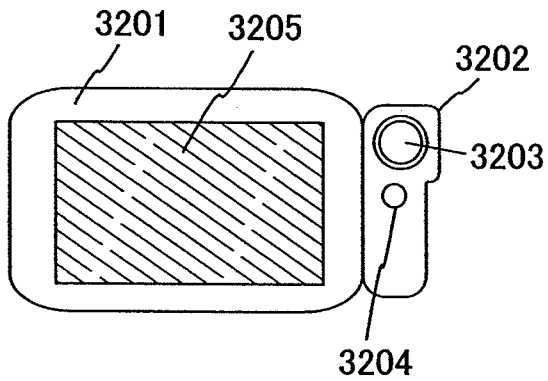


FIG. 12D

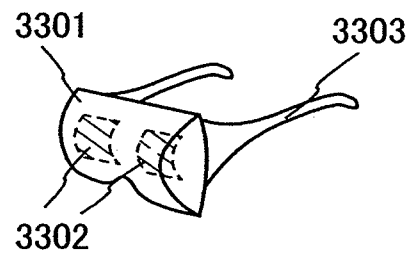


FIG. 12E

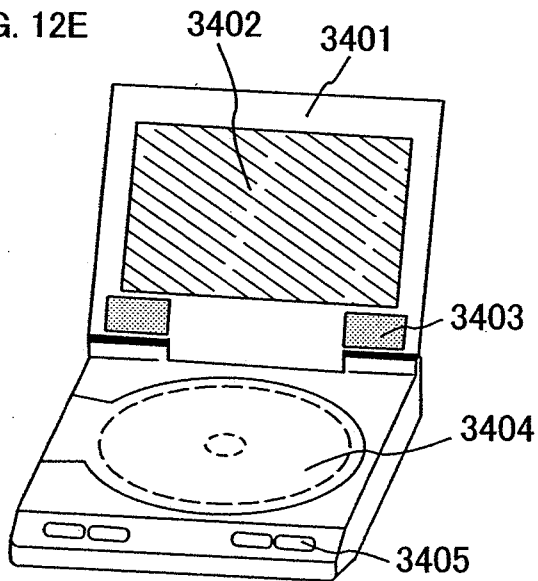
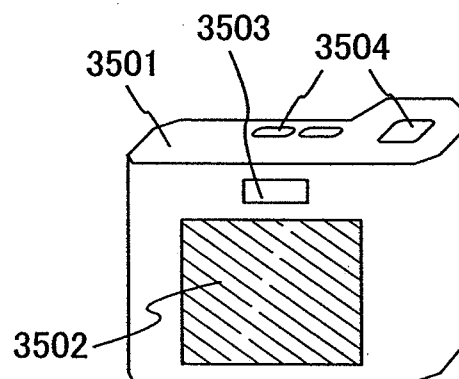


FIG. 12F



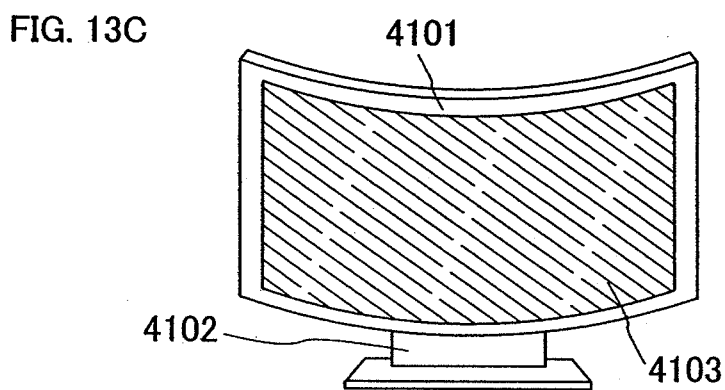
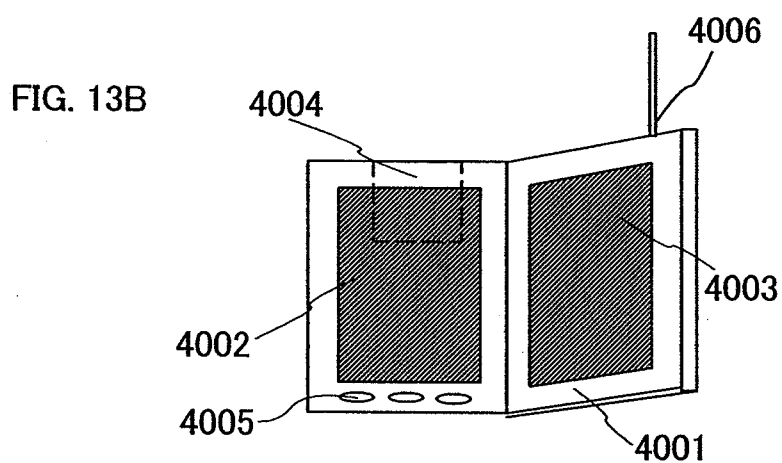
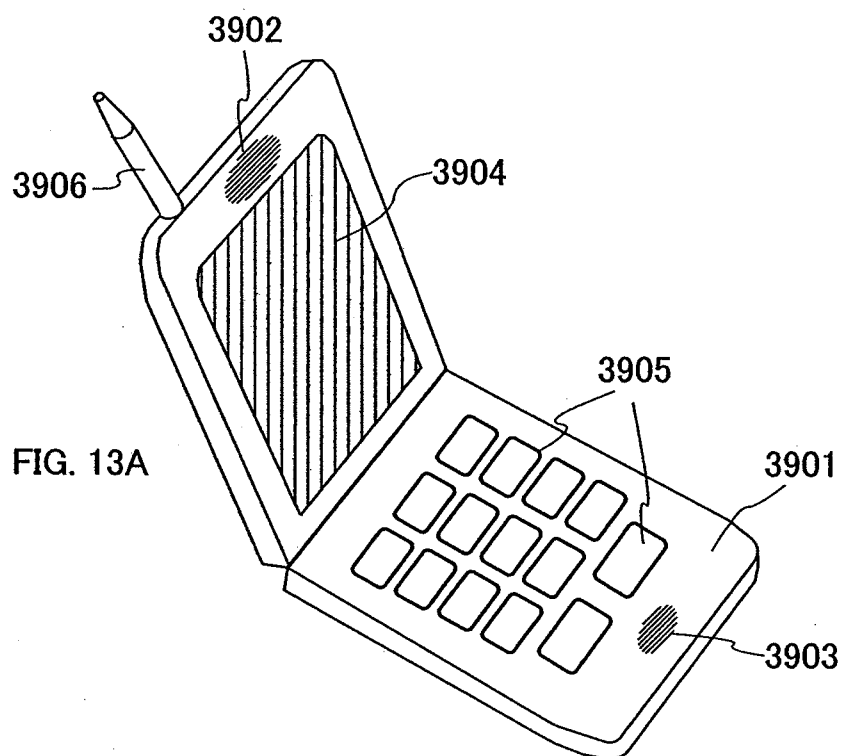


FIG. 14

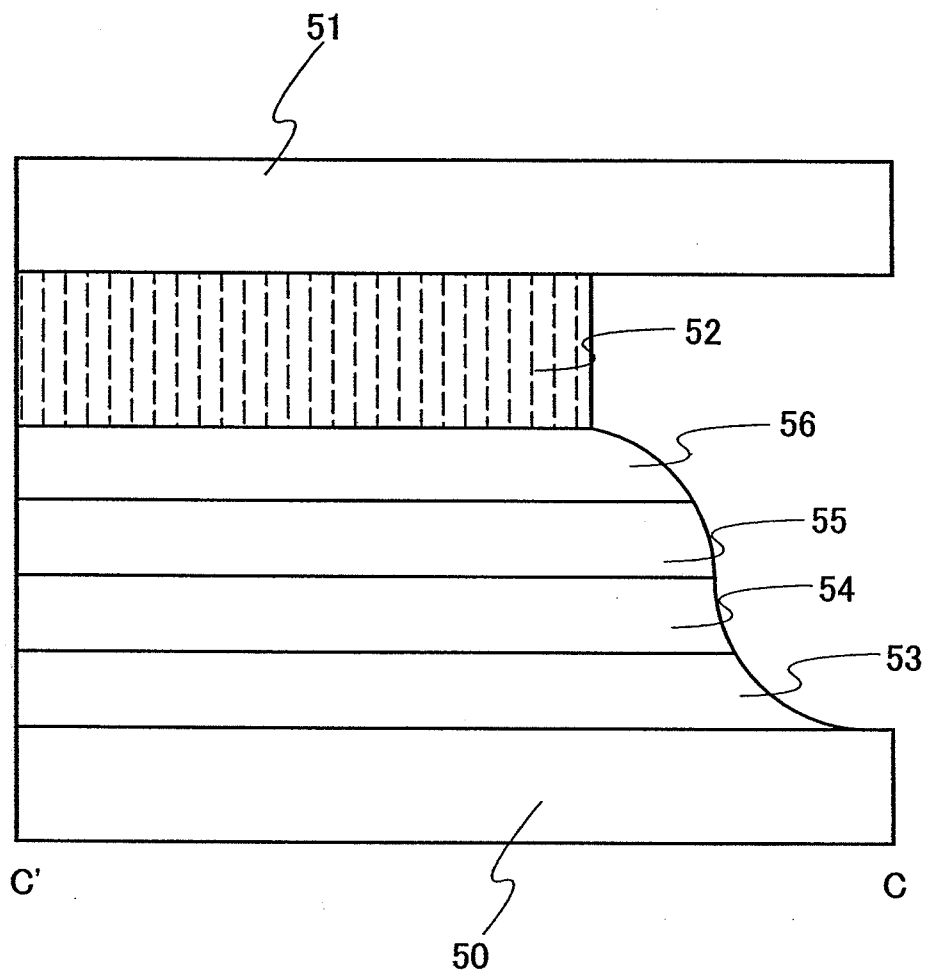
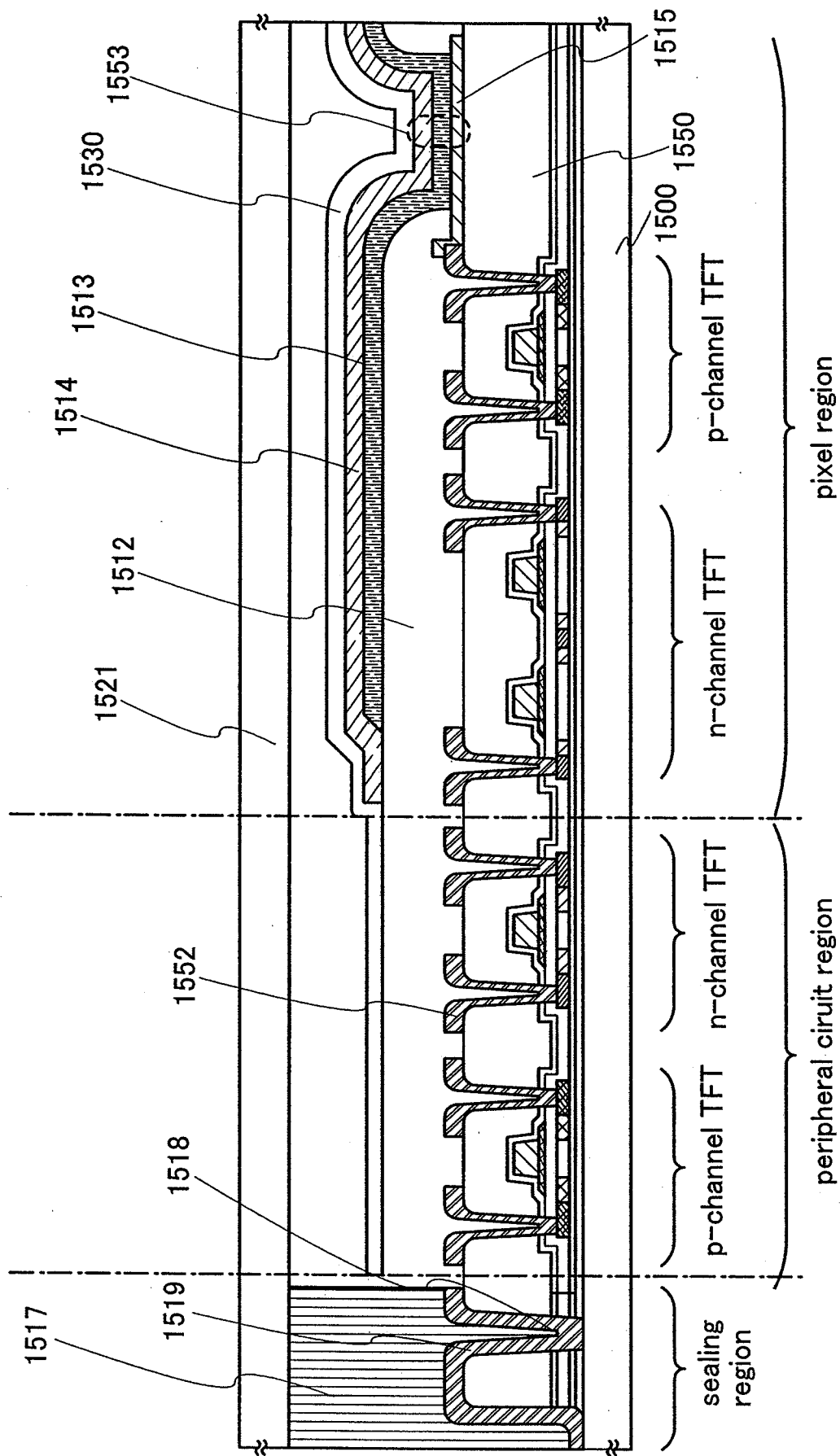


FIG. 15



This diagram shows a cross-sectional view of a display device, divided into a pixel region and a sealing region. The pixel region (right) contains a pixel array with subpixels (1601, 1602, 1603) and a common electrode (1611). The sealing region (left) contains a sealing structure (1617, 1618, 1619, 1620a, 1620b, 1620c, 1621a, 1621b, 1621c, 1622a, 1622b, 1622c, 1625a, 1625b, 1625c) and a common electrode (1614, 1615, 1616, 1617, 1618, 1619, 1620a, 1620b, 1620c, 1621a, 1621b, 1621c, 1622a, 1622b, 1622c, 1625a, 1625b, 1625c). The pixel region is labeled "pixel region" and the sealing region is labeled "sealing region".

FIG. 17

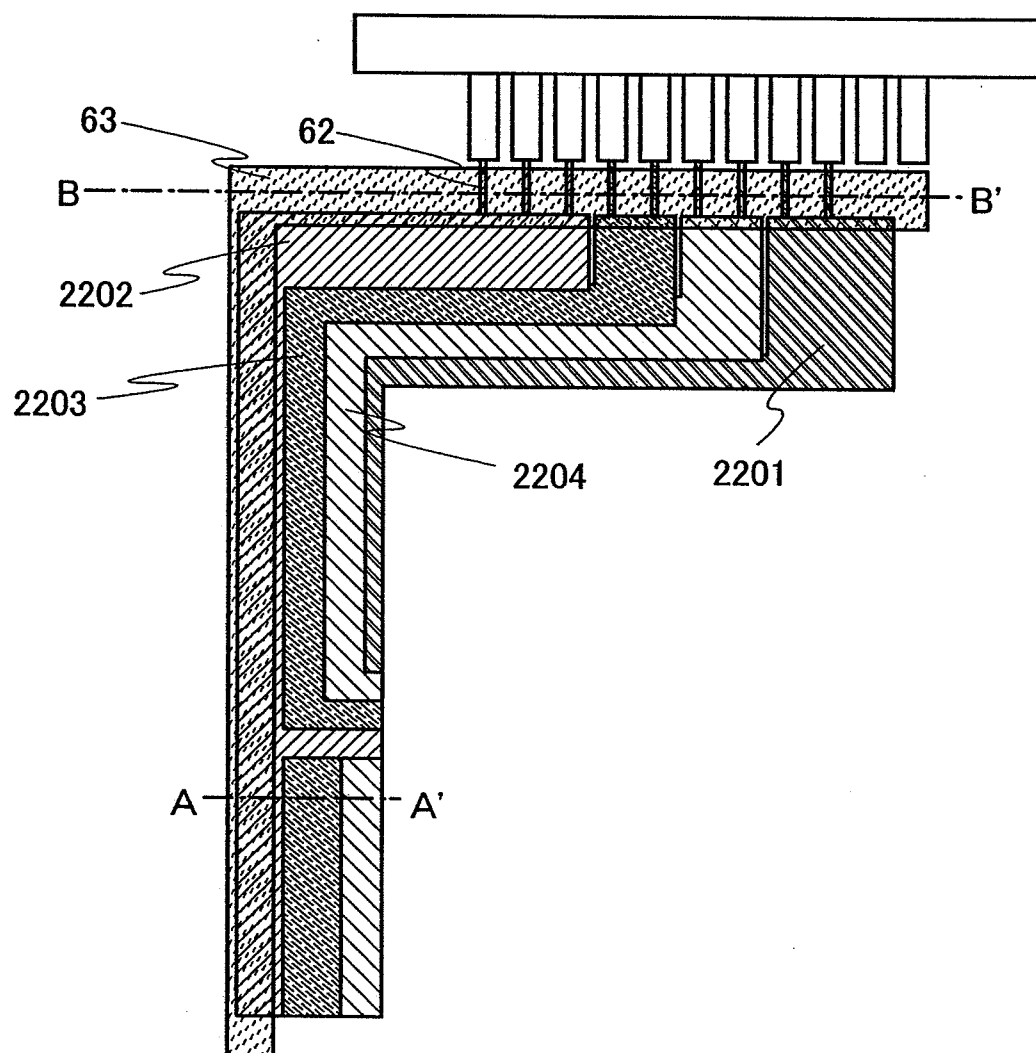


FIG. 18

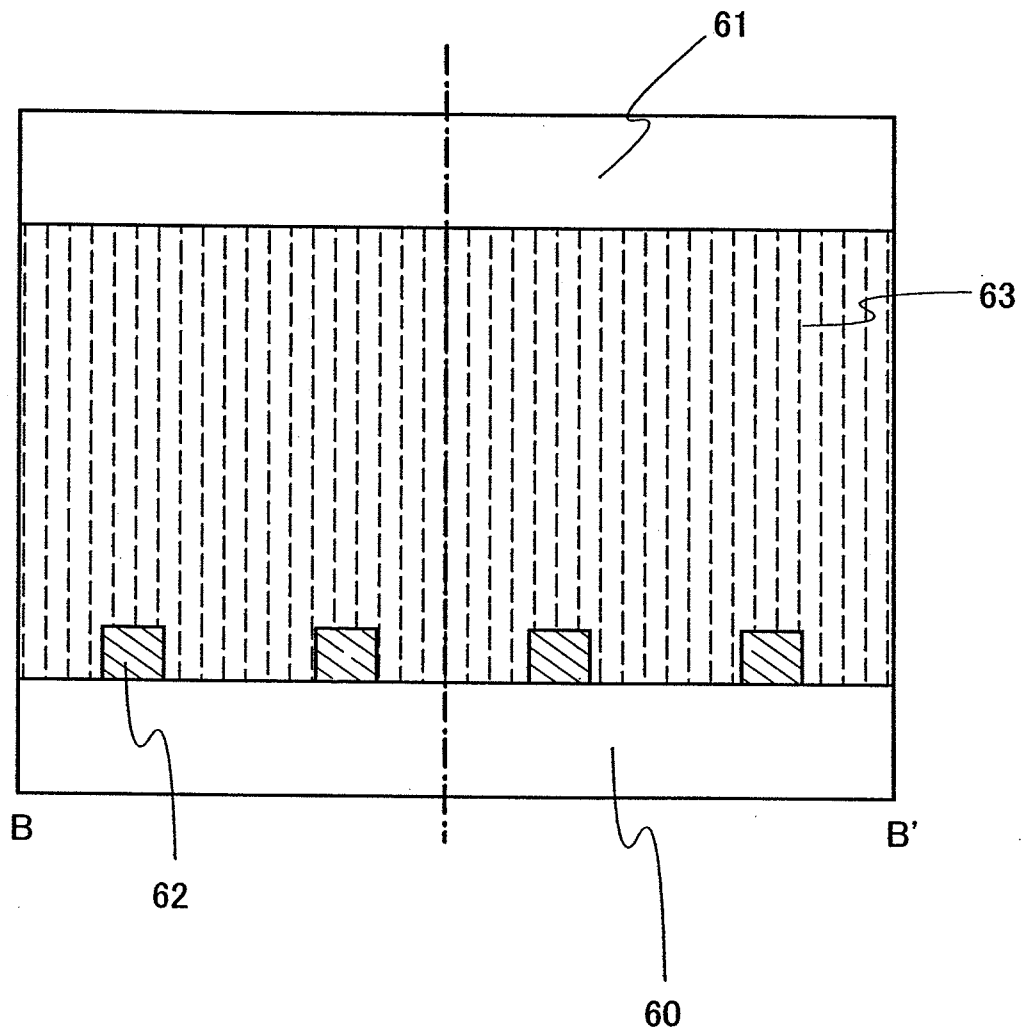


FIG. 19A

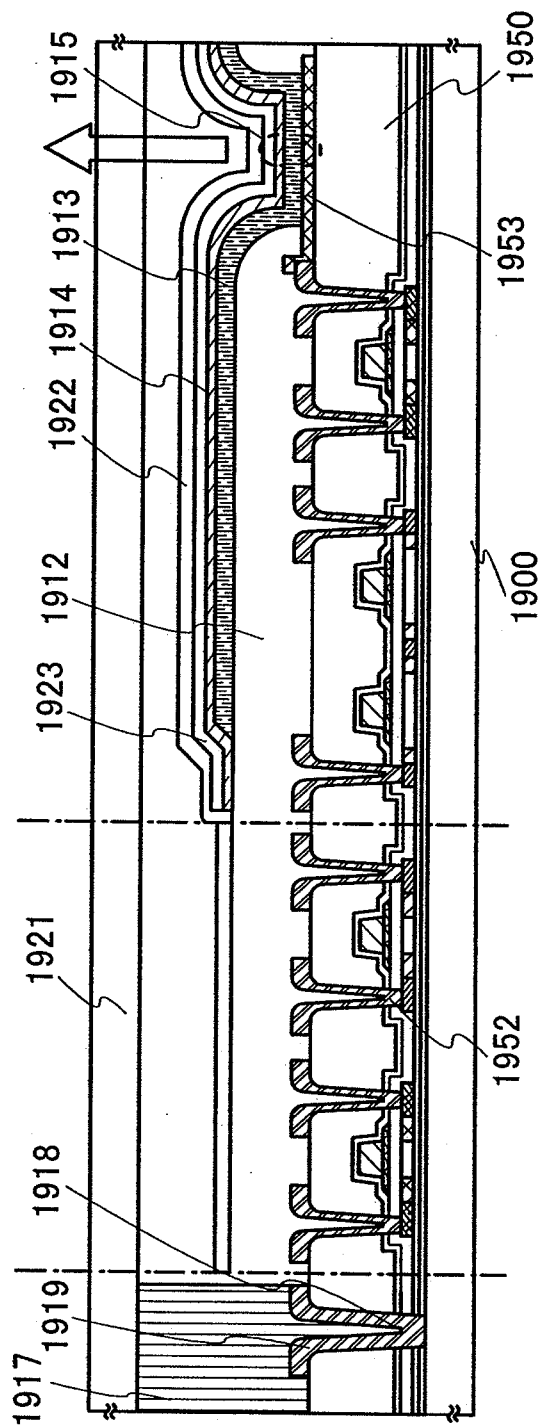


FIG. 19B

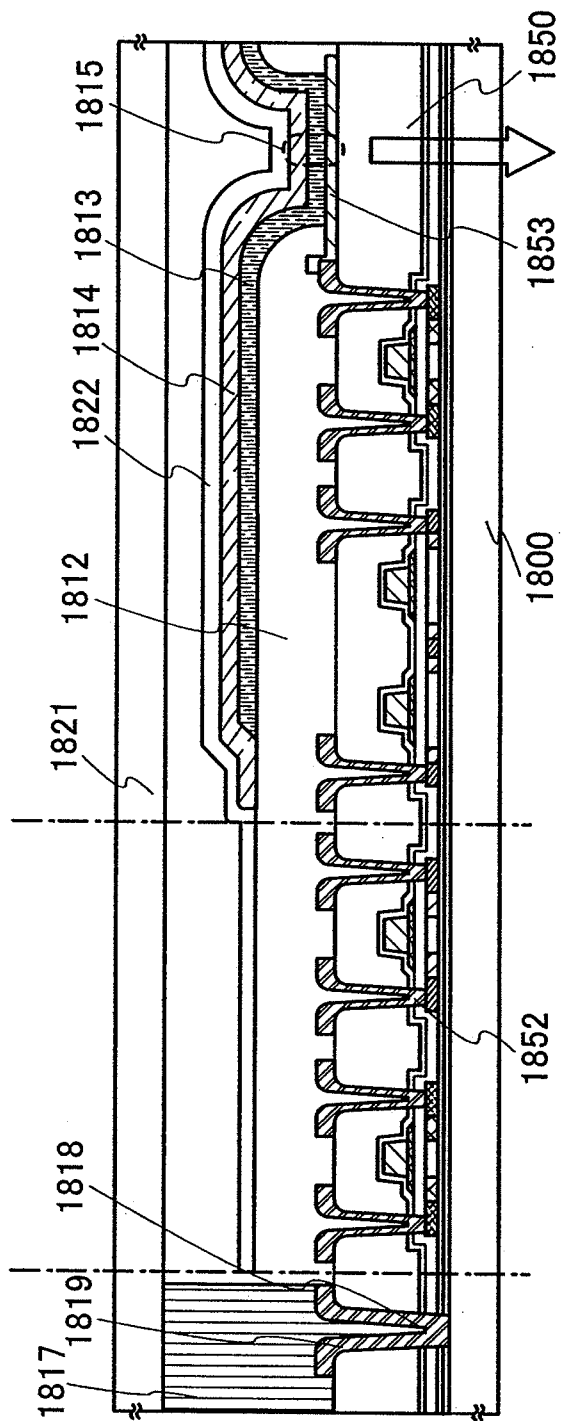


FIG. 20A

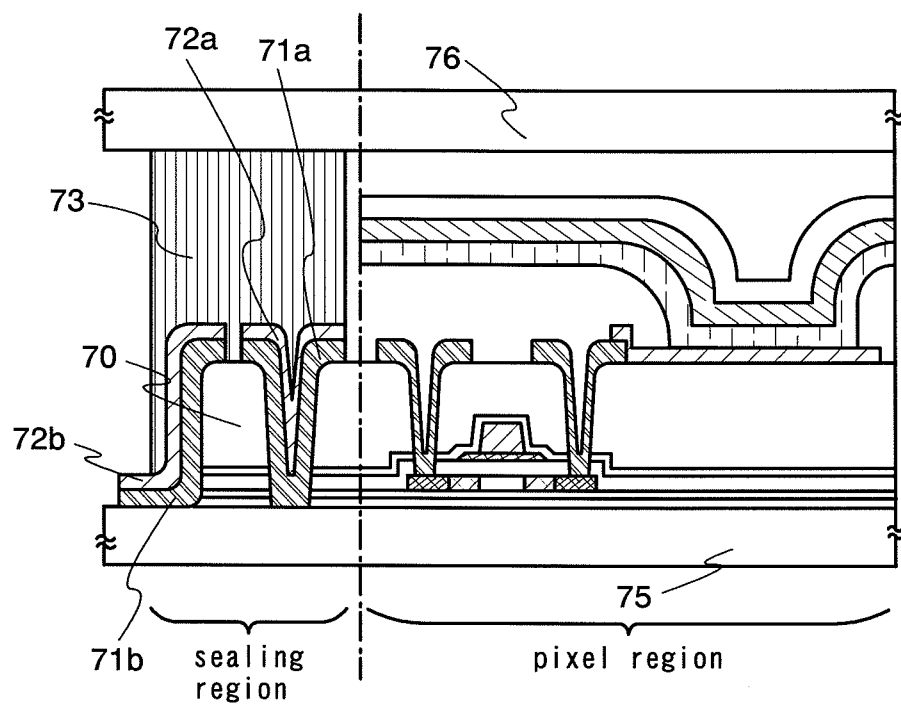


FIG. 20B

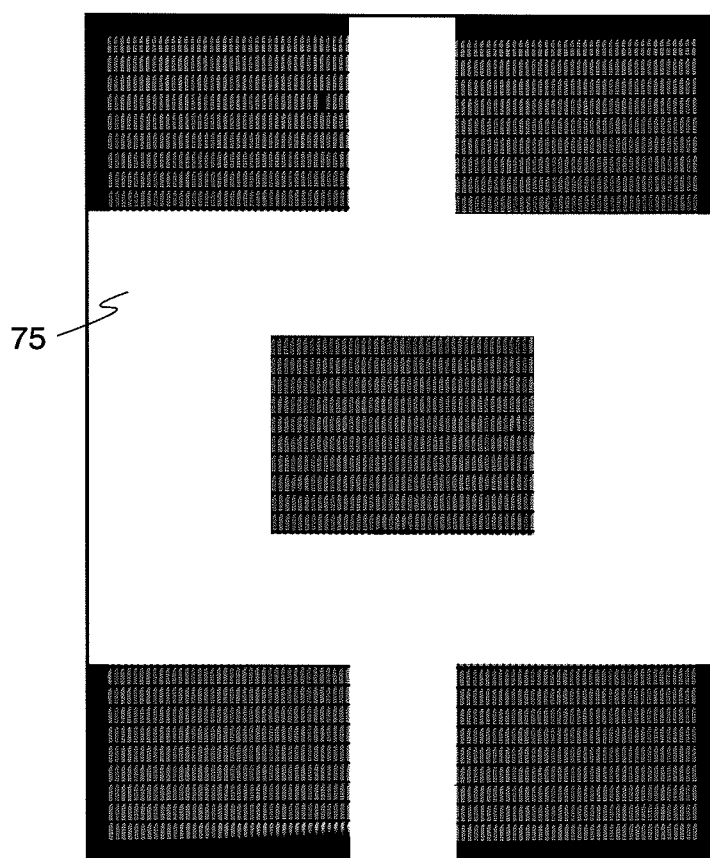


FIG. 21A

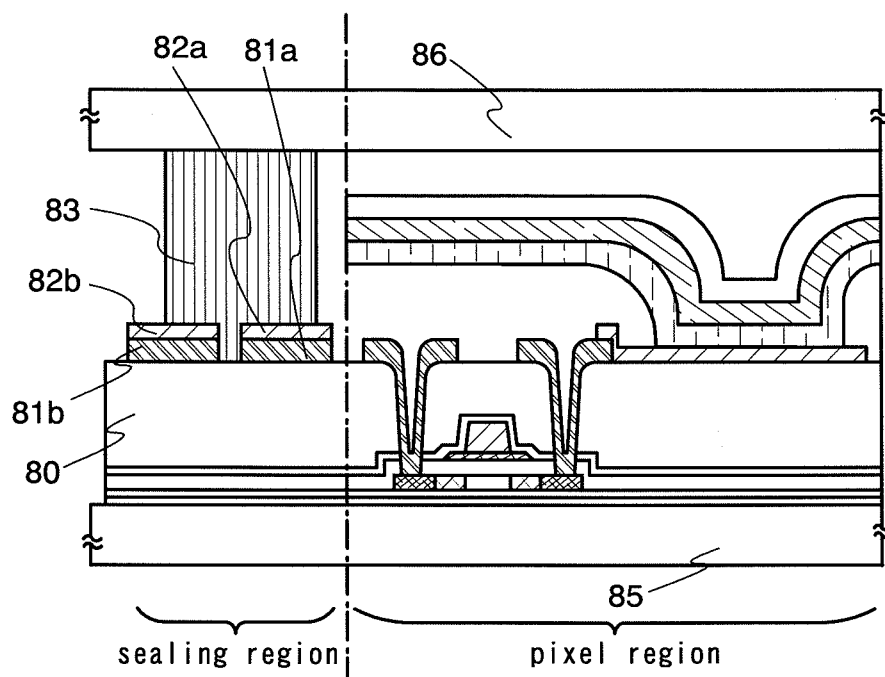


FIG. 21B

