

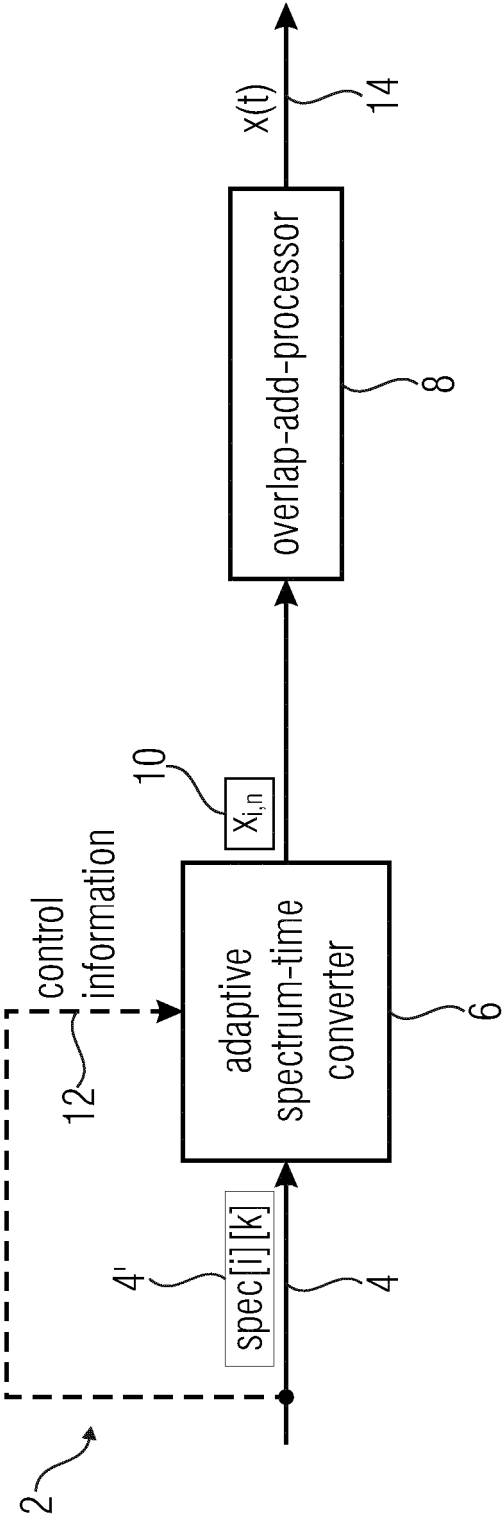


FIG 1

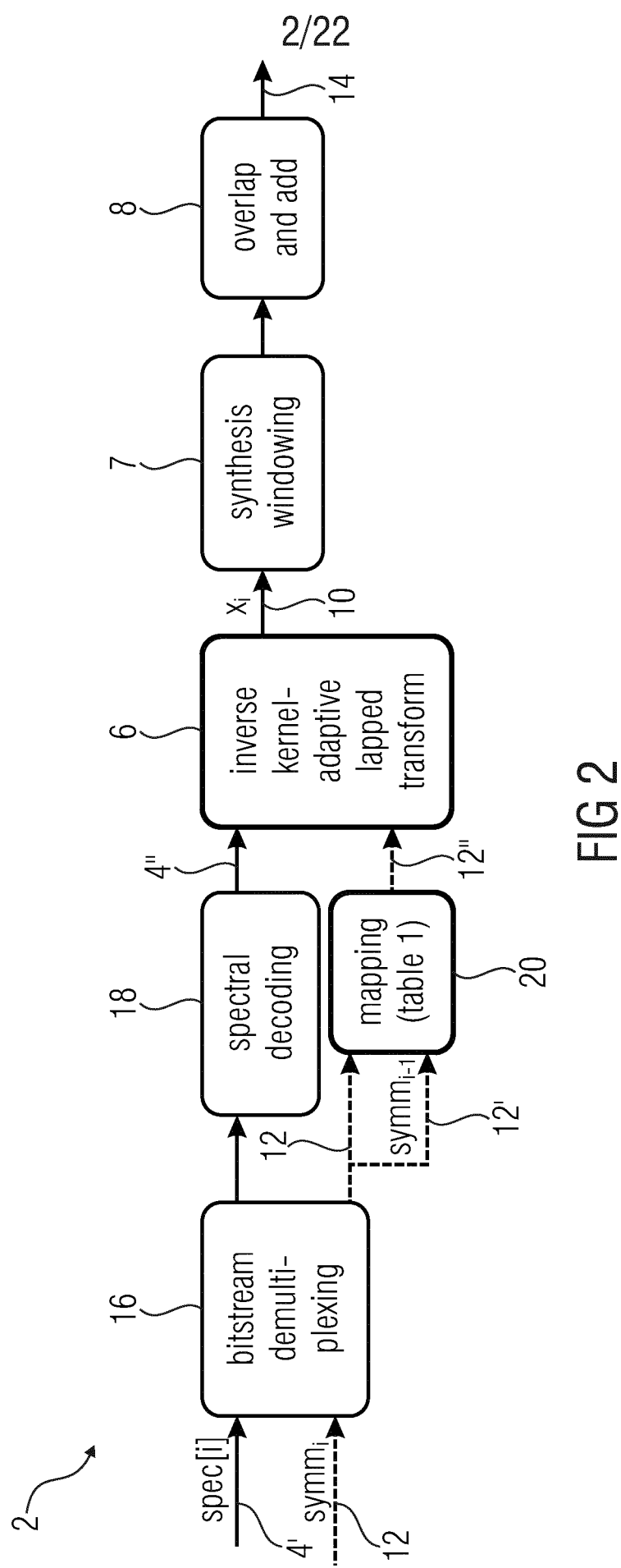


FIG 2

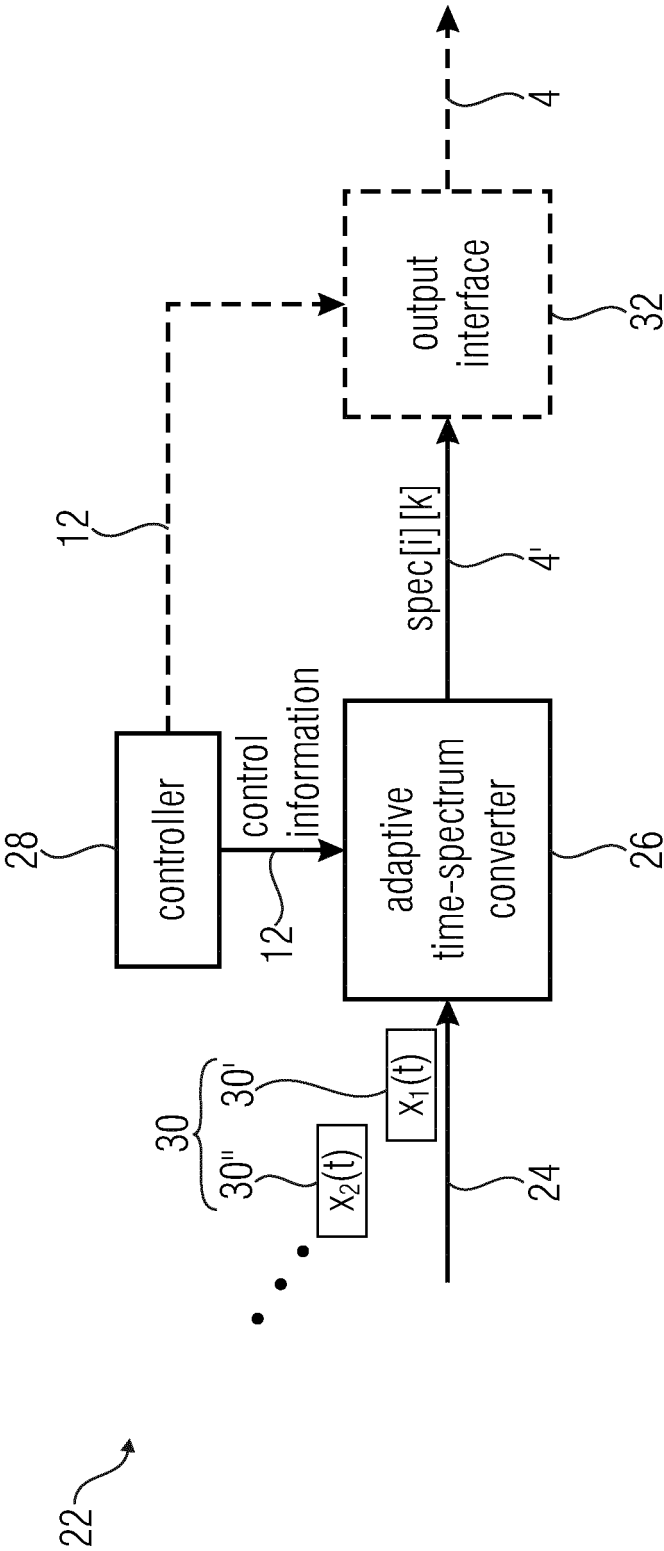


FIG 3

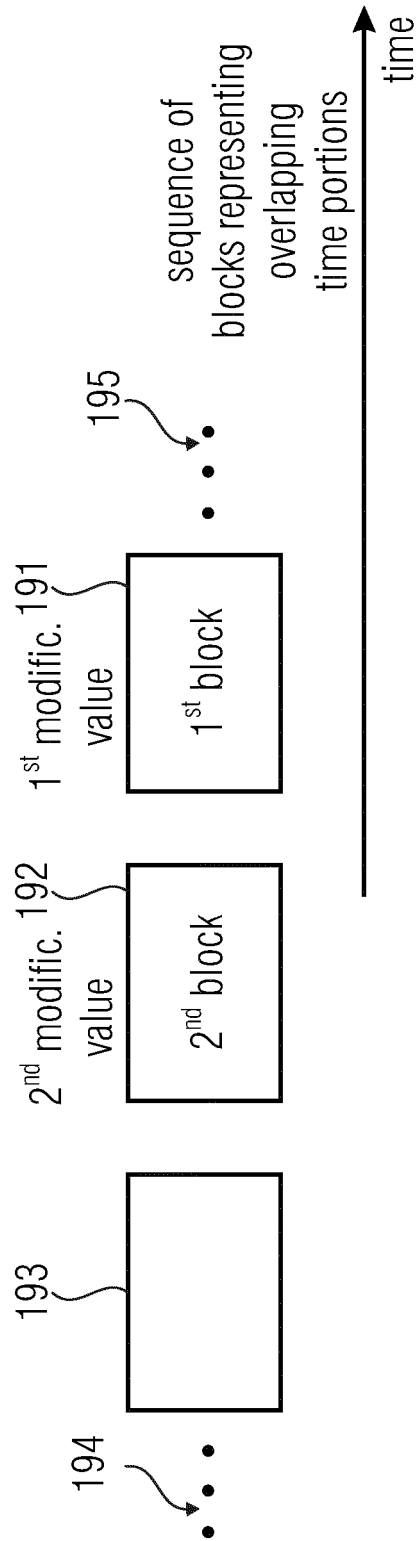


FIG 4A

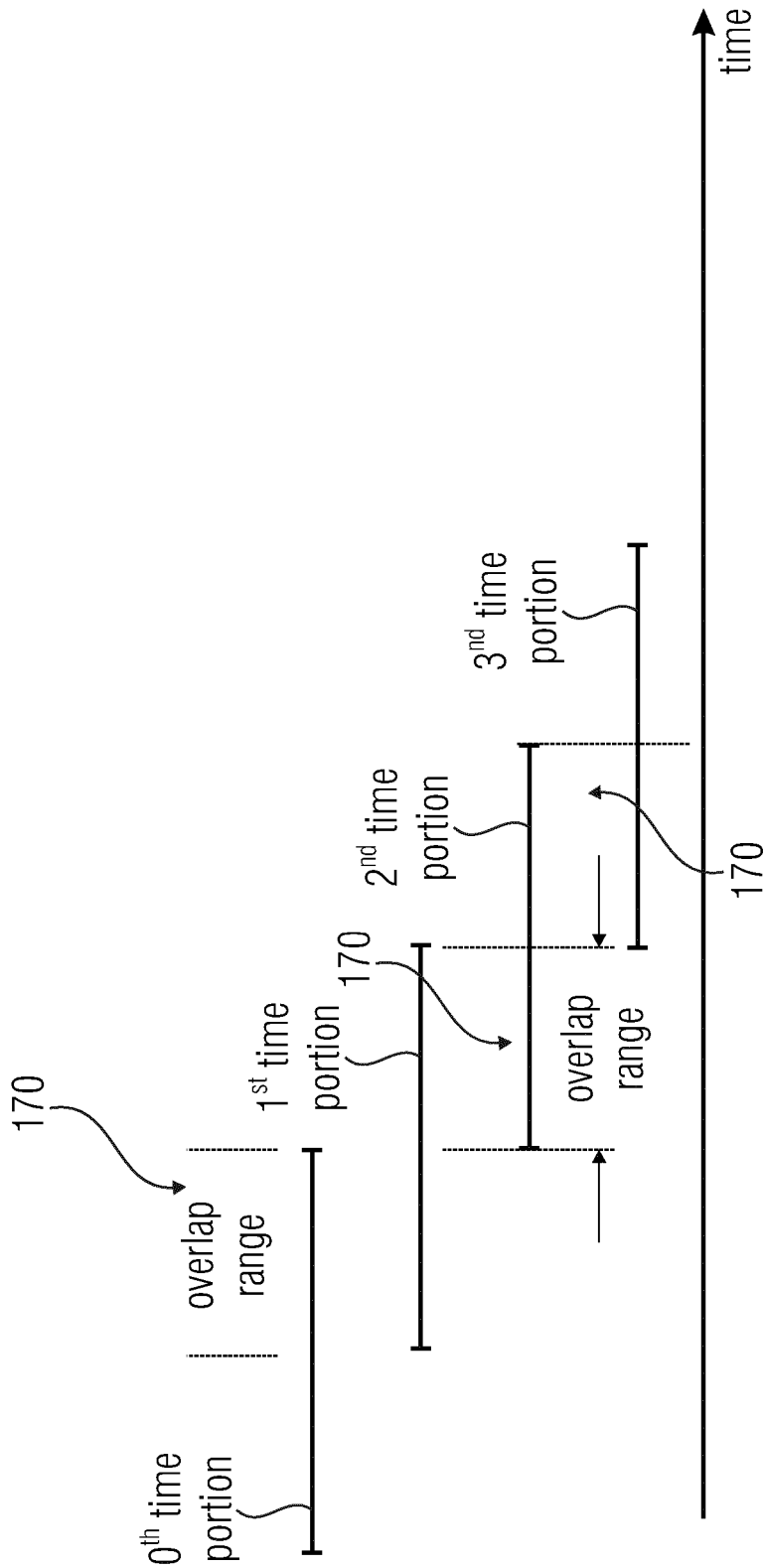


FIG 4B

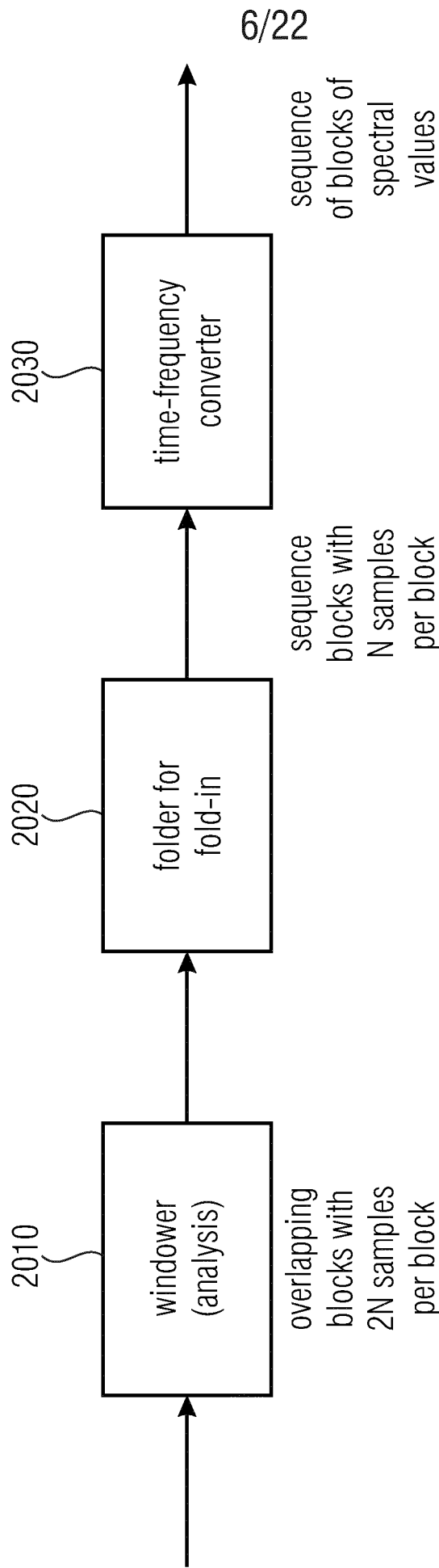


FIG 5A

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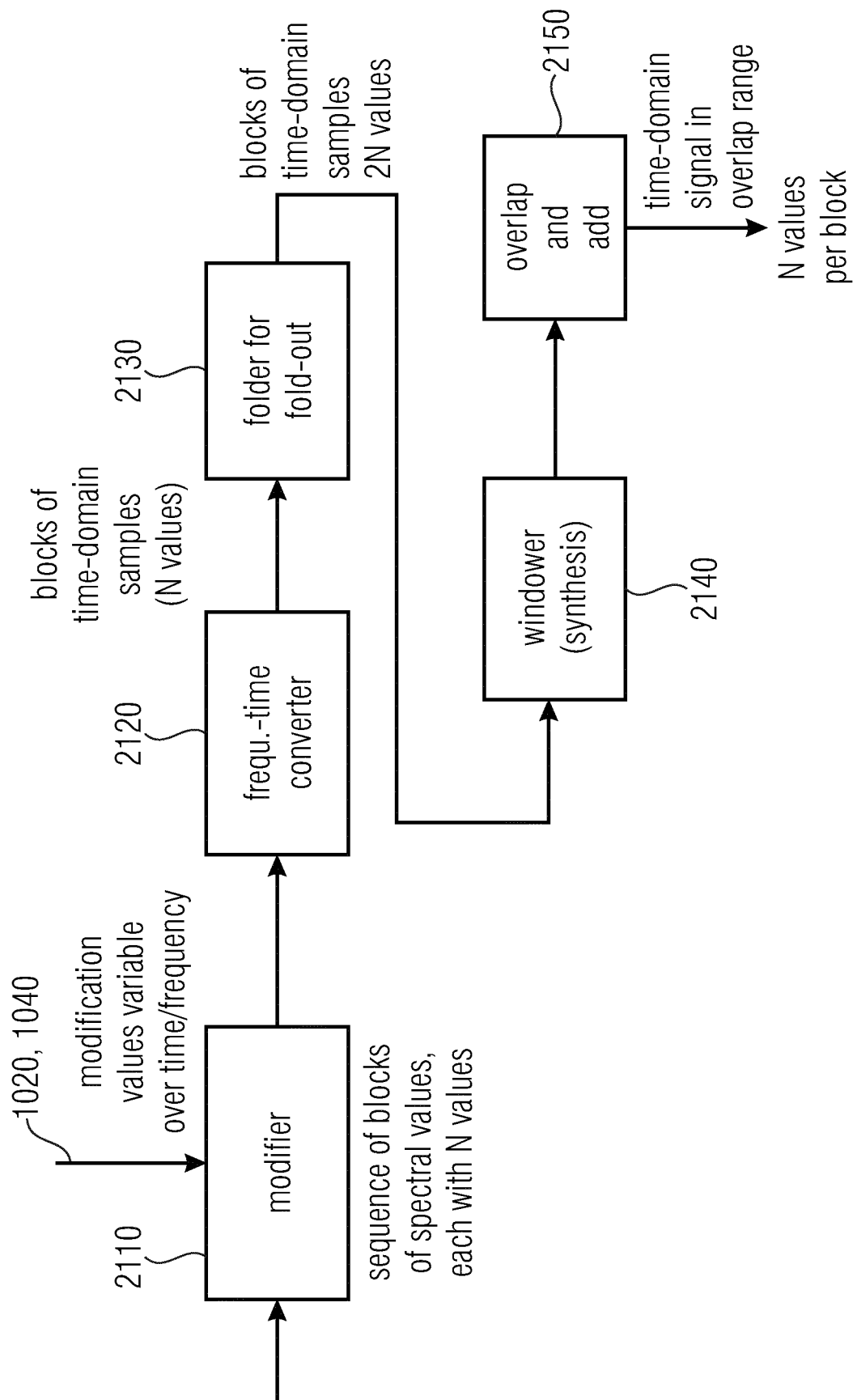
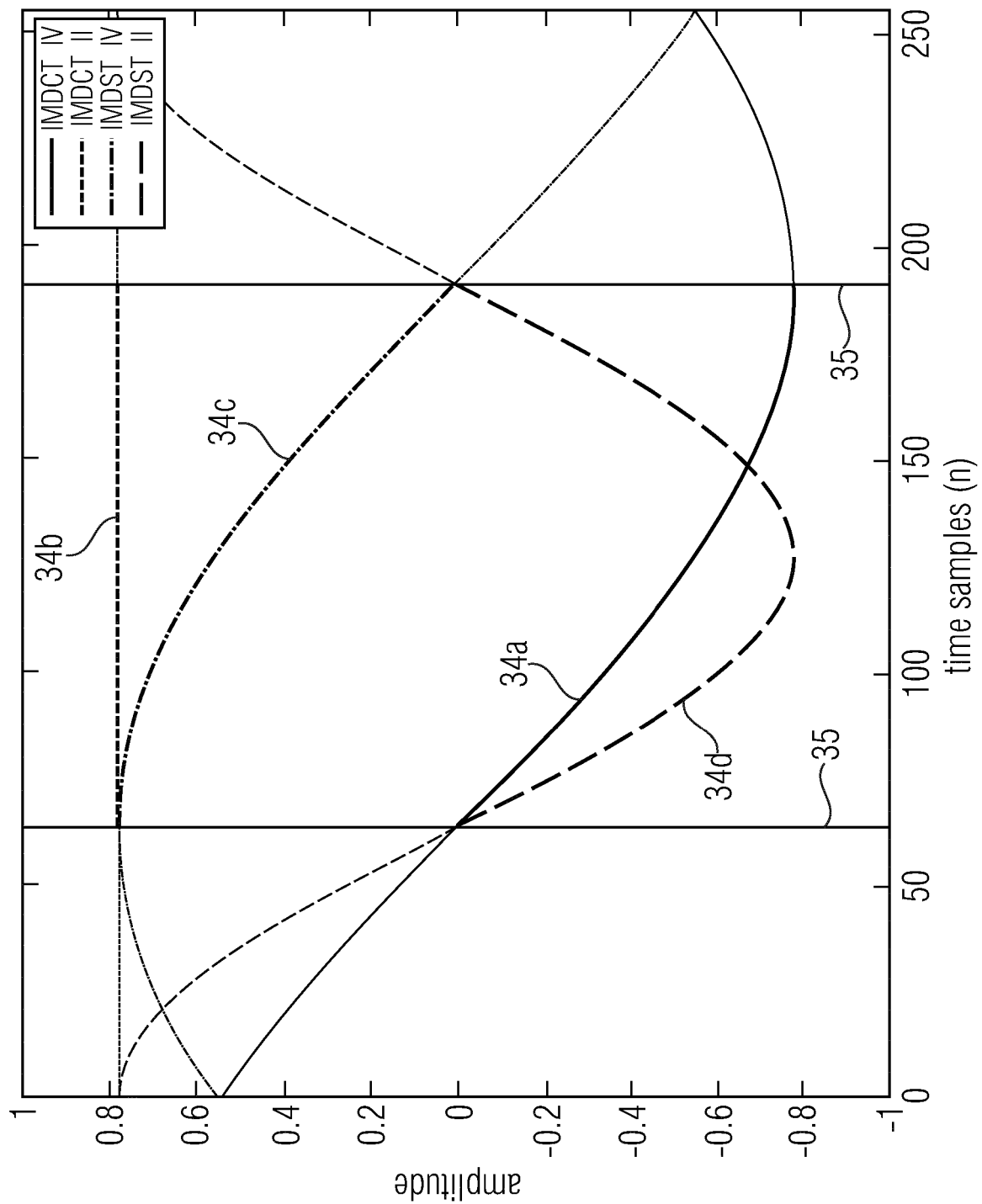


FIG 5B



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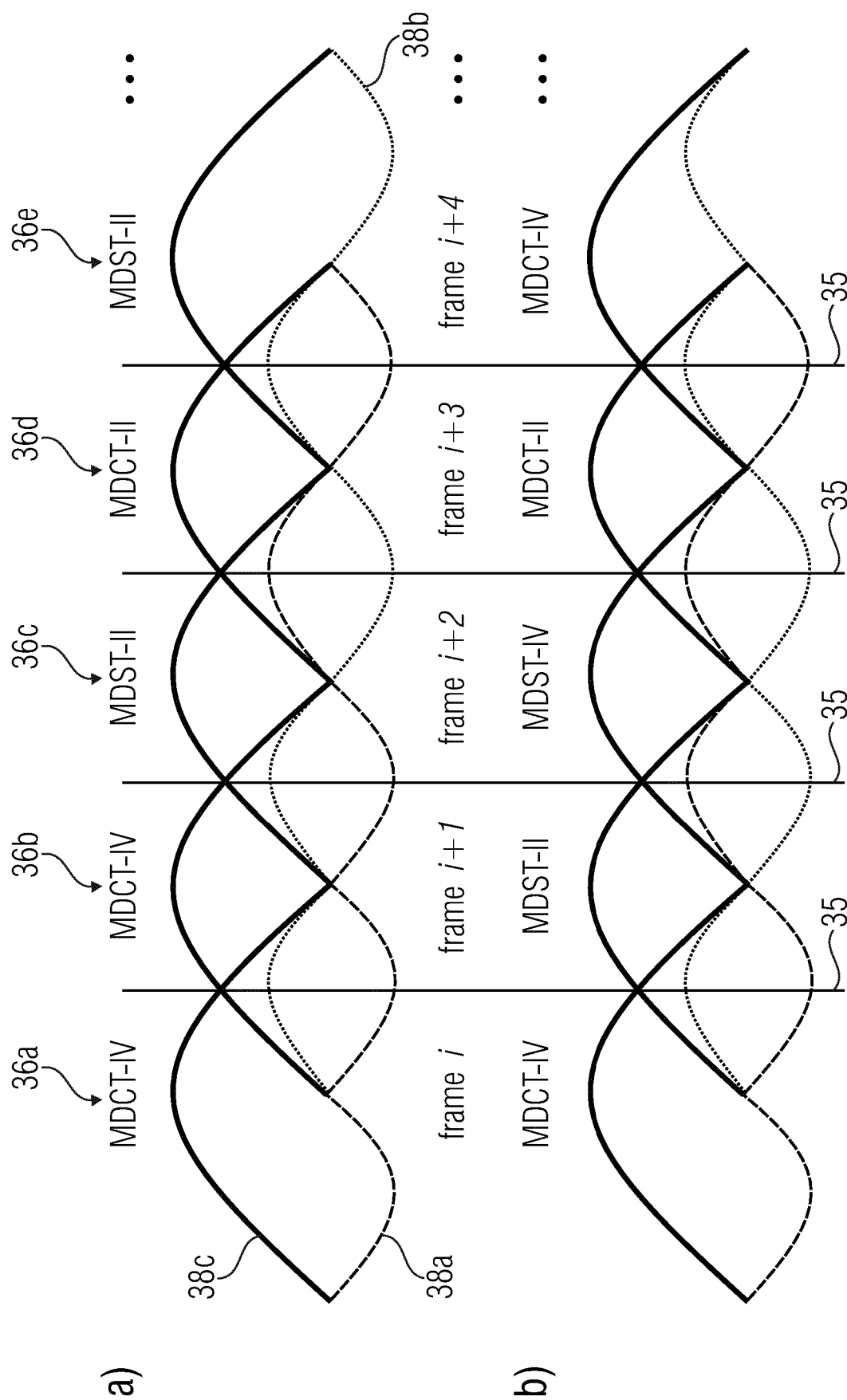


FIG 7

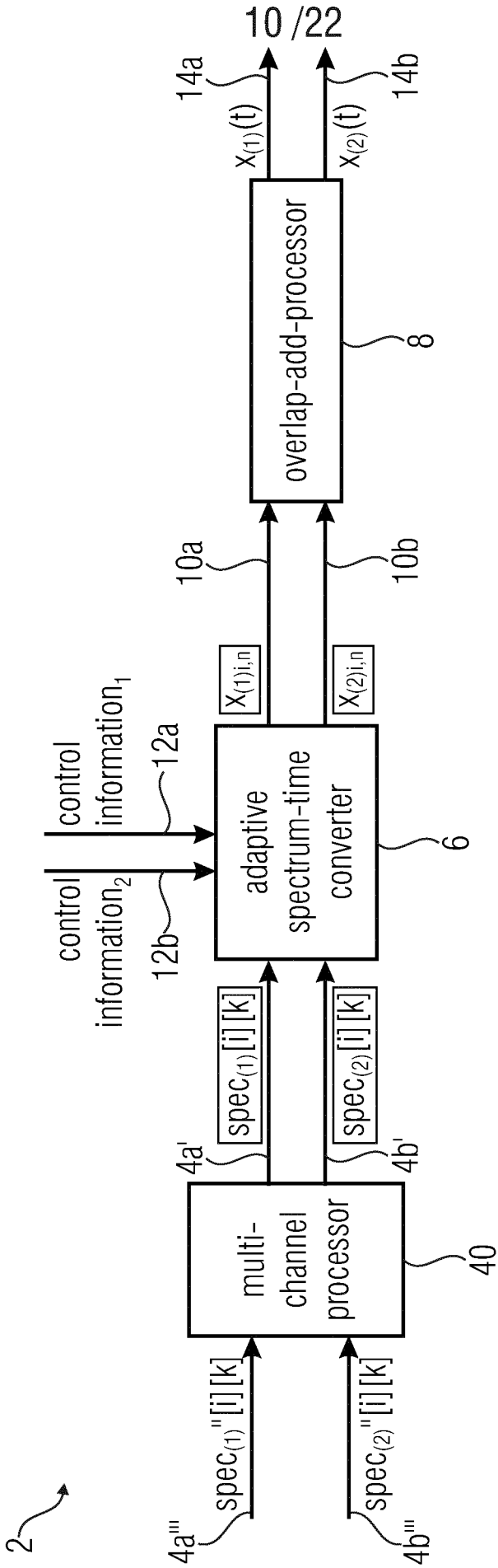


FIG 8

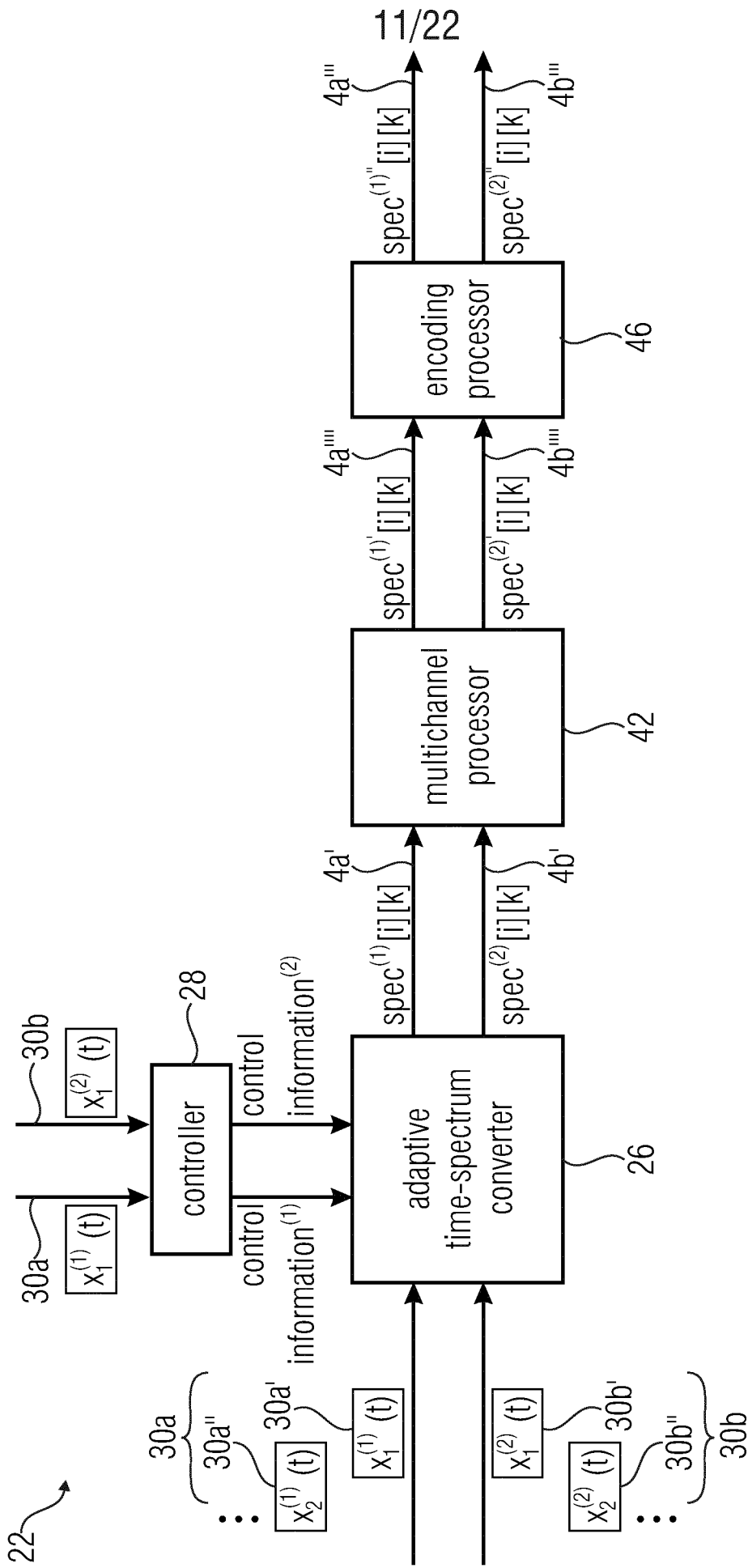


FIG 9

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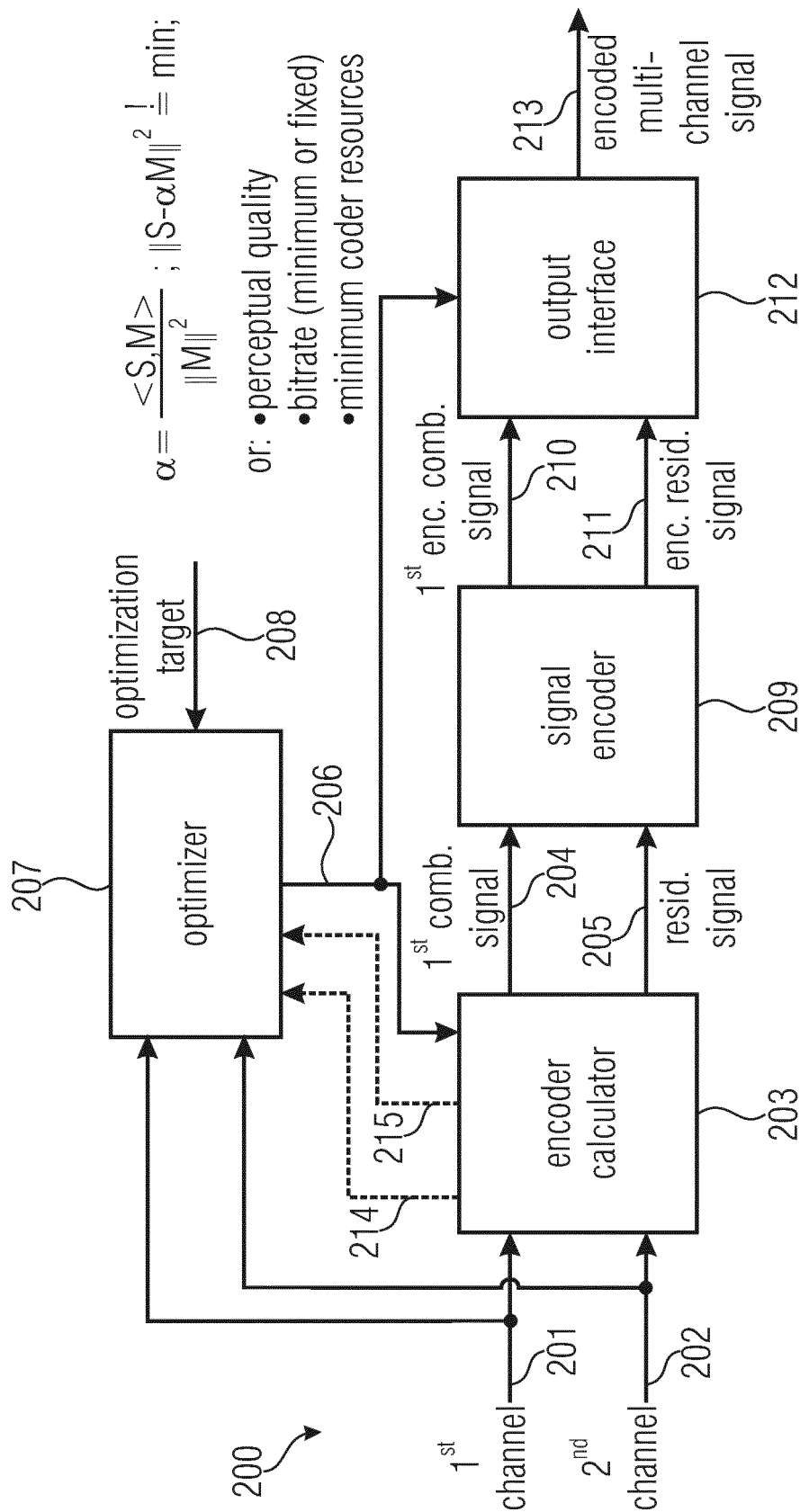


FIG 10
(AUDIO ENCODER)

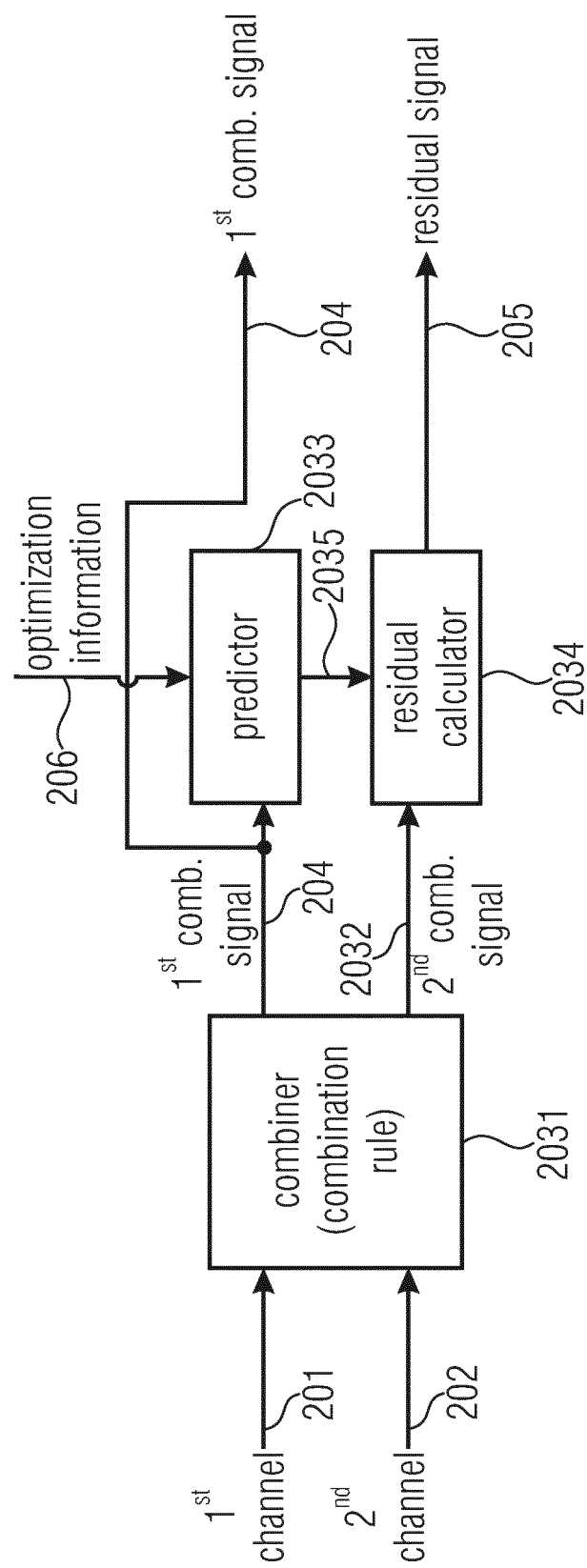


FIG 11A
(AUDIO ENCODER SIDE)

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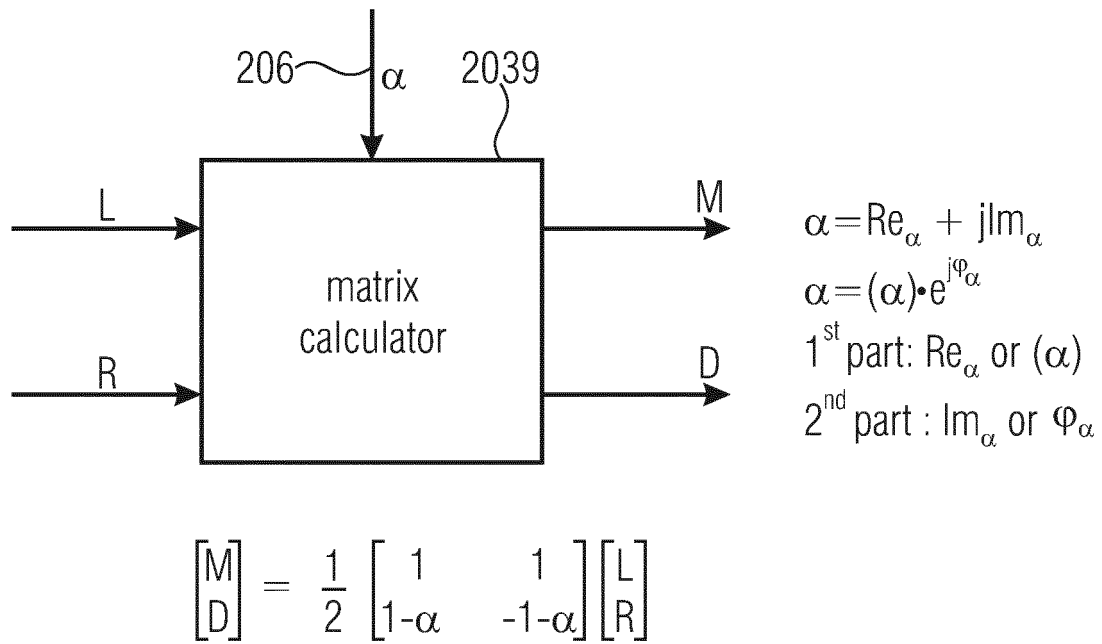


FIG 11B
(AUDIO ENCODER SIDE)

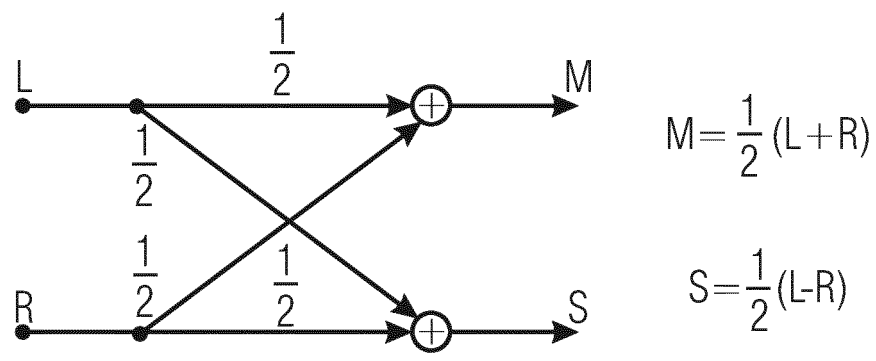


FIG 11C
(COMBINATION RULE)

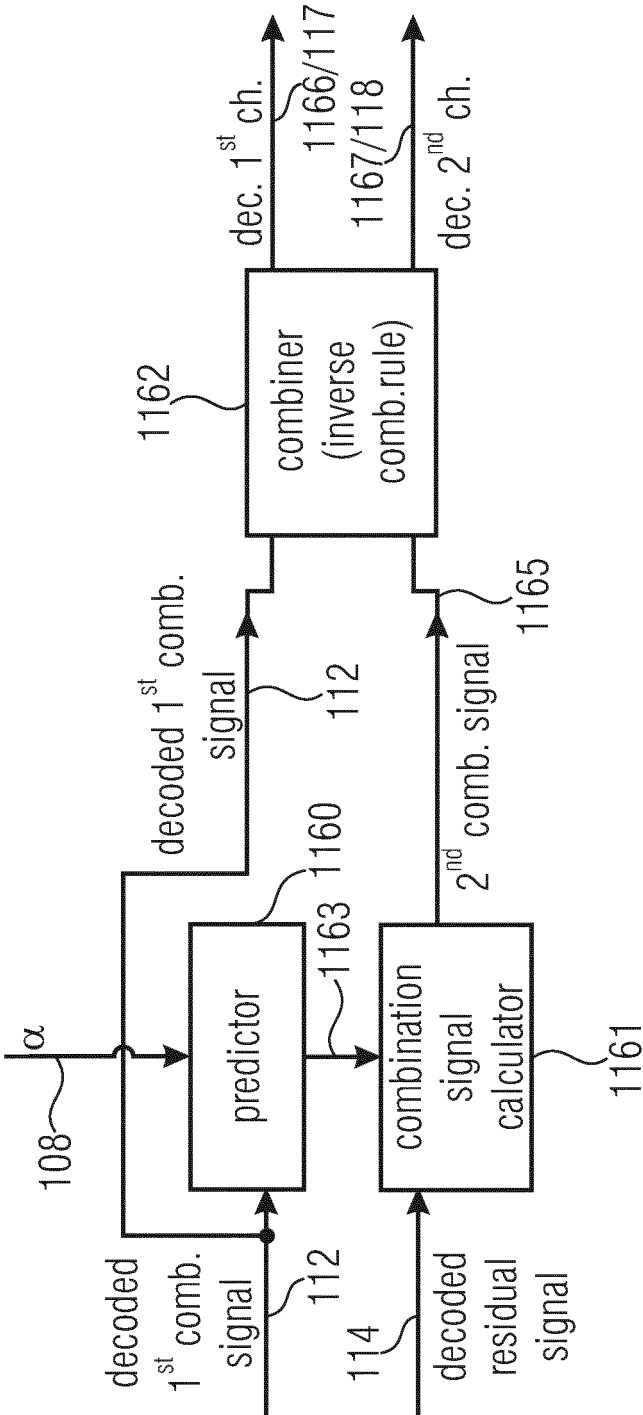


FIG 12A
(AUDIO DECODER SIDE)

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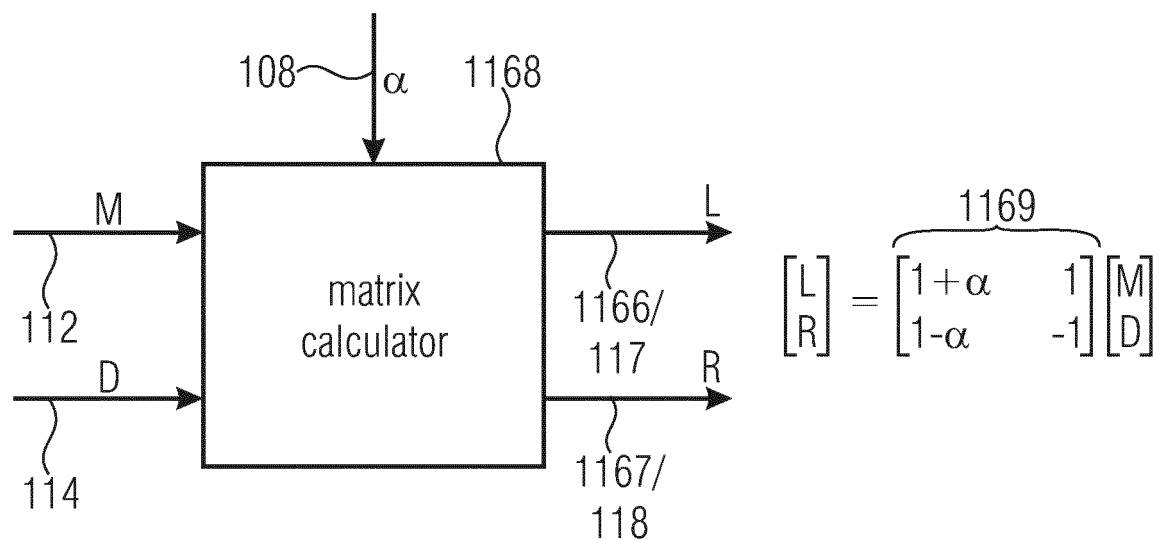


FIG 12B
(AUDIO DECODER SIDE)

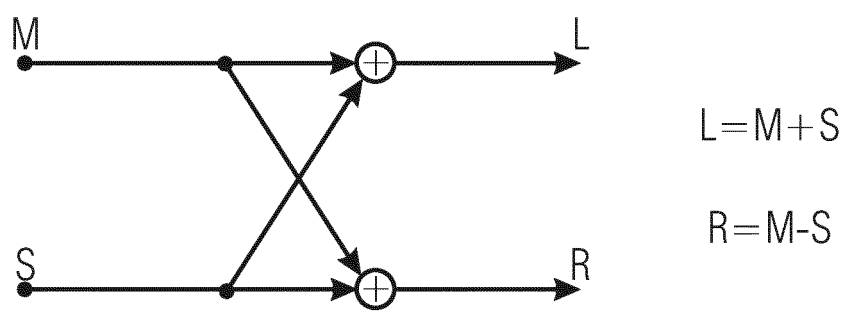


FIG 12C
(INVERSE COMBINATION RULE)

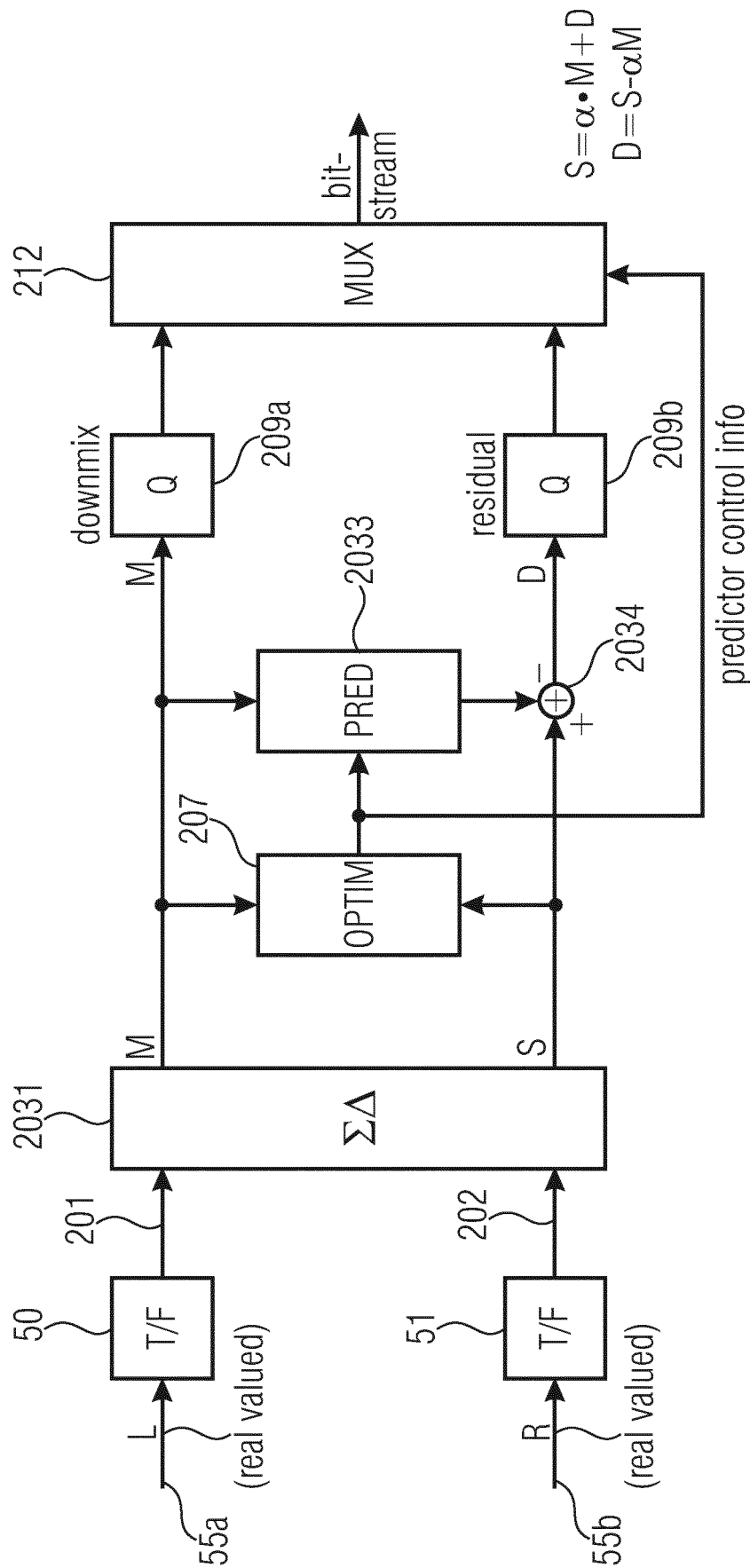


FIG 13A
(ENCODER SIDE)

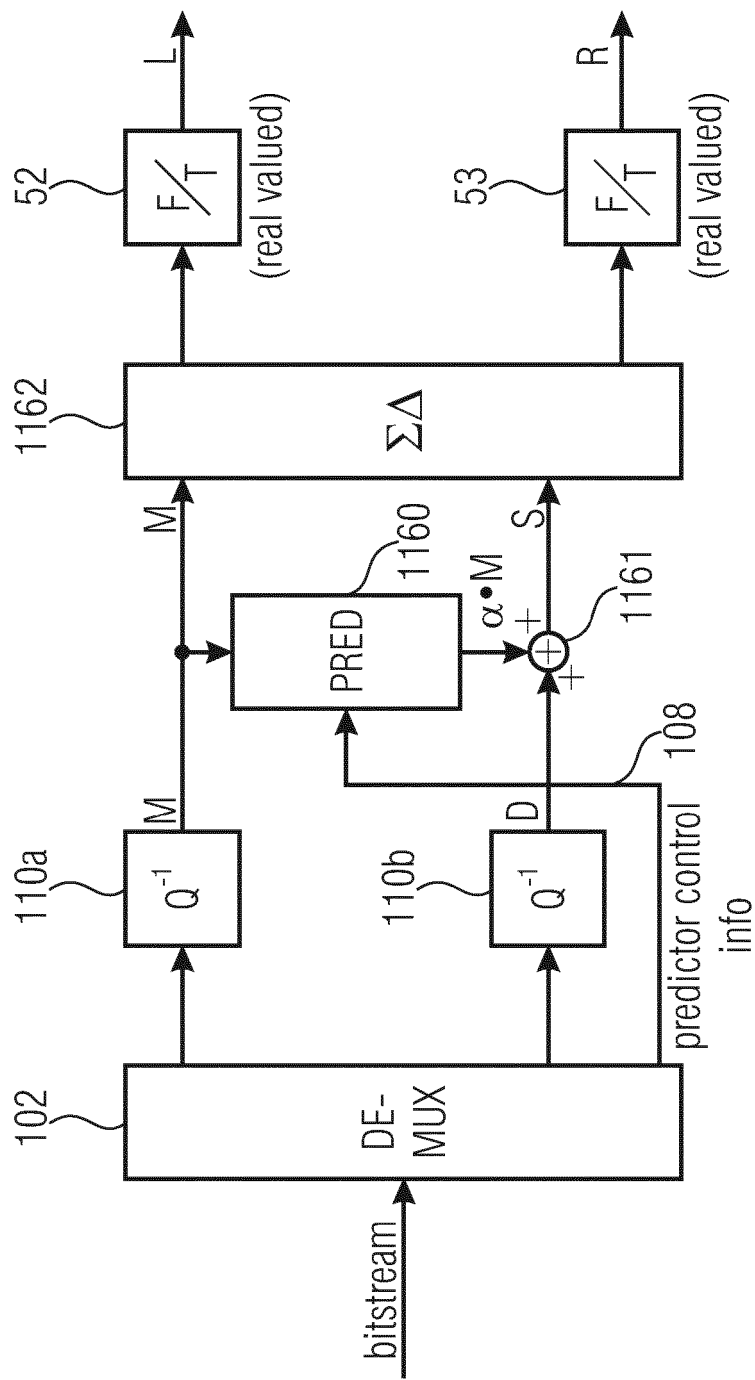


FIG 13B
(DECODER SIDE)

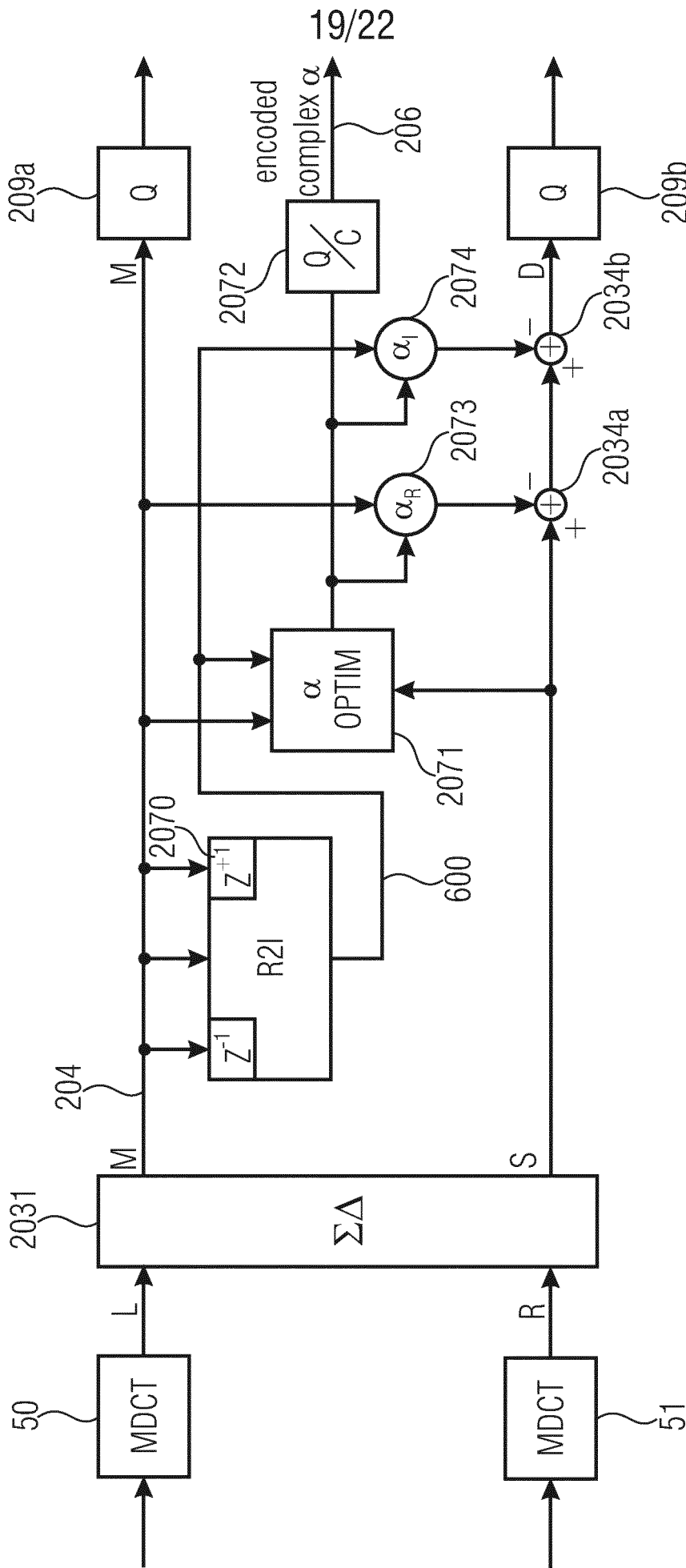


FIG 14A
(ENCODER SIDE)

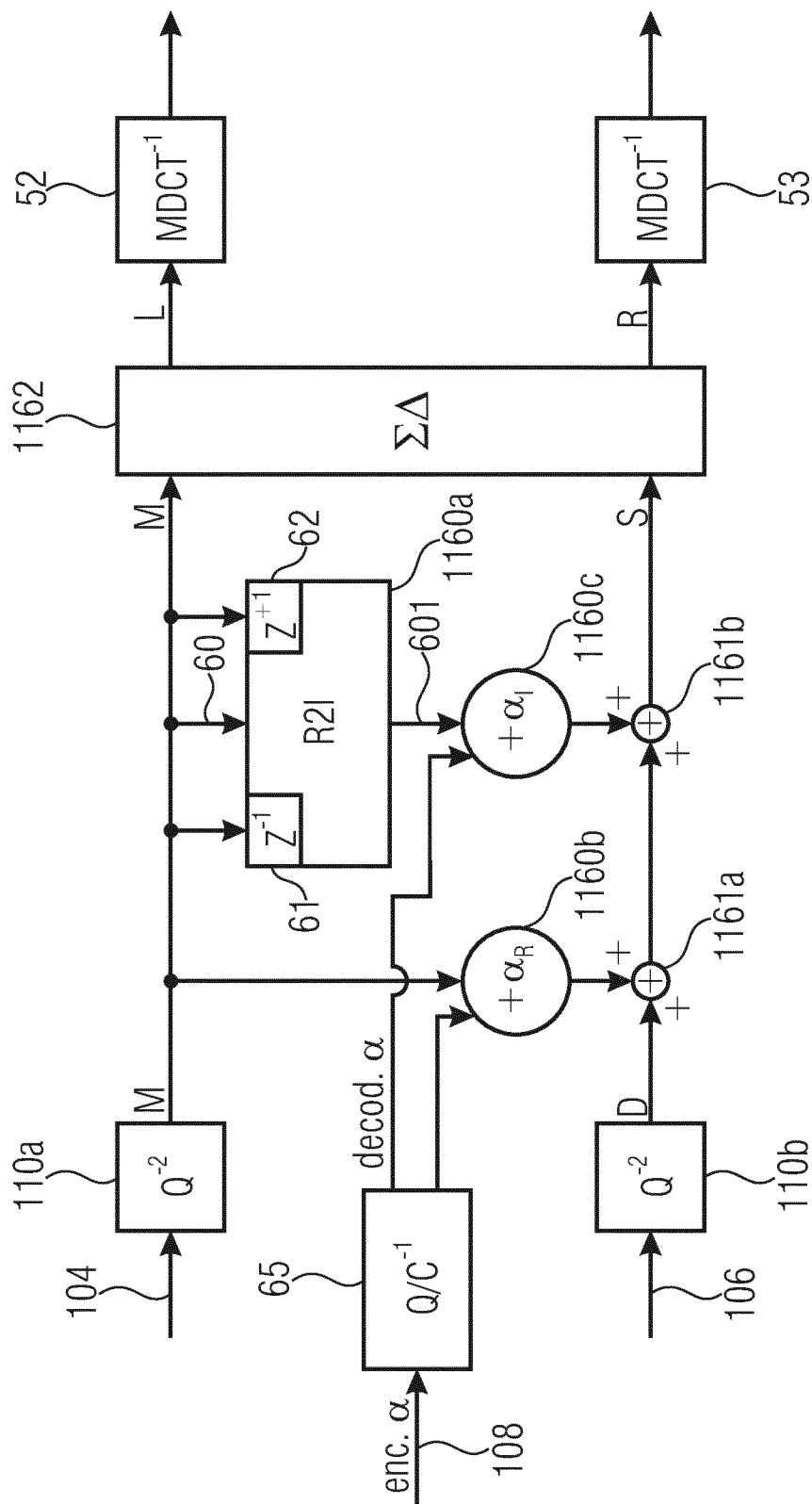


FIG 14B
(DECODER SIDE)

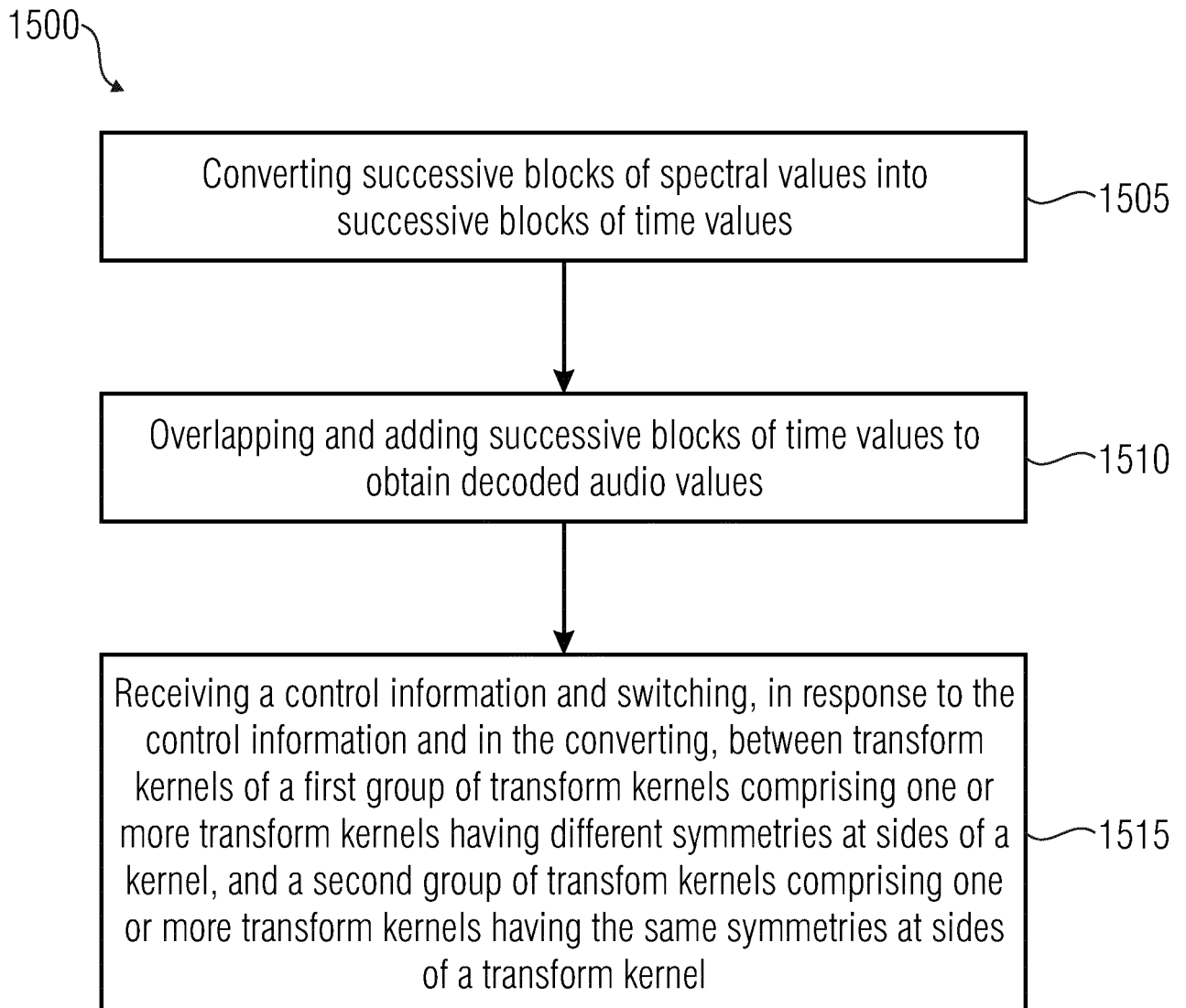


FIG 15

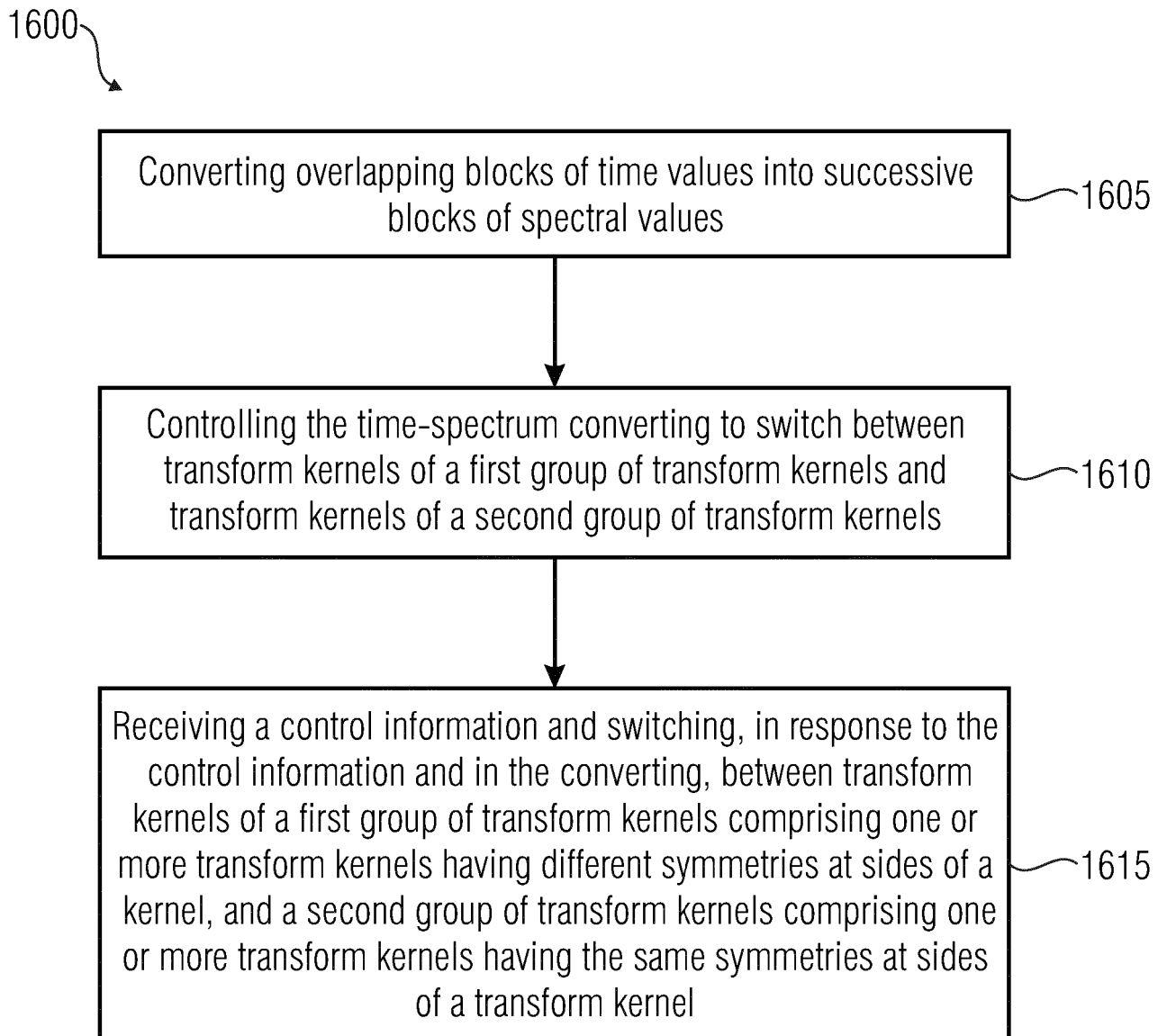


FIG 16