

VOLTAGE VARIATION COMPENSATION ARRANGEMENT FOR SAMPLE AND HOLD CAPACITOR

ABSTRACT

An analog multiplier (110) generates an output signal (OUT) that is coupled to a convergence winding (RH) of a projection television. The analog multiplier multiplies a horizontal rate sawtooth signal (HSAW) with a vertical rate parabola signal (VPAL1). The multiplier DC operating point is adjusted, by developing a DC voltage (VCAL) in a hold capacitor (C11) of a sample and hold arrangement during each vertical blanking interval. To prevent a change in the capacitor voltage during vertical trace, a vertical rate sawtooth signal (VSAW) is coupled to the hold capacitor via a second capacitor (CCOM).

SINGLE FIGURE

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