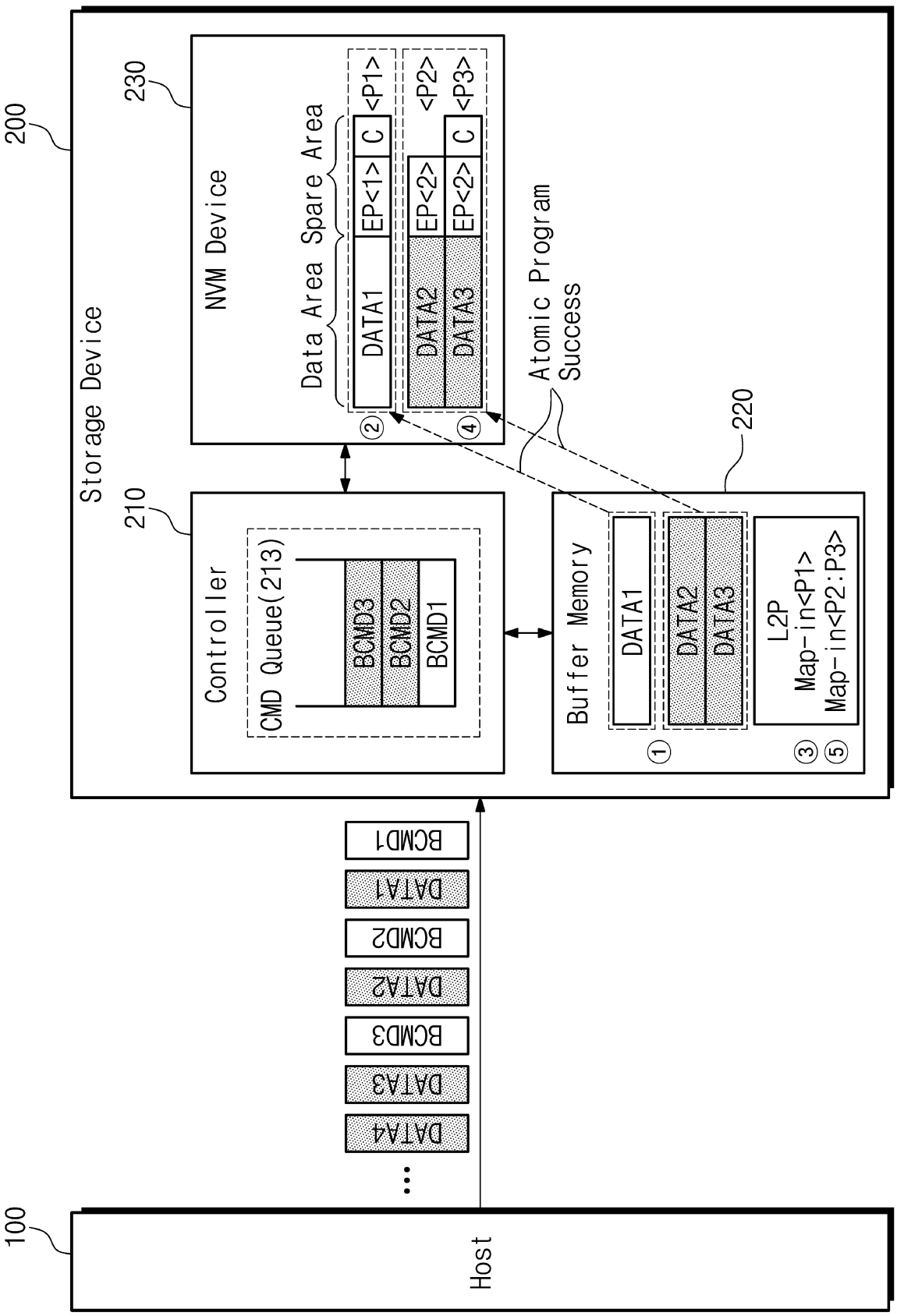
FIG. 12


10
100
Host
200
Storage Device
210
Controller
CMD Queue(213)
BCMD3
BCMD2
BCMD1
230
NVM Device
Data Area Spare Area
DATA1
EP<1>
C
<P1>
DATA2
EP<2>
<P2>
DATA3
EP<2>
C
<P3>
Atomic Program Success
220
Buffer Memory
DATA1
DATA2
DATA3
L2P
Map-in<P1>
Map-in<P2:P3>
①
②
③
④
⑤
BCMD1
DATA1
BCMD2
DATA2
BCMD3
DATA3
DATA4