

ABSTRACT OF DISCLOSURE

LATCH-UP ROBUST SCR-BASED DEVICES

An approach for providing a latch-up robust silicon control rectifier (SCR) is disclosed. Embodiments include providing a first N⁺ region and a first P⁺ region in a substrate for a SCR; providing first and second n-well regions in the substrate proximate the first N⁺ and P⁺ regions; providing a second N⁺ region in the first n-well region, and a second P⁺ region in the second n-well region; and coupling the first N⁺ and P⁺ regions to a ground rail, the second N⁺ region to a power rail, and the second P⁺ region to an I/O pad.

Fig. 4A