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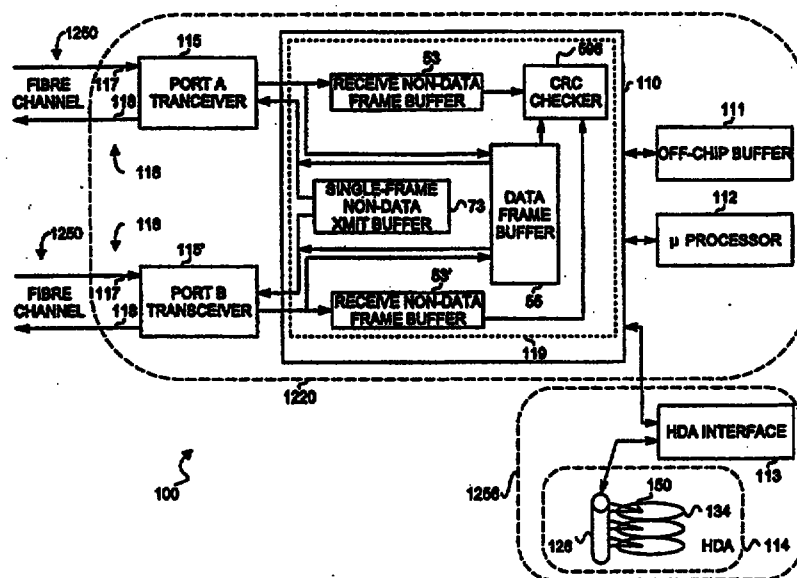
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(54) Title: METHOD AND APPARATUS FOR USING CRC FOR DATA INTEGRITY IN ON-CHIP MEMORY



(57) Abstract

An improved communications channel system and method therefor is described. The communications channel system includes a channel node (1220) having dual ports (116), each supporting a fibre-channel arbitrated-loop communications channel (1250). A frame including cyclic-redundancy-code (CRC), received from a fibre-channel loop (1250), is stored in an on-chip (110) buffer (119) and checked to ensure frame integrity by an integrity apparatus (596) when the frame is read from the buffer (119). In various embodiments, data frames with their CRC are stored in a data-frame buffer (55), and/or non-data frames with their CRC are stored in a non-data-frame buffer (53 or 53').