



A MULTIPLE OUTPUT CURRENT MIRROR

ABSTRACT OF THE DISCLOSURE

A multiple output current mirror comprising at least three mirror-connected PNP transistors whose bases are connected to a first node, at least three cascode-connected transistors, each cascode transistor being associated to one mirror transistor, a current input corresponding to the collector of the first cascode transistor, and mirror outputs corresponding to the collectors of the two other cascode transistors. The current mirror further comprising means for detecting the base current of each mirror transistor and for reproducing this base current on the collector of the cascode transistor to which each mirror transistor is associated.

THIS SHEET HAS BEEN REPLACED
