

**A METHOD AND SYSTEM FOR DETERMINING WHICH MEMORY
LOCATIONS HAVE BEEN ACCESSED IN A SELF TIMED
CACHE ARCHITECTURE**

ABSTRACT

The present invention provides a method and system for providing observability of memory address access for self-timed cache designs. A system according to the present invention for determining which memory location has been accessed in a self-timed cache comprises a content addressable memory (300); a secondary memory (302) coupled to the content addressable memory (300), wherein the secondary memory (302) includes at least one memory location which may be selected by the content addressable memory (300) based upon a self-timed cache access. The system further includes a test circuitry (304) coupled to the content addressable memory (300), wherein the test circuitry (304) stores a pointer which points to a selected memory location in response to the self-timed cache access of the secondary memory.

Fig. 3