

FAULT PROTECTION USING MICROPROCESSOR POWER UP RESET**Abstract of the Disclosure**

The microprocessor controller (22) of a television receiver controls run mode power supplies coupled to the horizontal deflection
5 circuits (44) to define a standby mode and a run mode. The microprocessor has a power-up reset input terminal (pin 33) for initializing operation after power is applied at the AC mains. At least one of the run mode loads (44) is subject to overcurrent or overvoltage faults. A fault detection circuit (28) detects a fault
10 condition such as overvoltage or overcurrent, and triggers a power up reset circuit (26) which applies a momentary reset pulse to the power-up reset input (pin 33) of the microprocessor. Upon receiving the reset pulse, the microprocessor places the television receiver into standby, thereby removing power from the run
15 mode load subject to the overvoltage or overcurrent condition.

Figure 1

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This Sheet Has Been Replaced

