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ABSTRACT OF THE DISCLOSURE

The present invention is directed to compensating the $\sin(x)/x$ response attendant the output signals from a Digital-To-Analog converter (57) in systems consisting of the cascade connection of a digital processor (56) having a non-uniform frequency response $h(nT)$ and a Digital-To-Analog converter. This is accomplished by altering (53,54) the processor to provide a frequency response of $h(nT)*g(nT)$, where $h(nT)$ is the impulse response of the processor, $g(nT)$ is the impulse response of the reciprocal of the frequency response of the DAC, and the asterisk represents convolution. Alternatively, if the Fourier transform of the frequency response of the processor is given by $F(\omega)$, and the DAC exhibits a frequency response $S(\omega)$, which is the frequency transform of the time response of the DAC sample and hold type output, then the processor is substituted with one having a frequency response defined by the inverse Fourier transform $IF(F(\omega)/S(\omega))$ over the frequency band of interest.

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