ABSTRACT OF THE DISCLOSURE

TESTING STORAGE SYSTEM ELECTRONICS USING LOOPBACK

A system includes a hard disk controller (HDC) module that controls a hard disk and a read channel (RC) device that communicates with the HDC module via a read bus and a write bus. The RC device includes a loopback circuit that selectively loops back the write bus to the read bus. The RC device generates a write clock for the HDC module to write data on the write bus and a read clock for the HDC module to read the data on the read bus, wherein the write clock is independent of the read clock.

FIG. 3



162162



G00002