

1/6

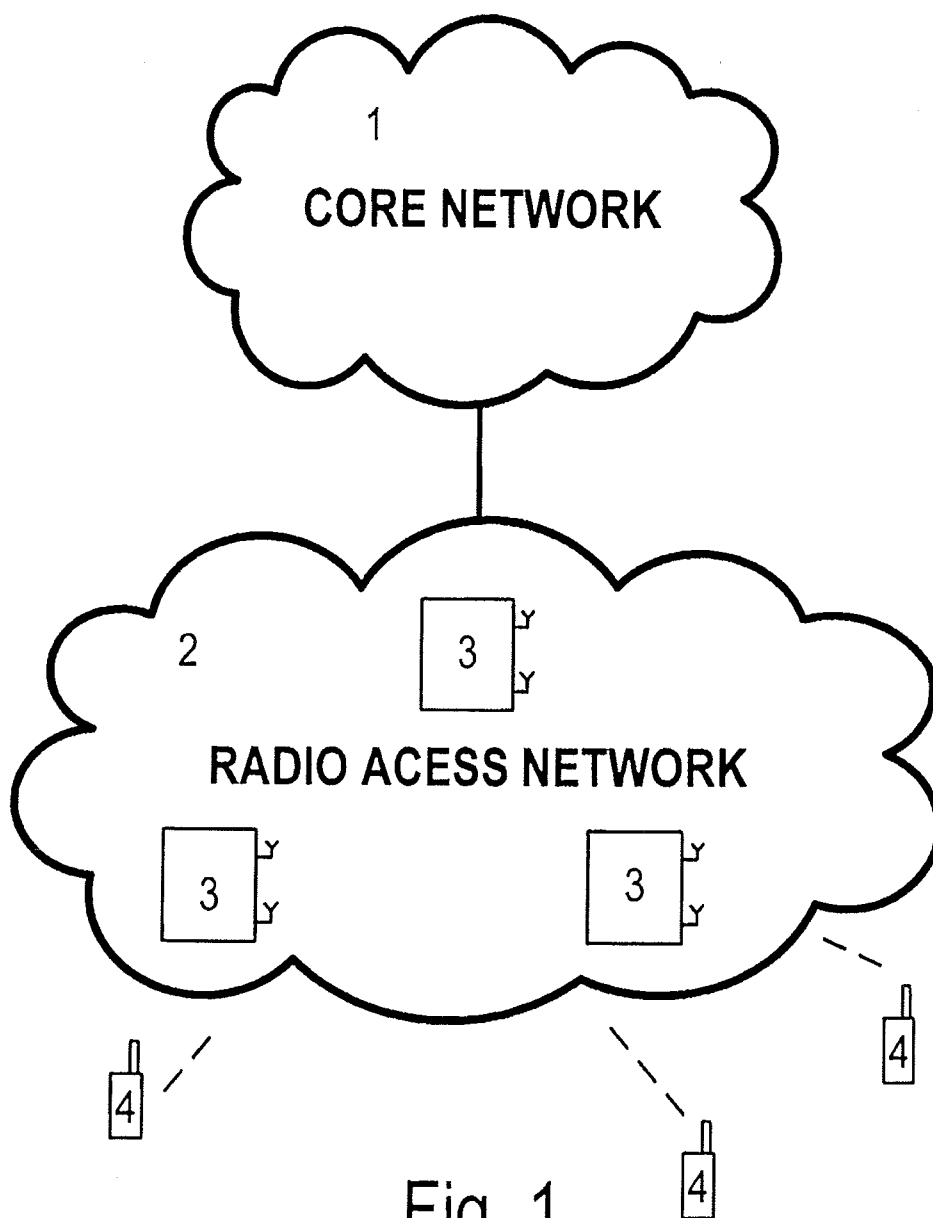


Fig. 1

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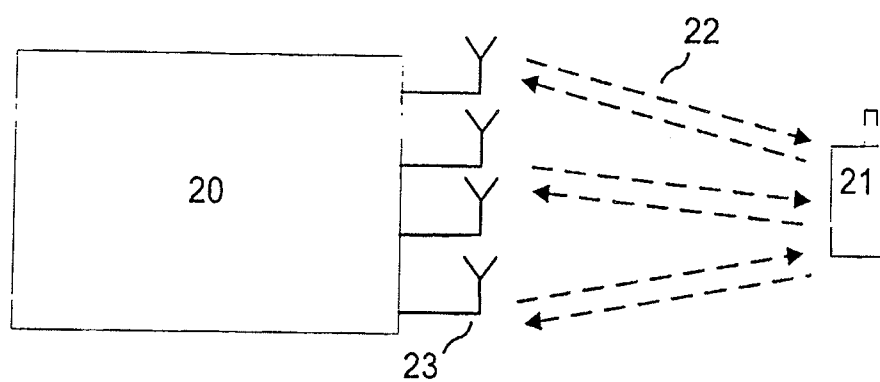


Fig. 2

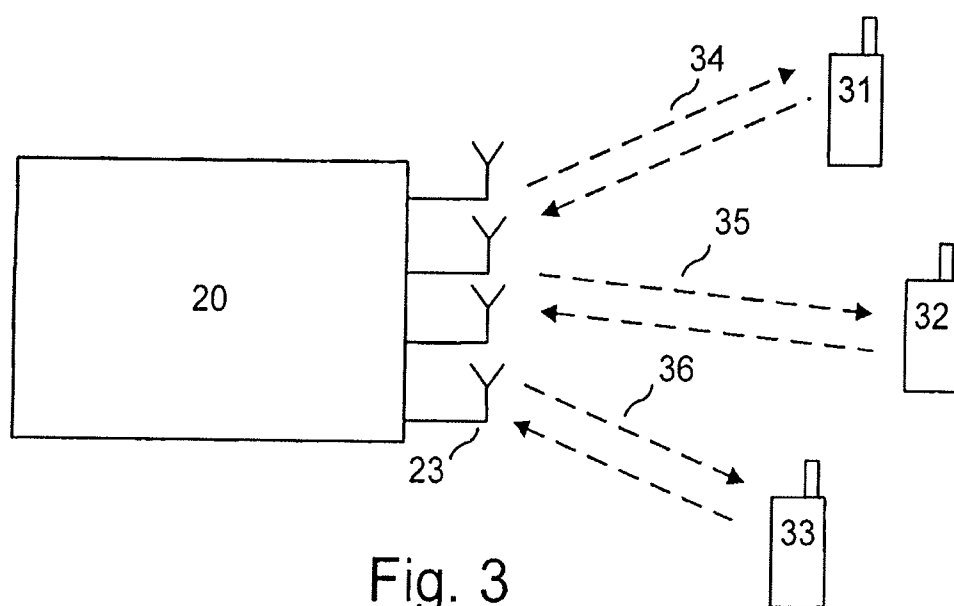
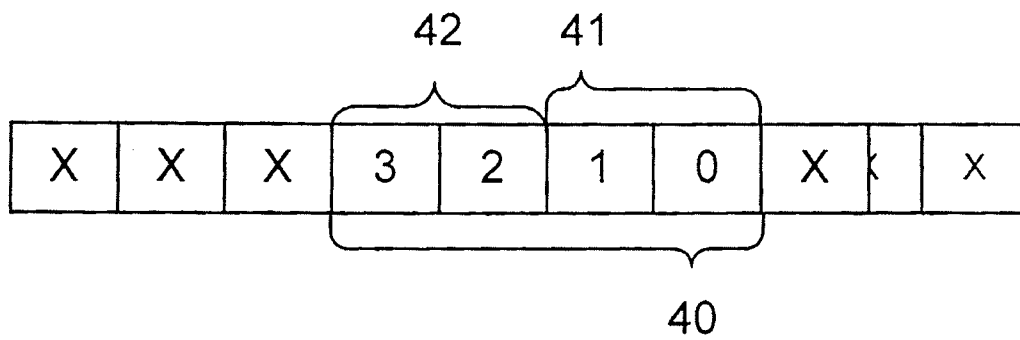


Fig. 3

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40: Bits reserved for SU-MIMO mode

41: Bits necessary for 2 Tx correlated SDMA
in MU-MIMO mode

42: Redundant bit for 2 Tx MU-MIMO mode

Fig. 4

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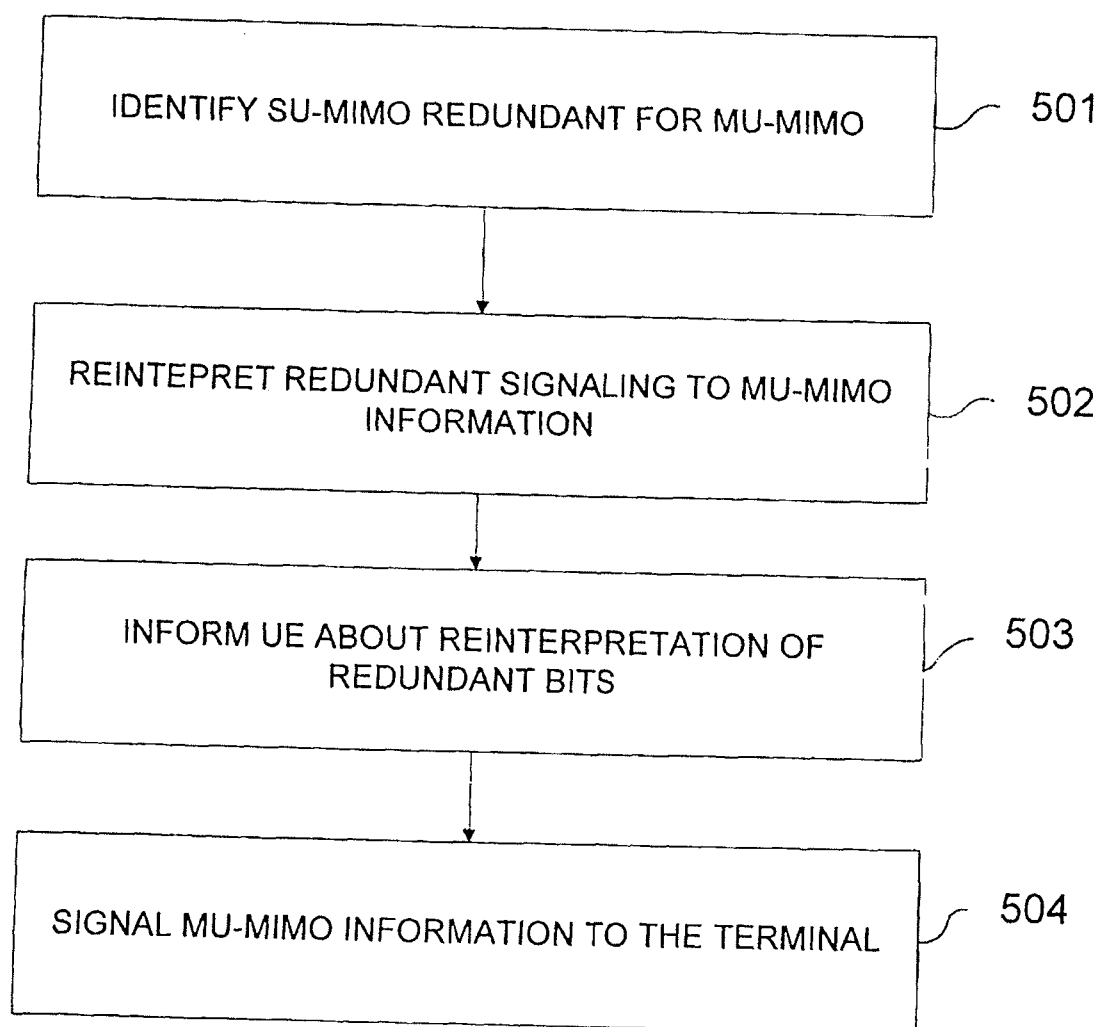


Fig. 5

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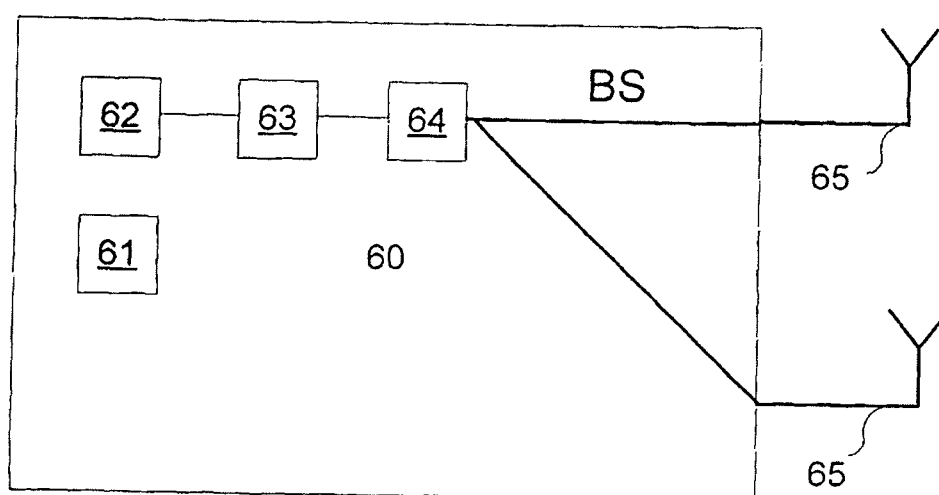


Fig. 6

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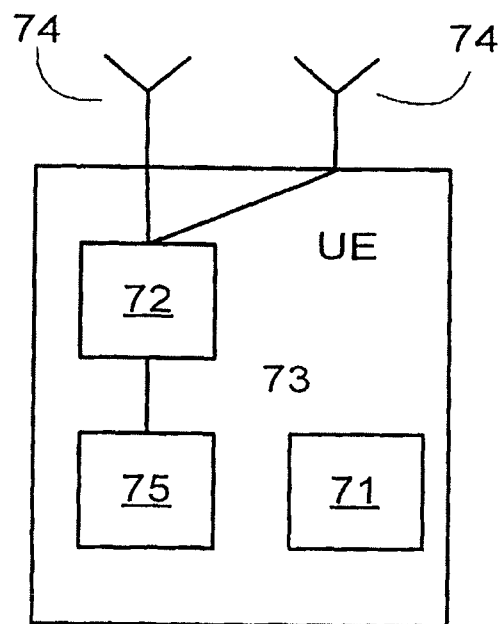


Fig. 7