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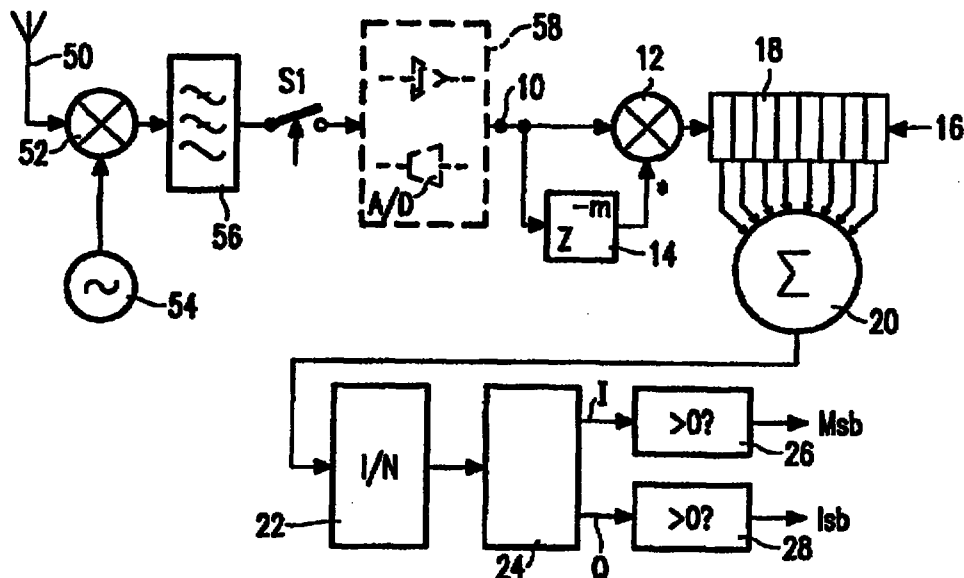
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(54) Title: RECEIVER FOR M-ary FSK SIGNALS



(57) Abstract

A method of, and receiver for, receiving and demodulating M -ary FSK symbols, where M equals two or four, comprising over sampling a received signal to obtain sub-symbols which are treated as repeated DBPSK $\pi/4$ DQPSK symbols, respectively. The sub-symbols are applied to a delay and multiply demodulator in which the duration of the delay has been optimised to give M equidistant points on a unit circle. The output from the demodulator comprises log likelihood ratios which are then integrated in an integrating filter to give a maximum likelihood estimate of the bits comprising the symbols transmitted.