

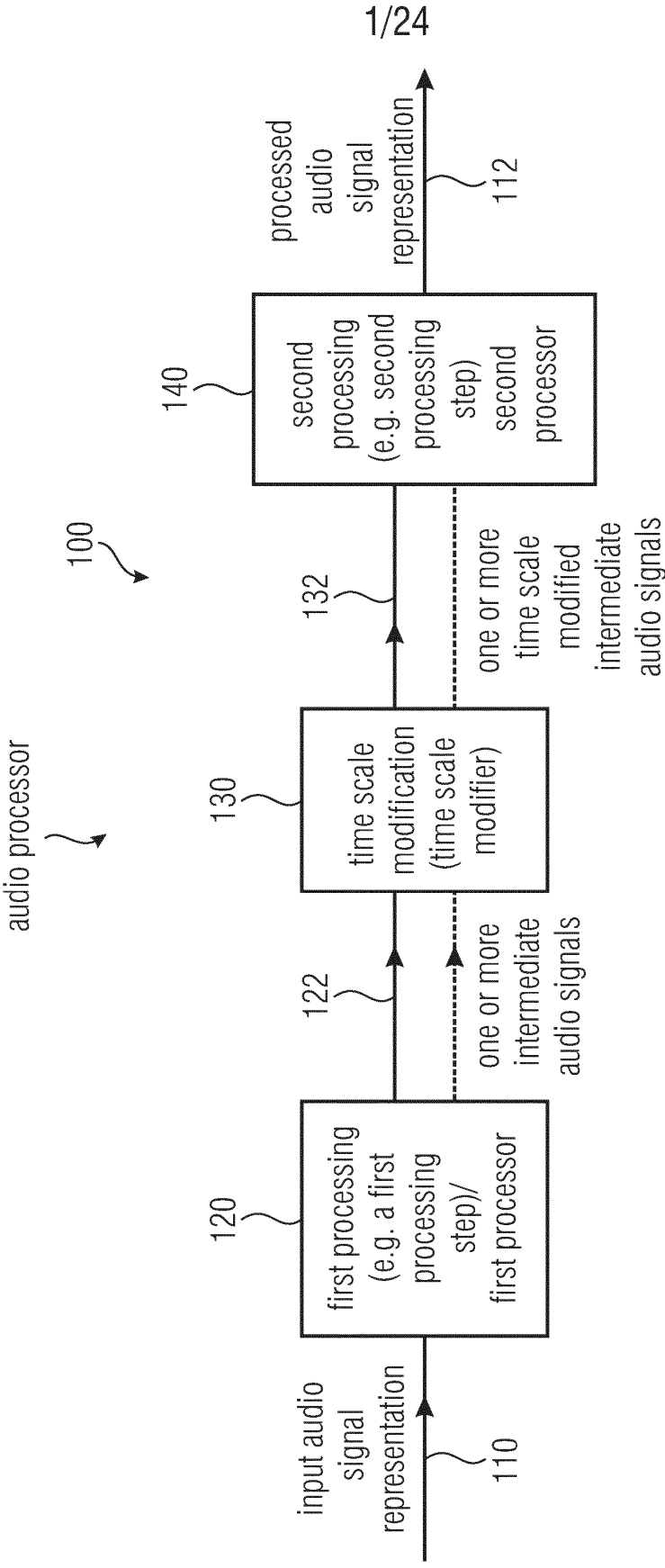


Fig. 1

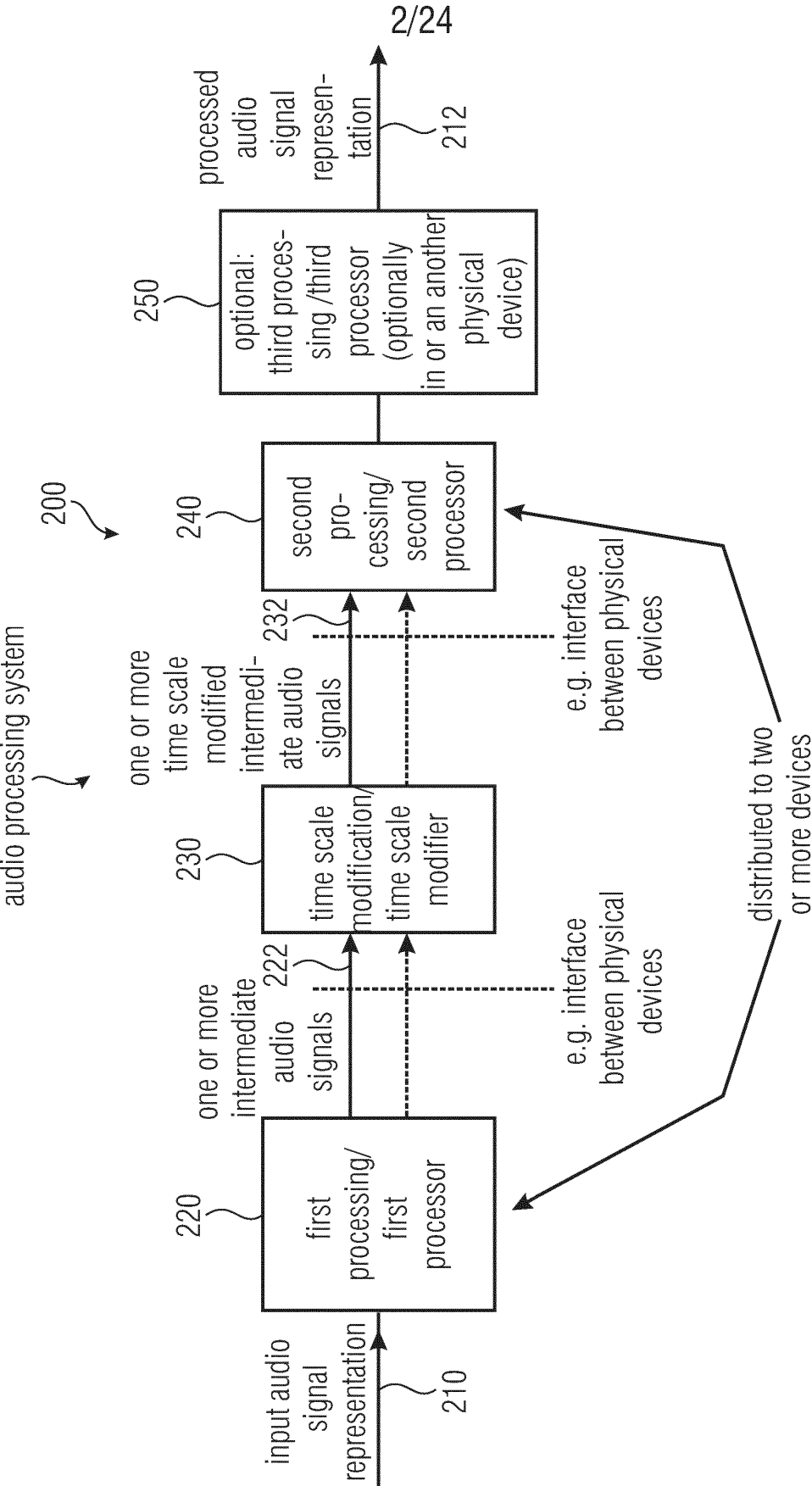


Fig. 2

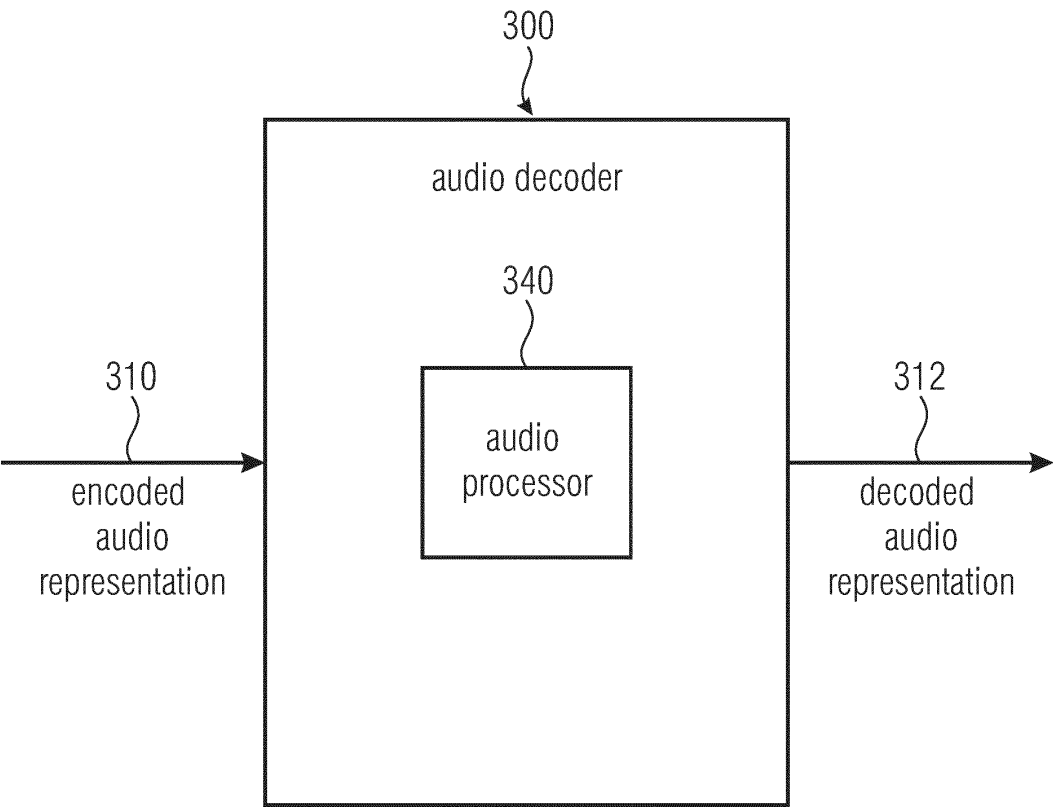


Fig. 3

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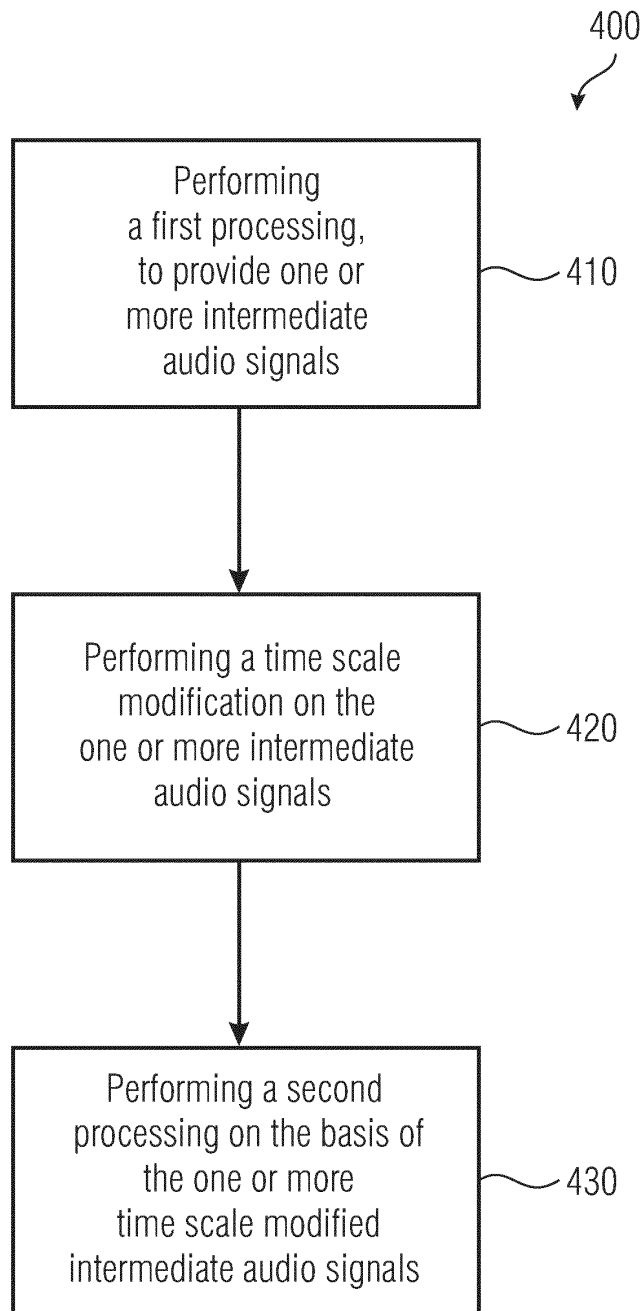
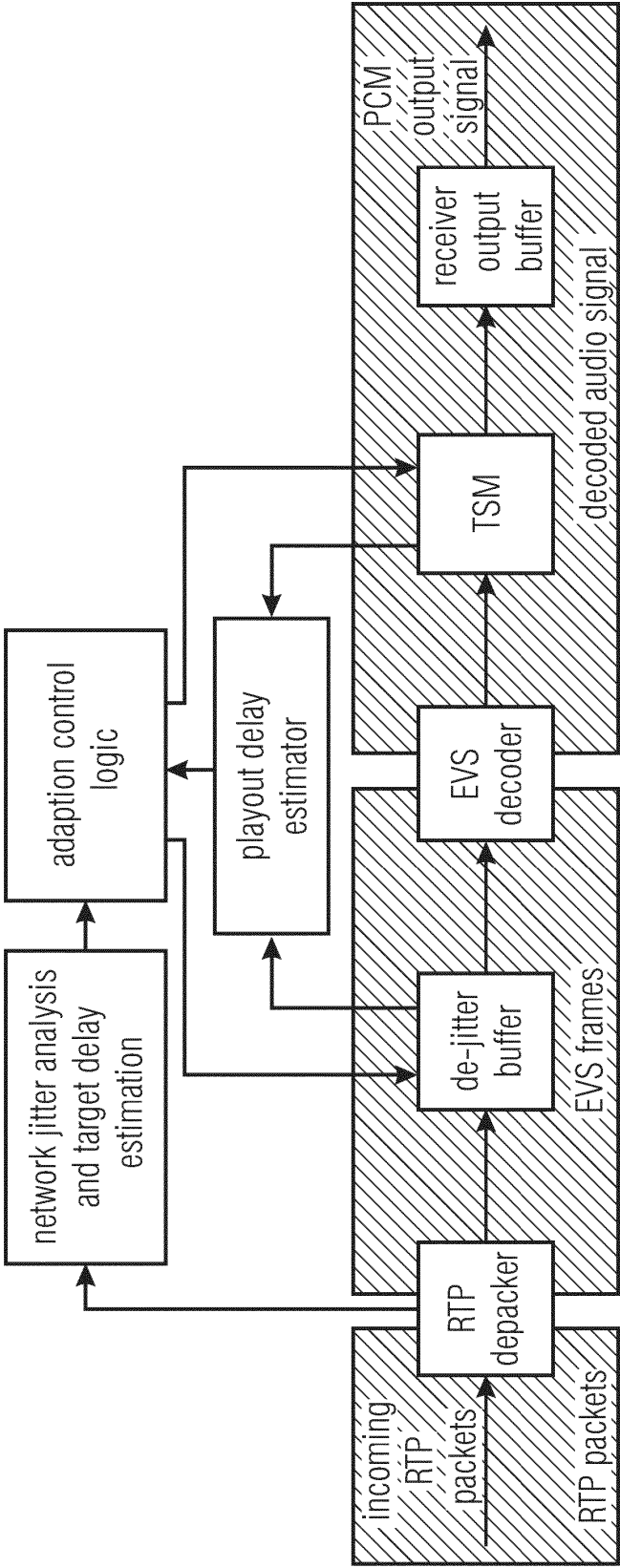
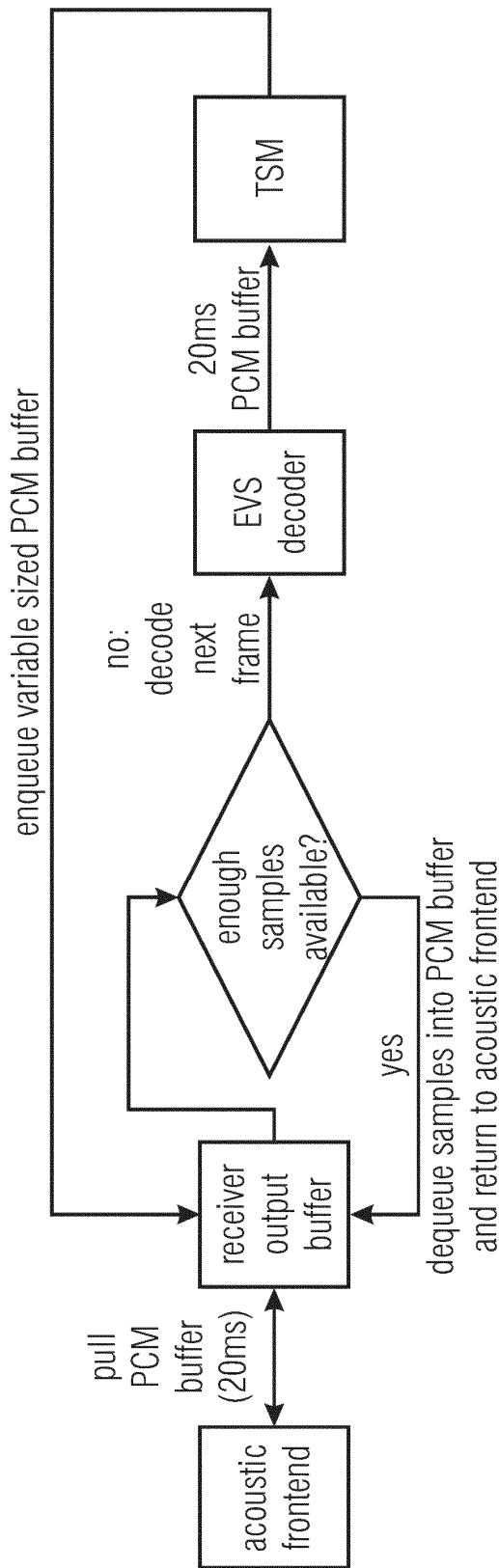


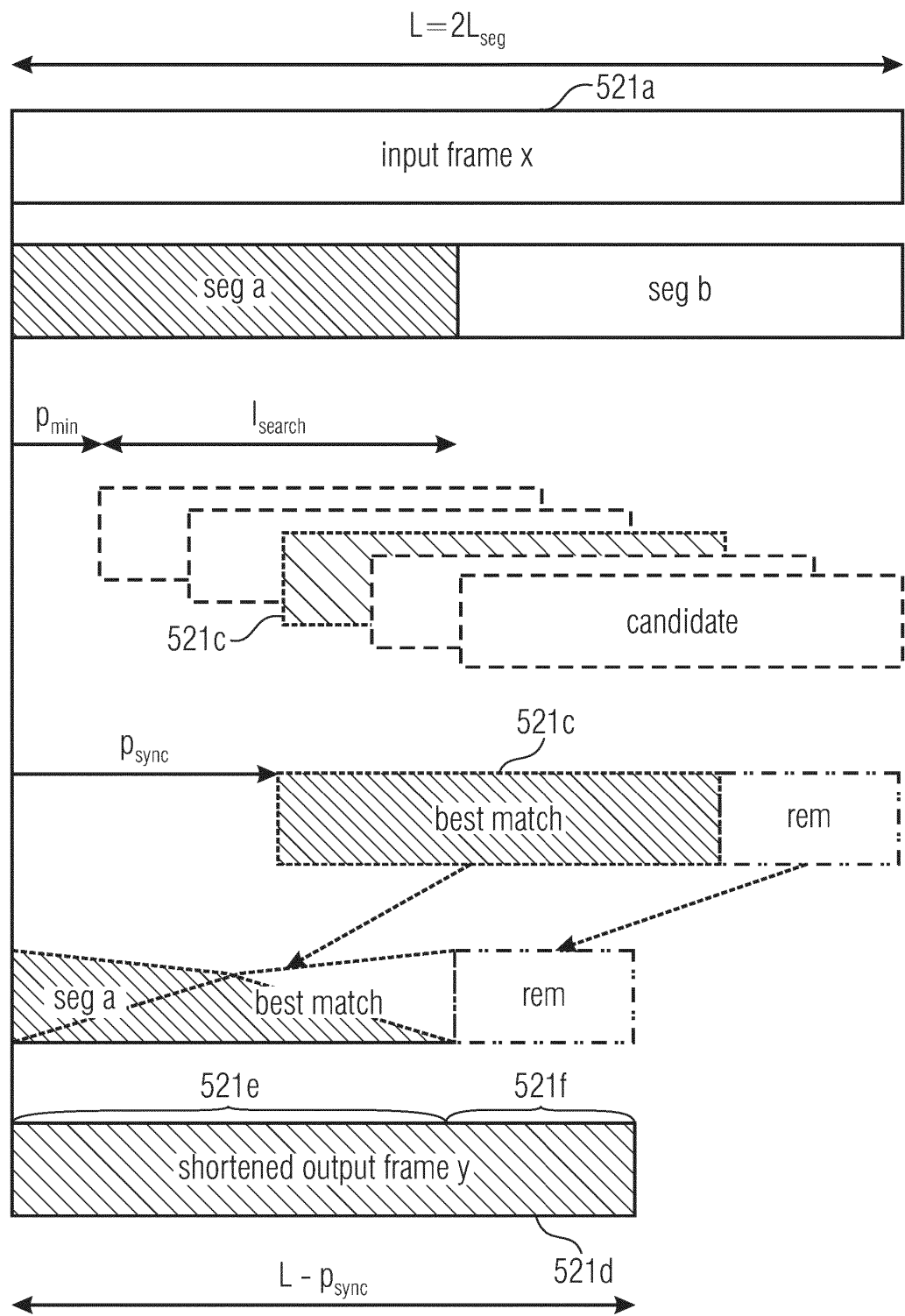
Fig. 4



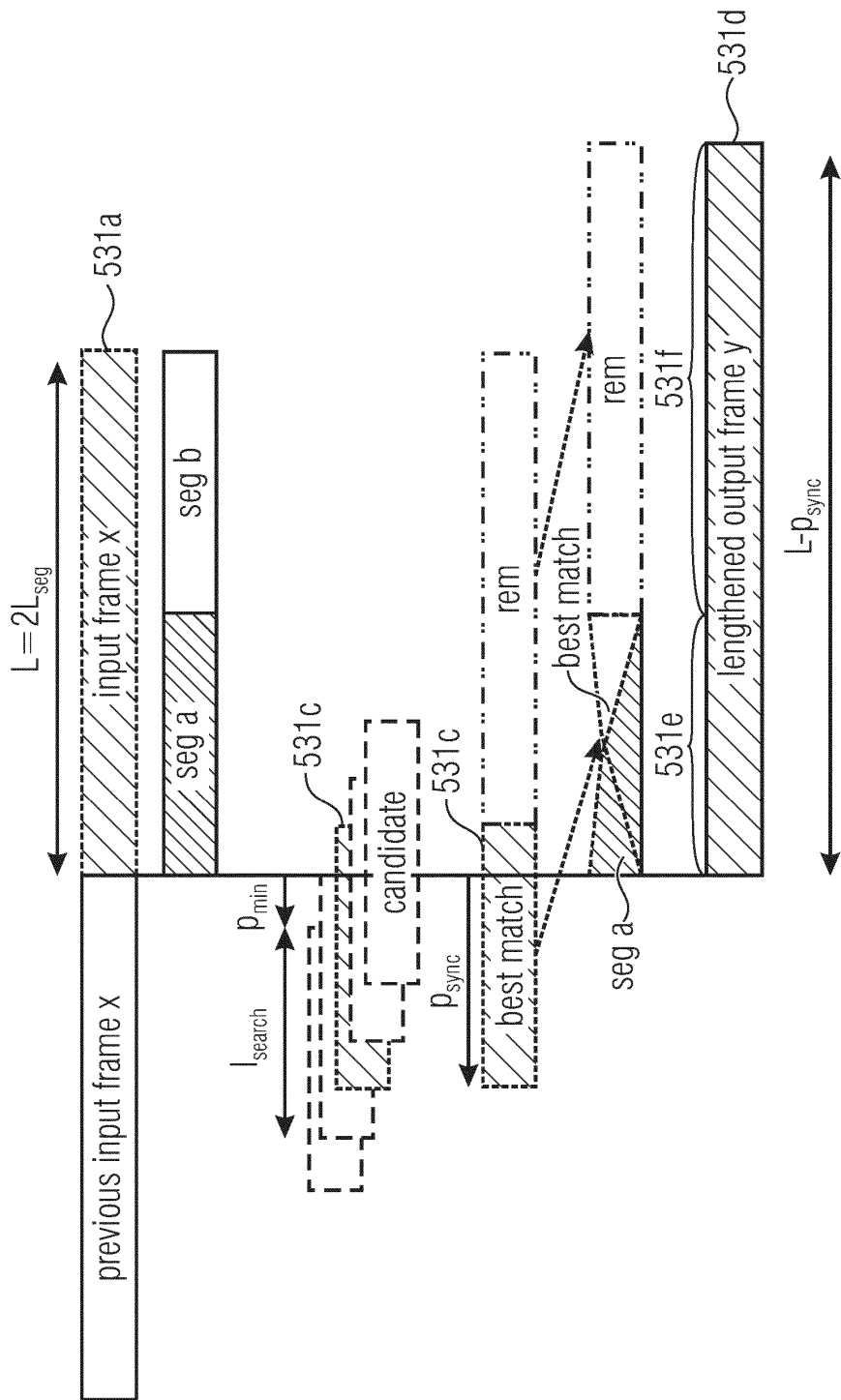
modules of the EVS jitter buffer management solution (Fig. 2 of [2])
Fig. 5a



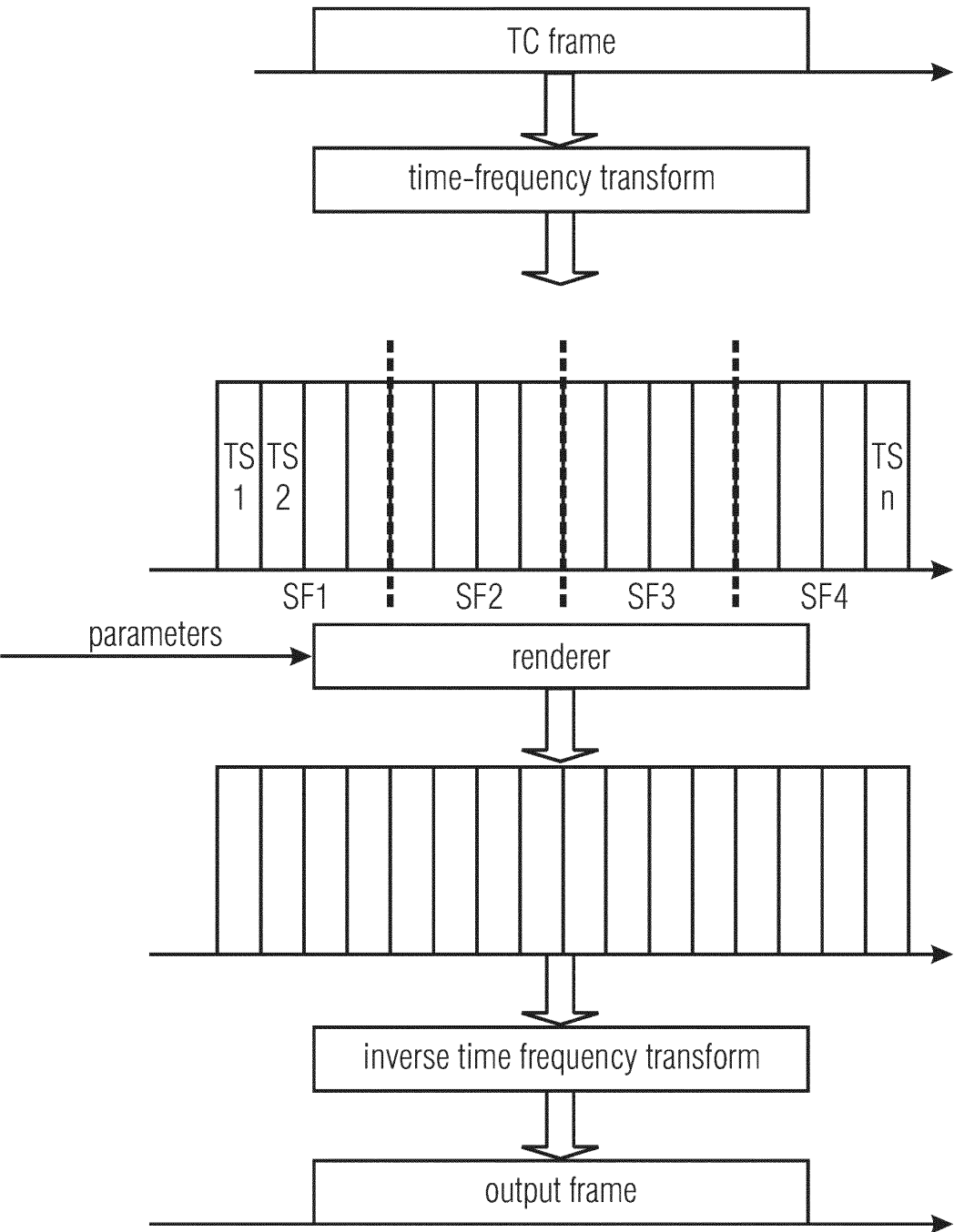
Receiver output buffer (Fig. 7 of [2])
Fig. 5b



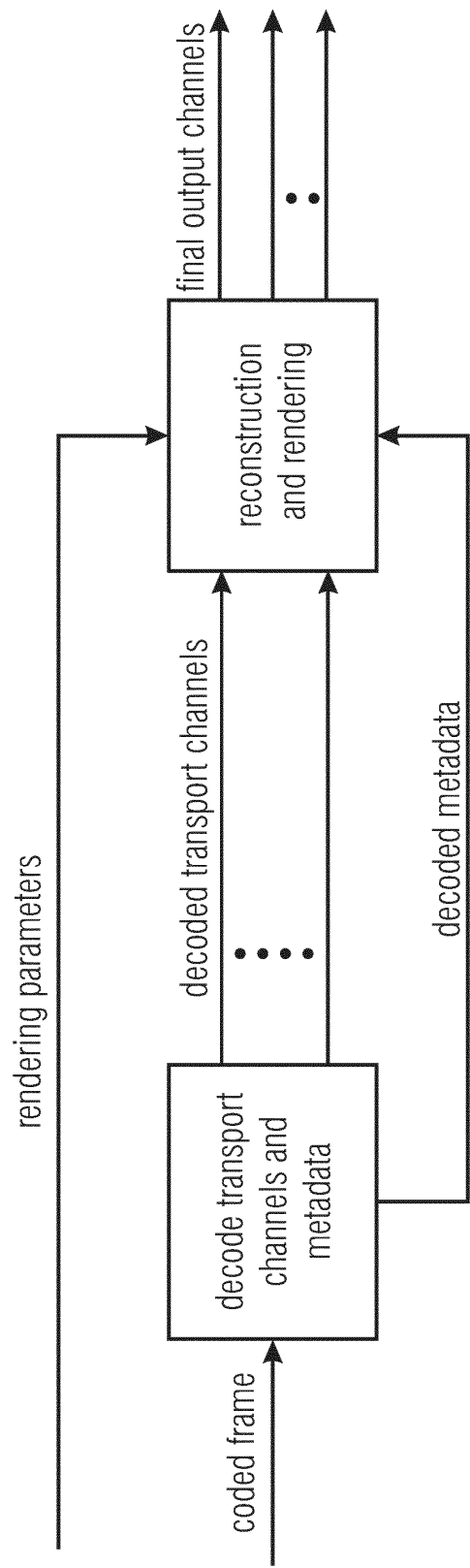
Shortening of an input frame (Fig. 5 of [2])
Fig. 5c



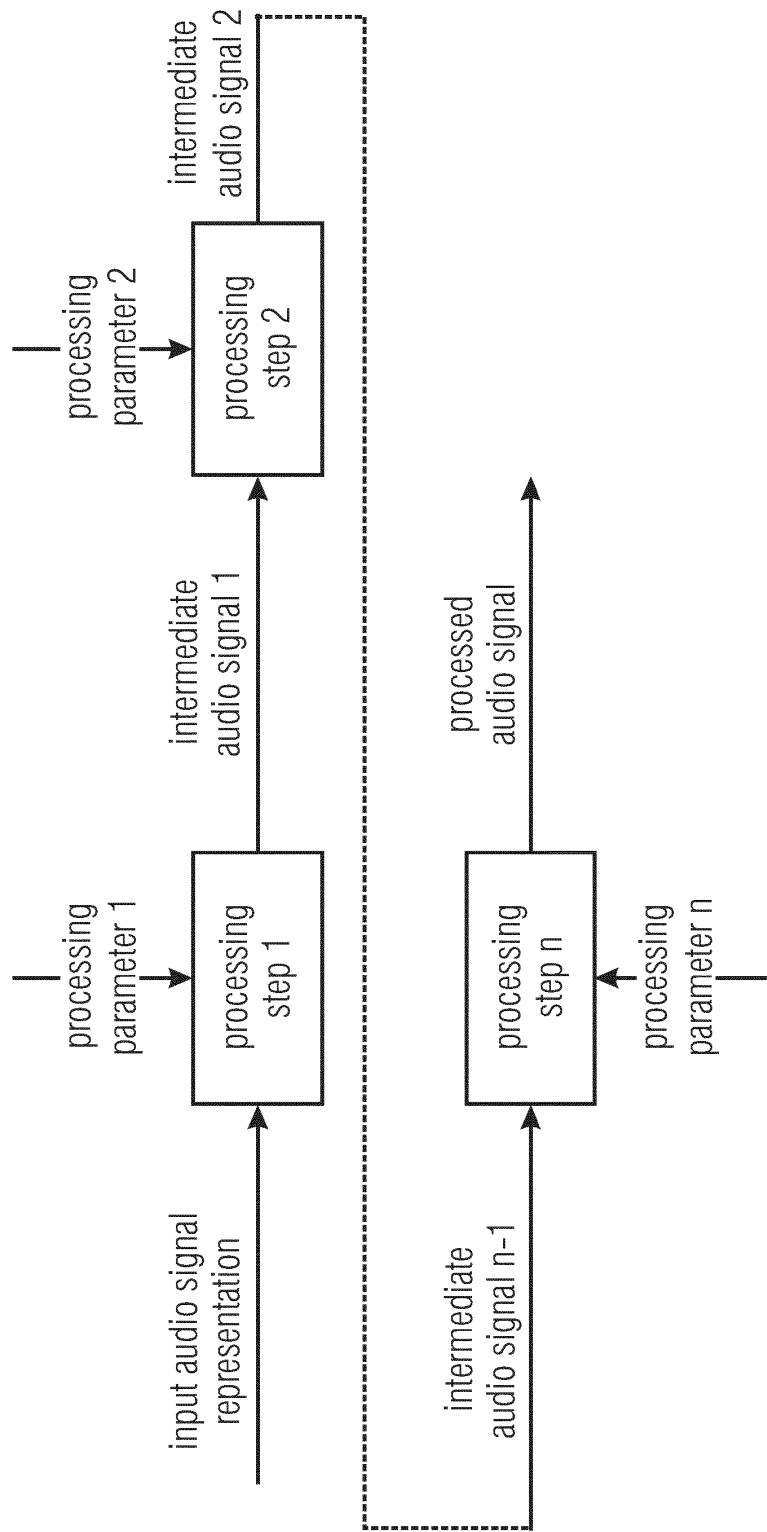
lengthening of an input frame (Fig. 6 of [2])
Fig. 5d



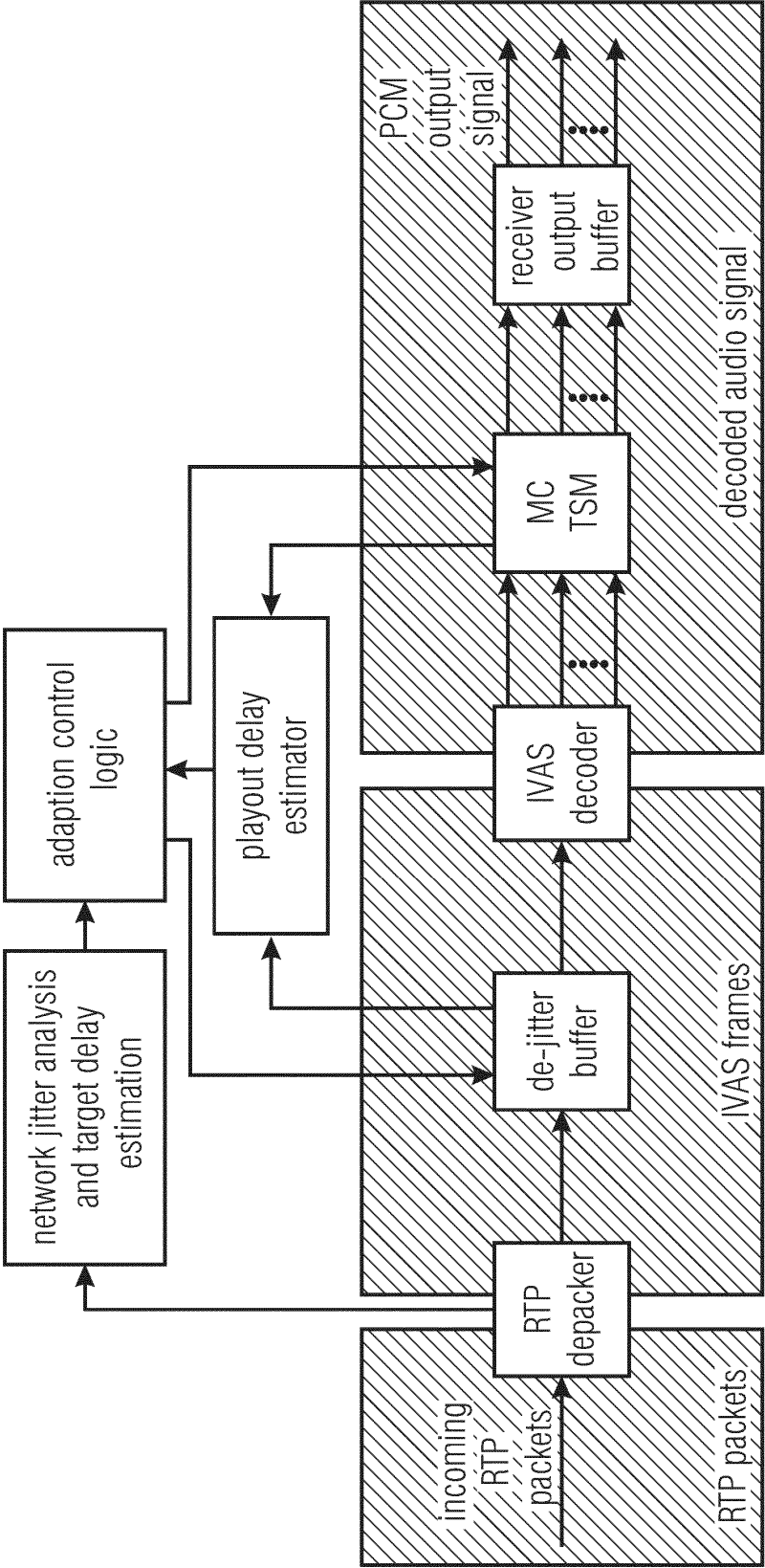
FD rendering with sub frames
Fig. 5e



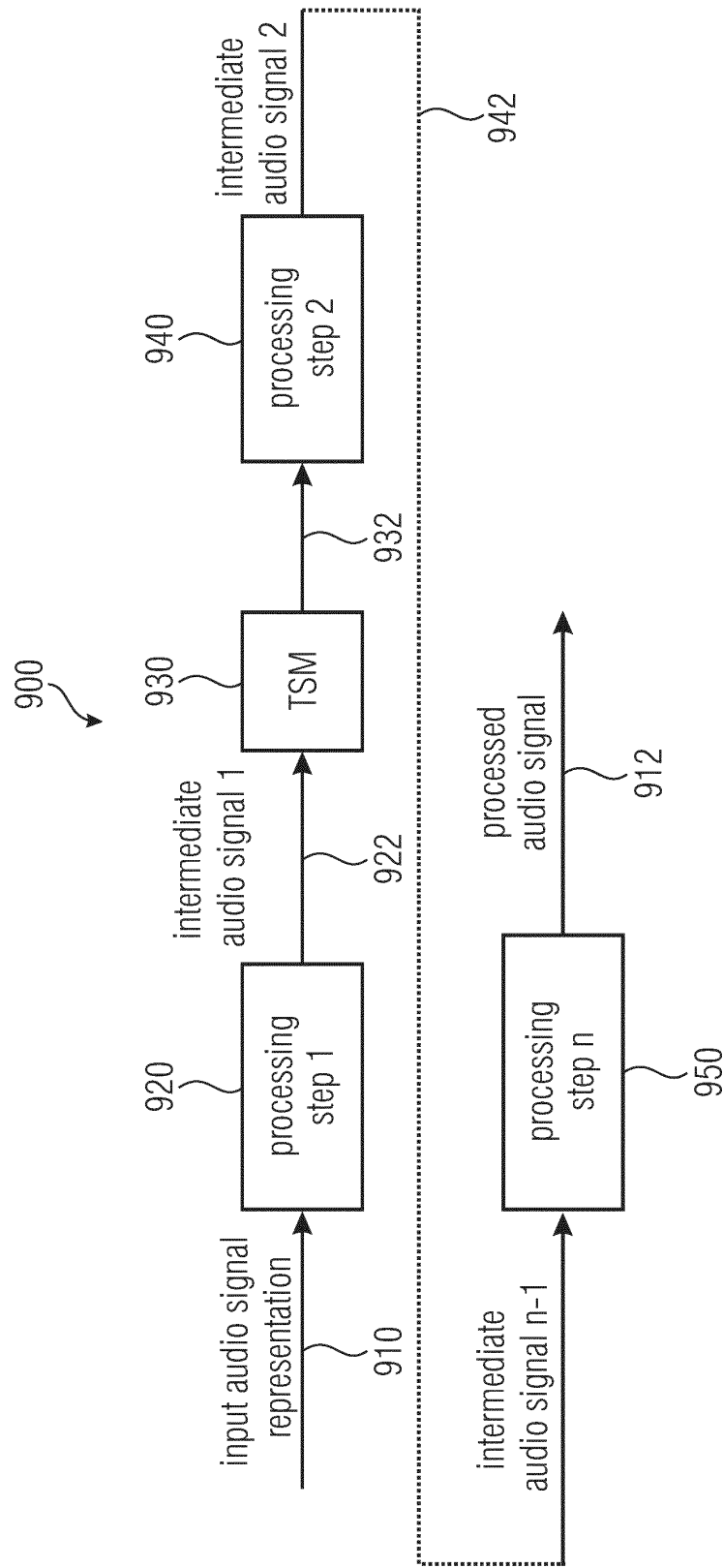
generic block diagram of IVAS with parametric reconstruction
Fig. 6



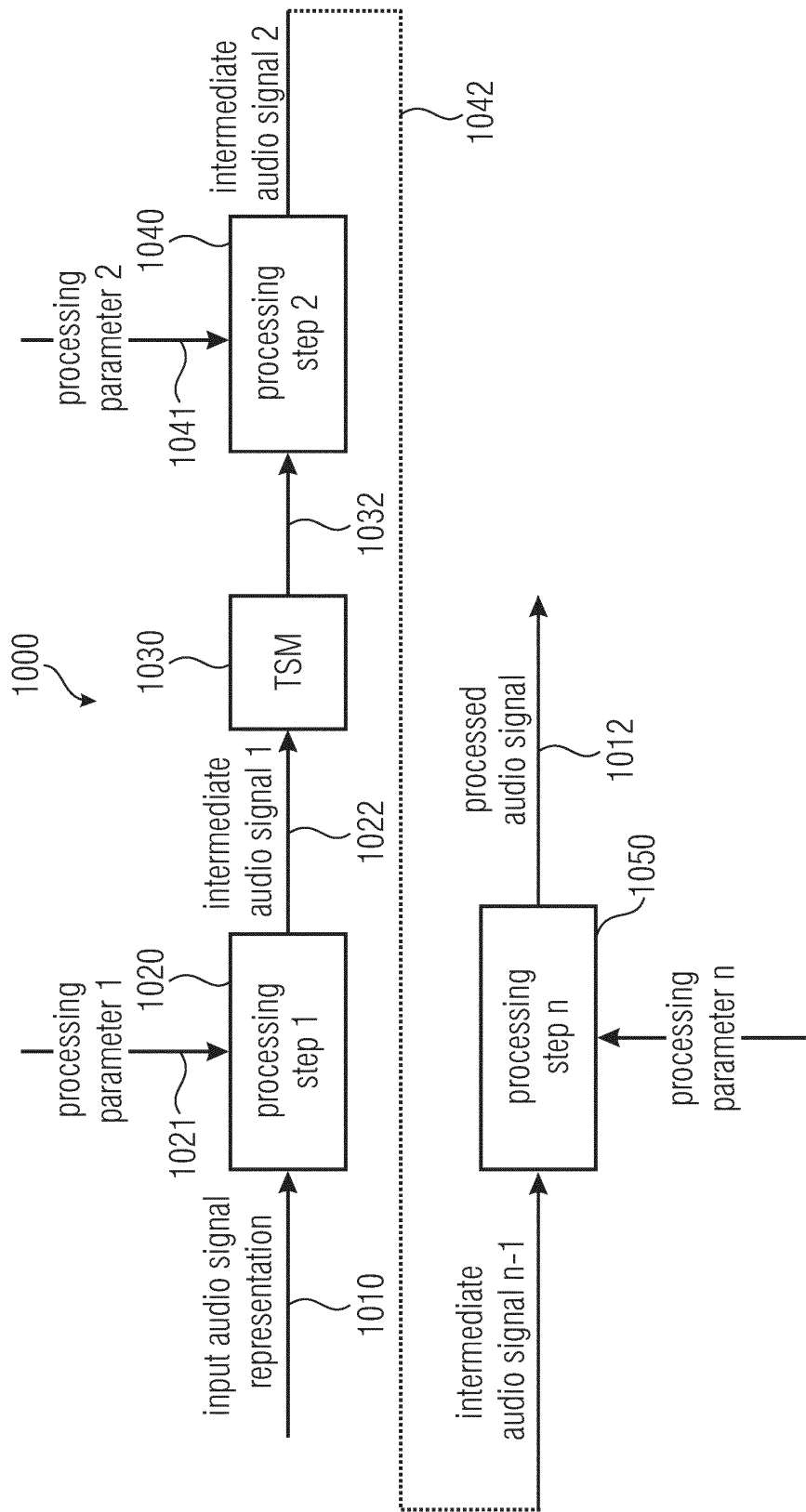
generic multi-stage audio processor
Fig. 7



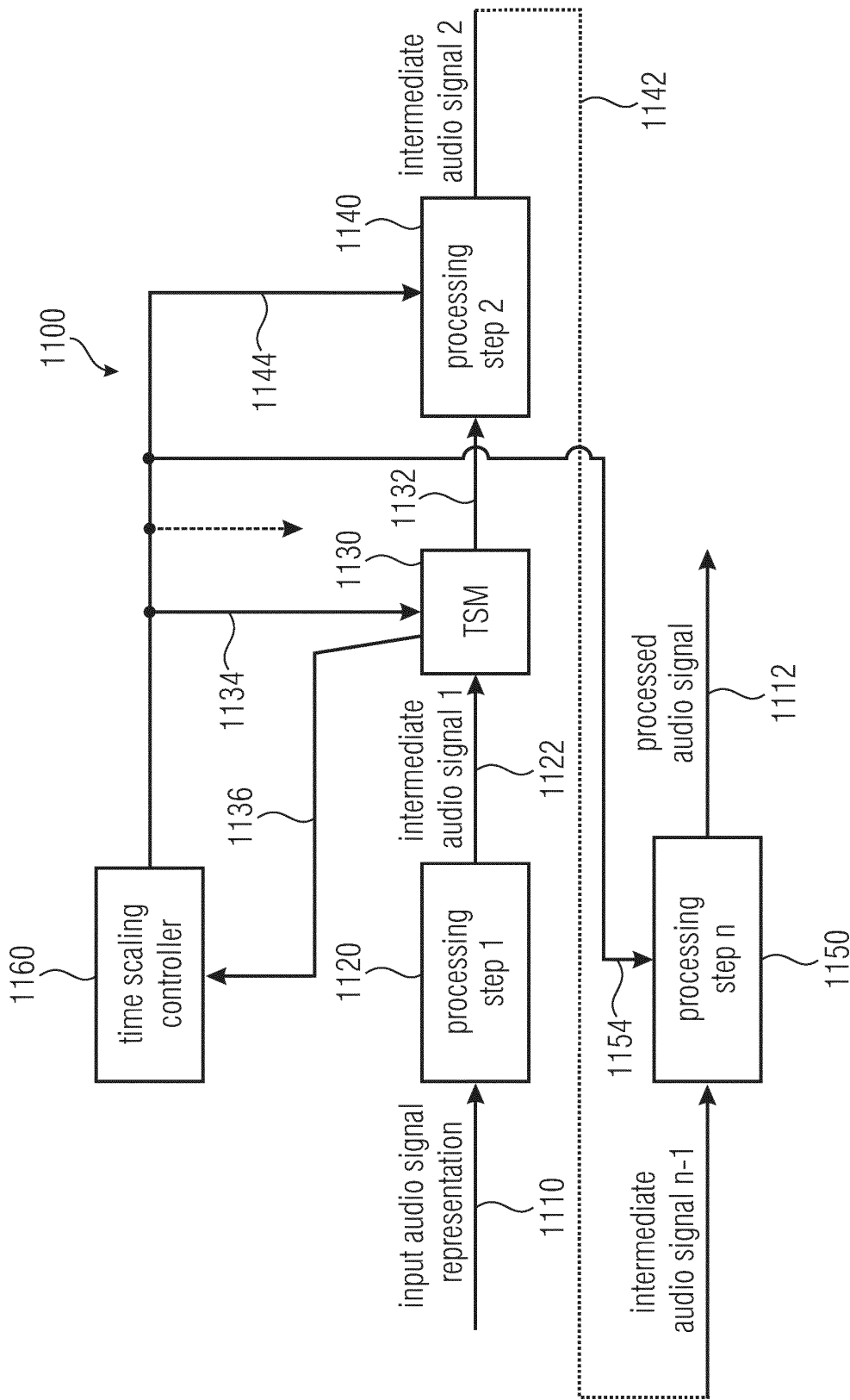
IVAS JBM with TSM on the output channels
Fig. 8



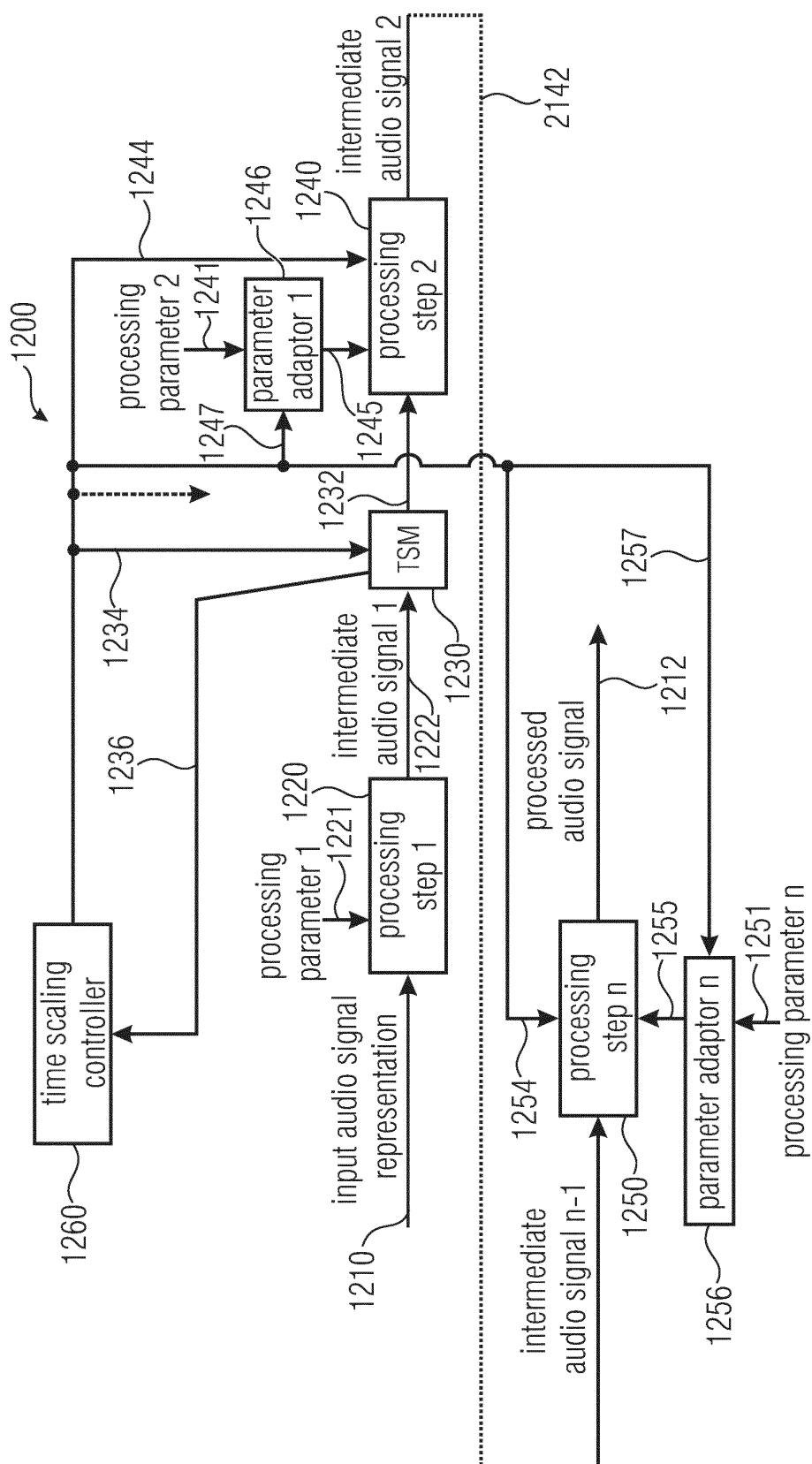
audio processor with TSM on an intermediate audio signal
Fig. 9



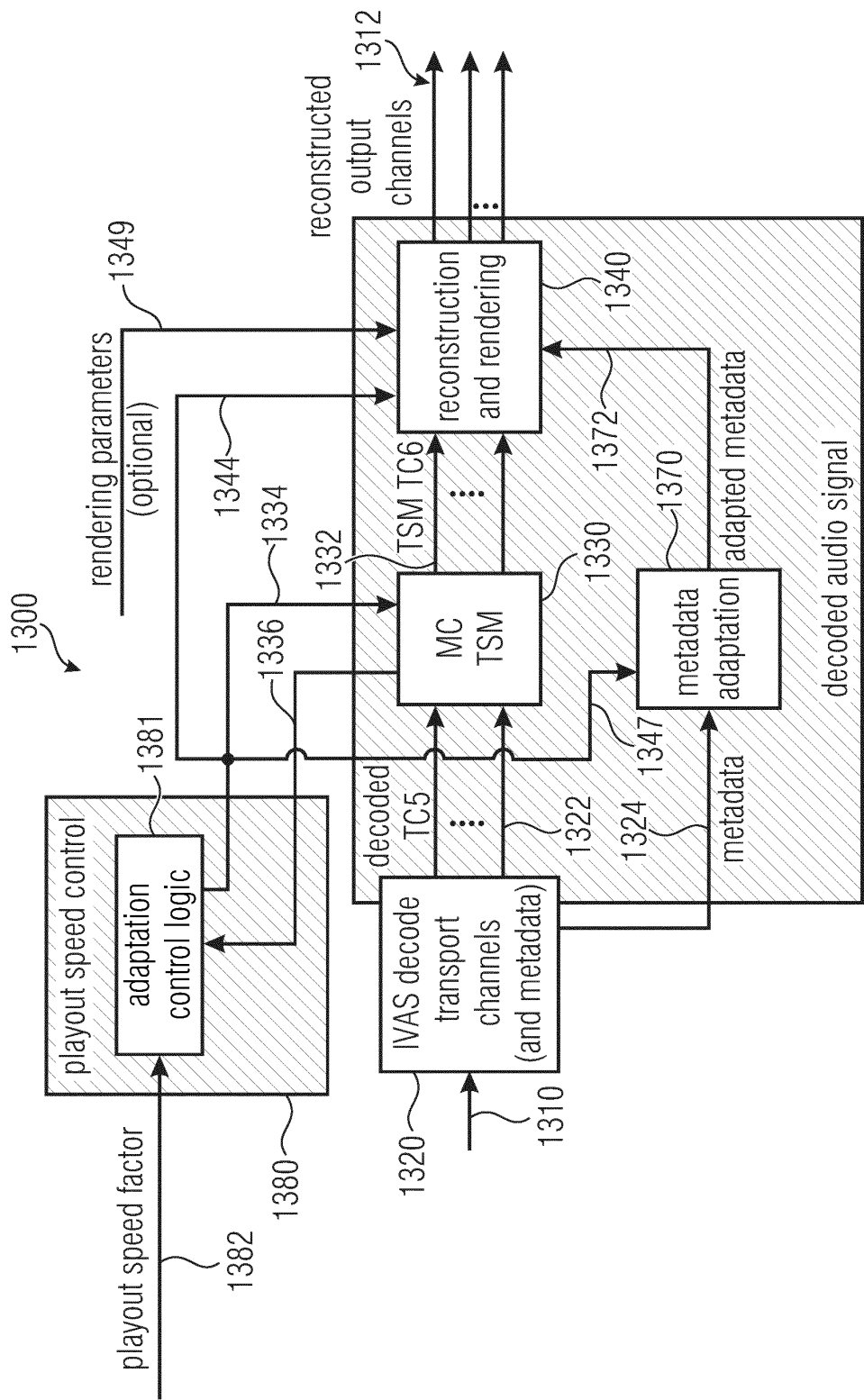
audio processor with parameters and TSM on an intermediate audio signal
Fig. 10



audio processor with TSM on an intermediate audio signal and time scaling controller
Fig. 11



audio processor with TSM on an intermediate audio signal, processing parameter adapters, and a time scaling controller



playback speed control
Fig. 13

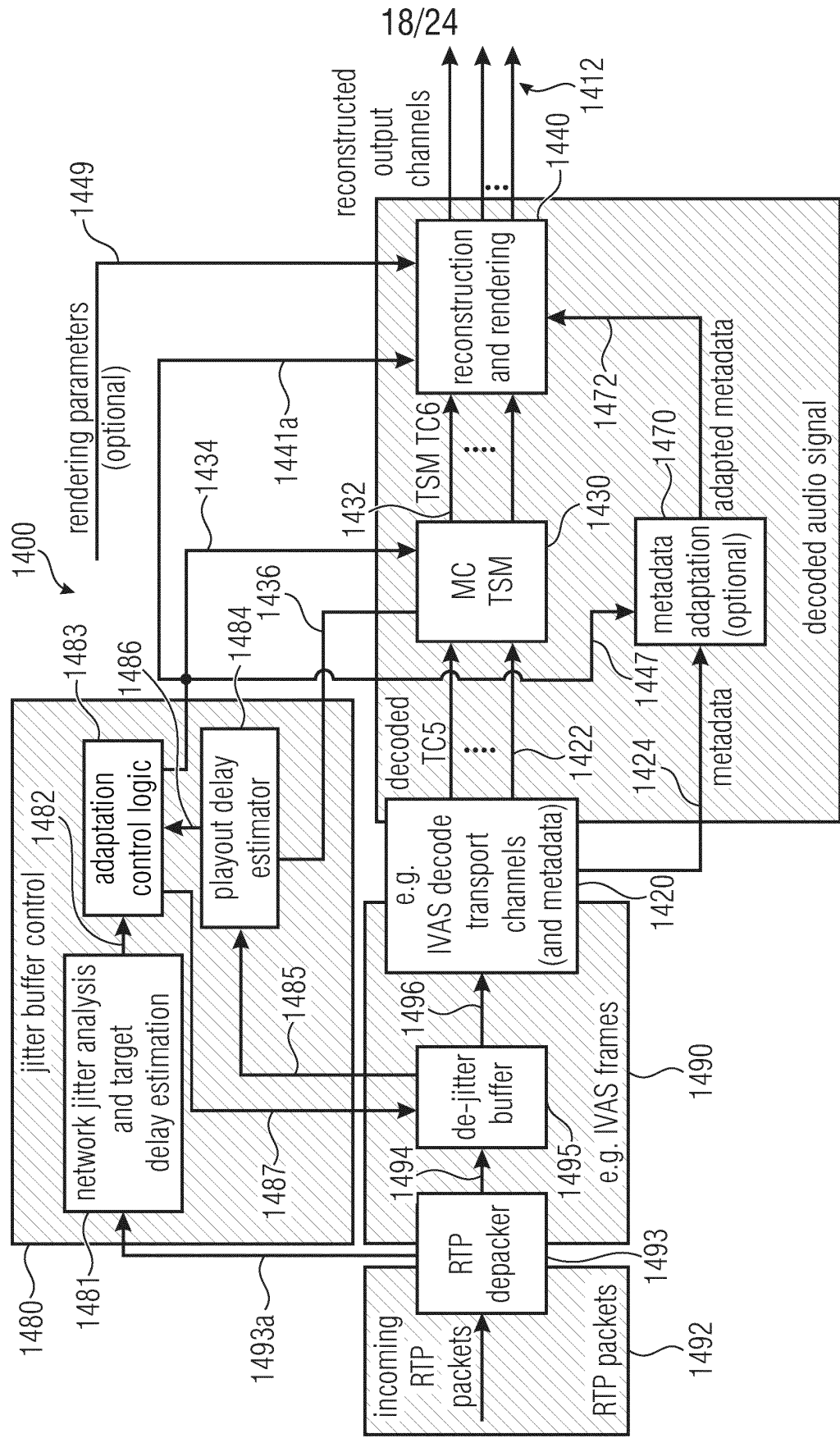
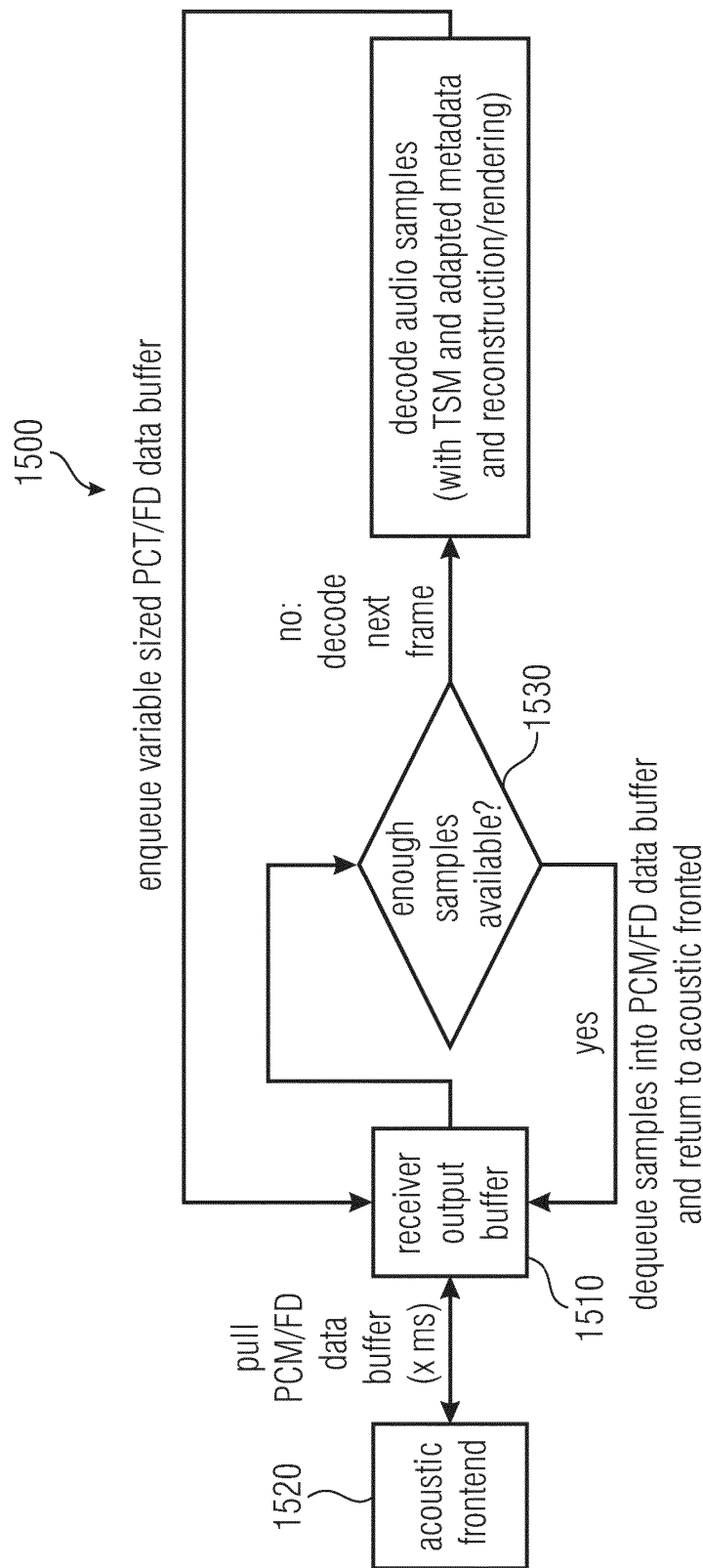


Fig. 14



receiver buffer for the IVAS JBM
Fig. 15a

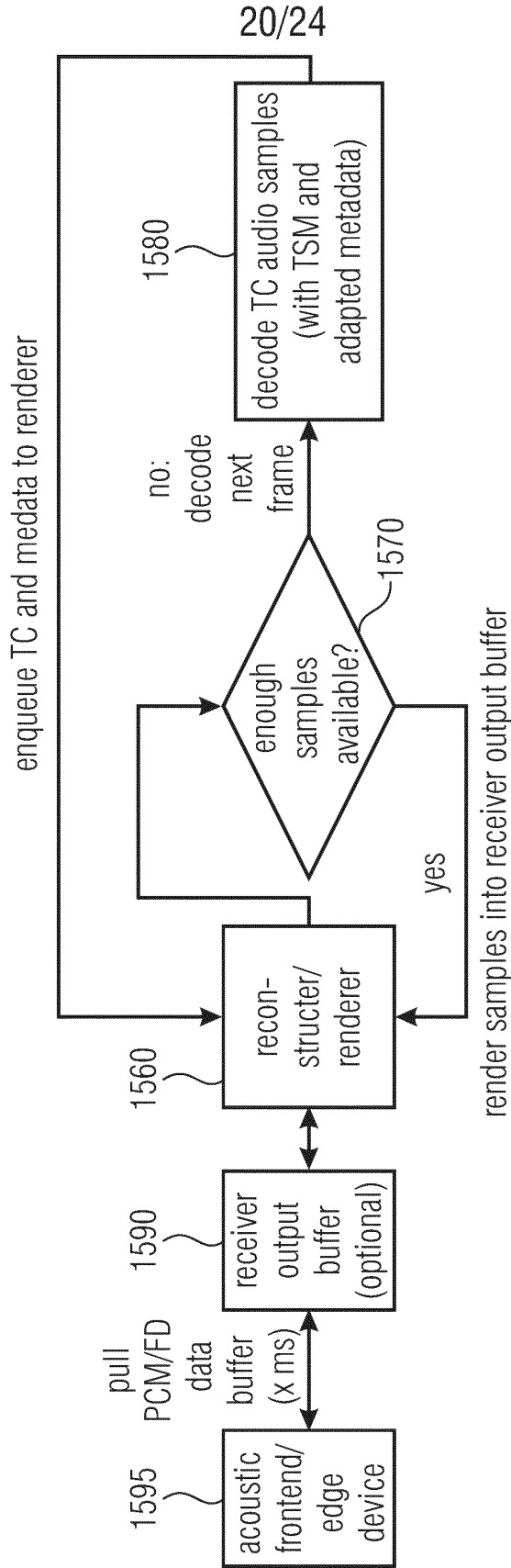
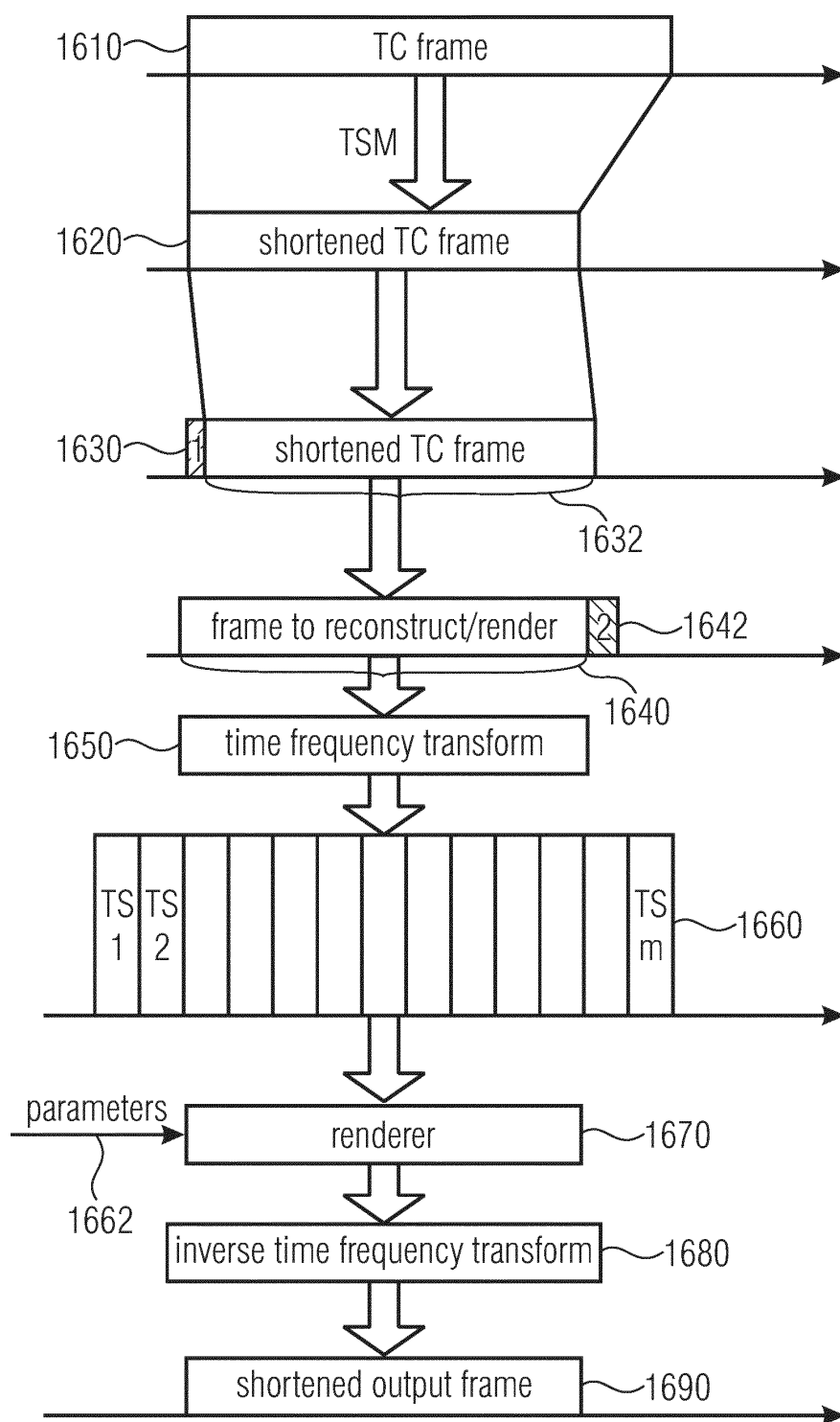


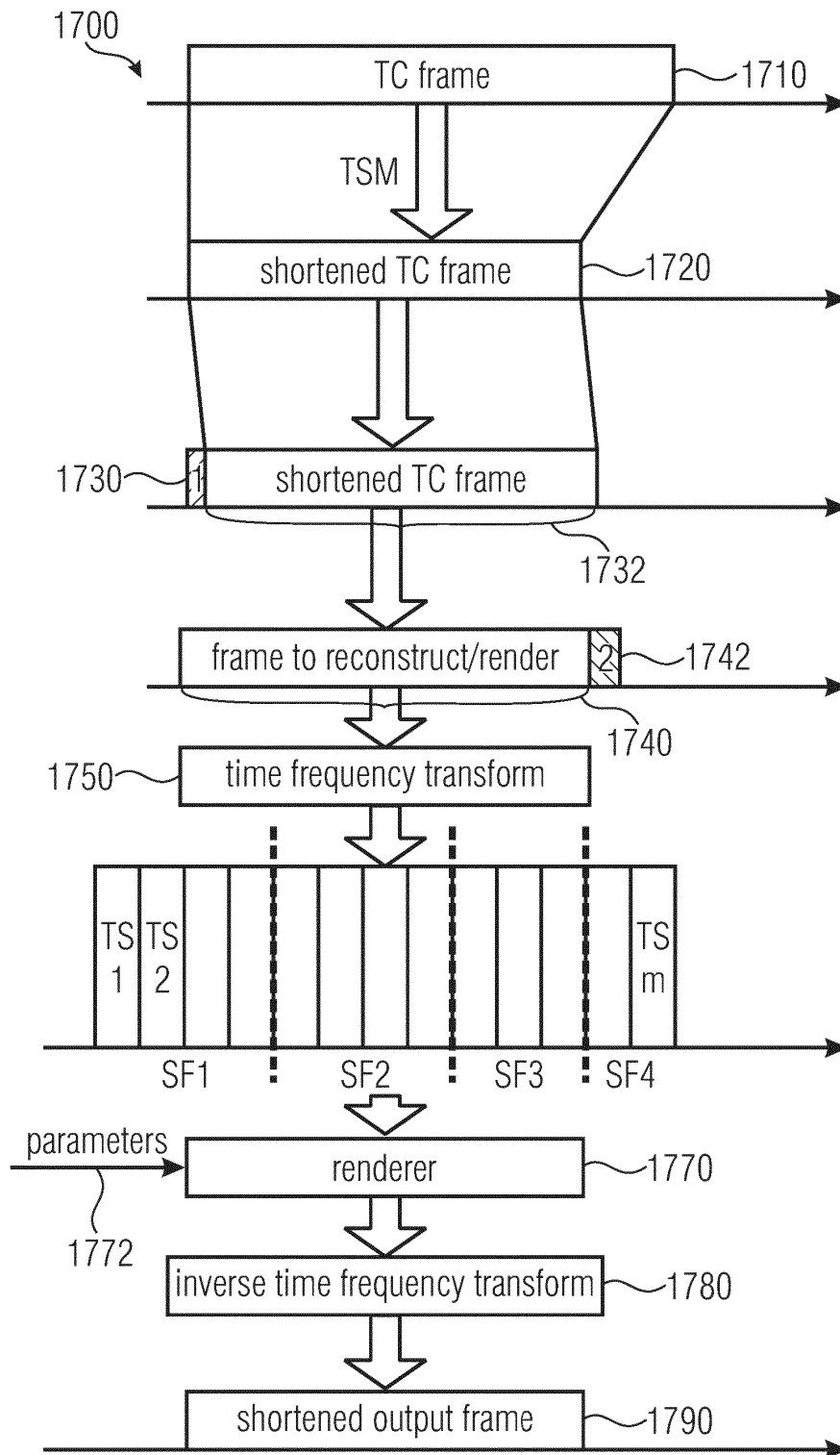
Fig. 15b

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TC frame buffering for FD rendering
Fig. 16

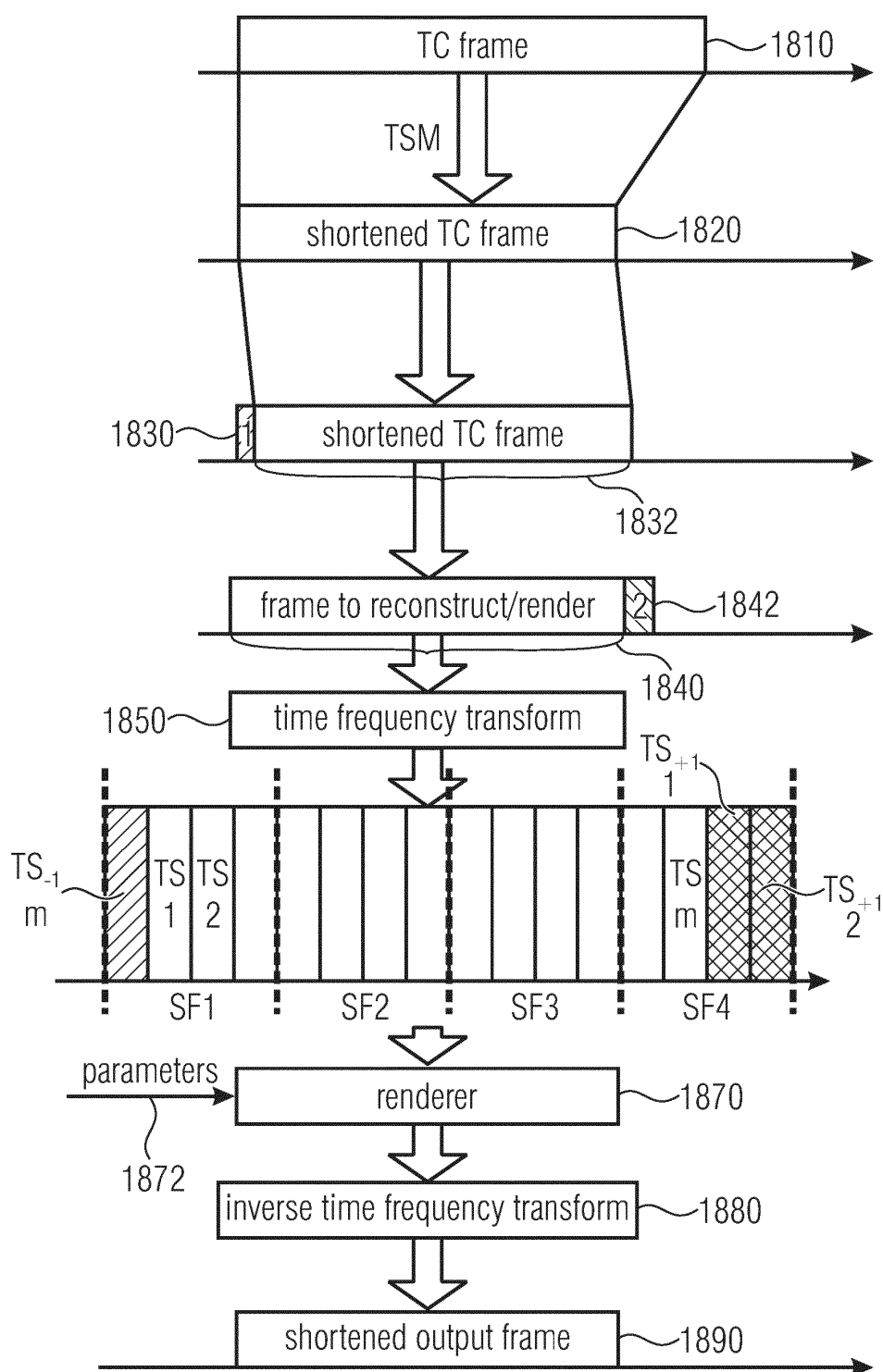
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FD rendering with adapted subframe size

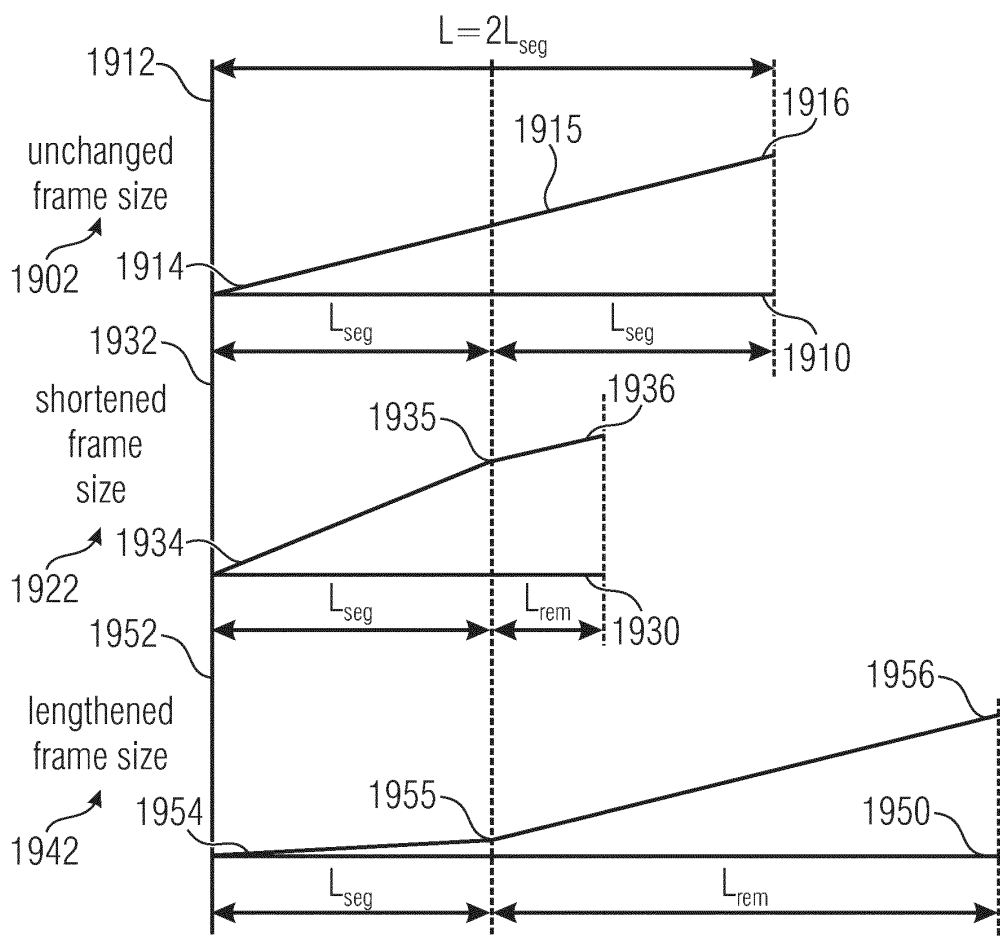
Fig. 17

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rendering with constant subframe size
and adapting the metadata

Fig. 18



adapting interpolation functions
Fig. 19