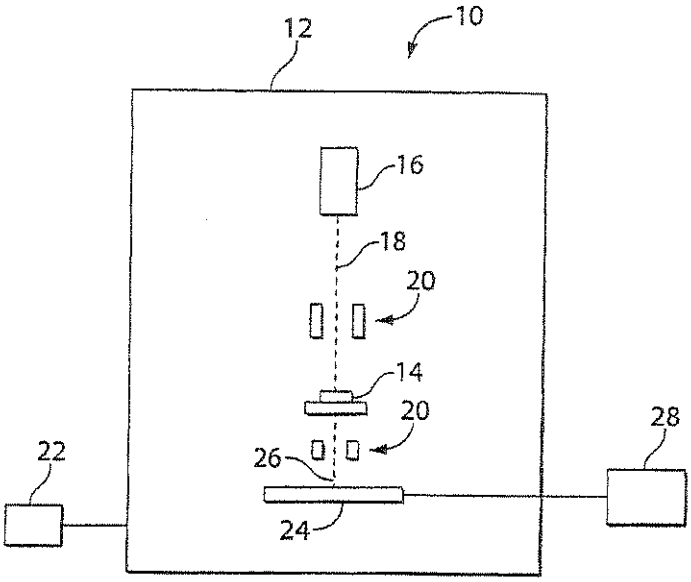


Fig. 1

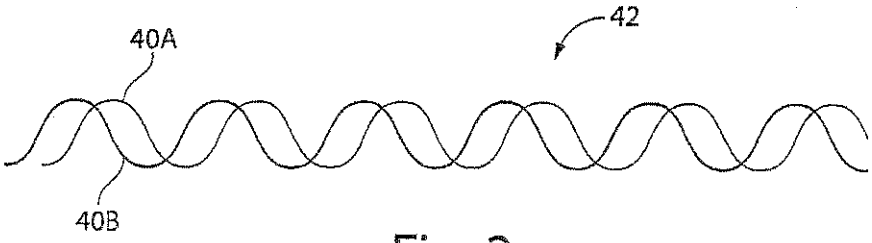


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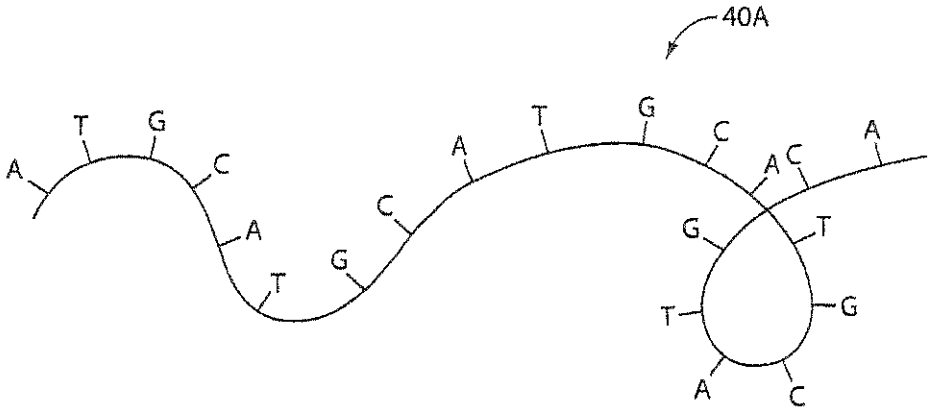


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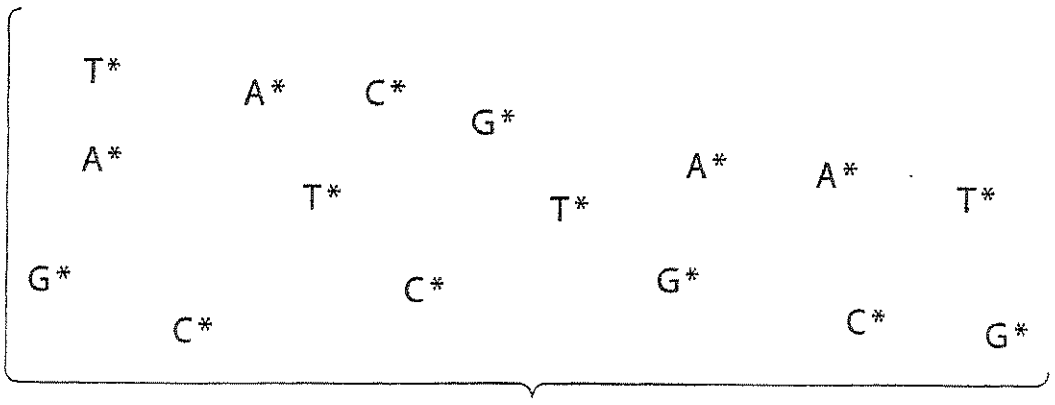


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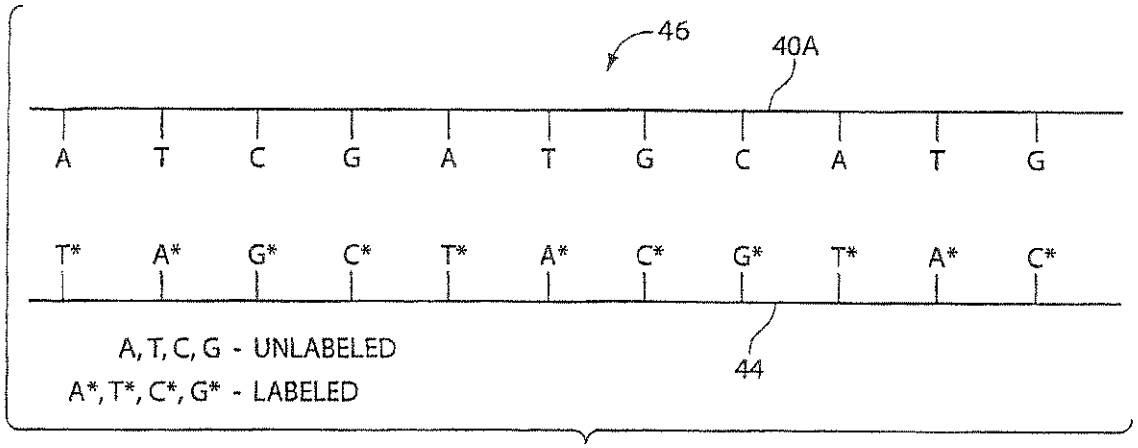

46
40A
A T C G A T G C A T G
T* A* G* C* T* A* C* G* T* A* C*
44
A, T, C, G - UNLABELED
A*, T*, C*, G* - LABELED

Fig. 5

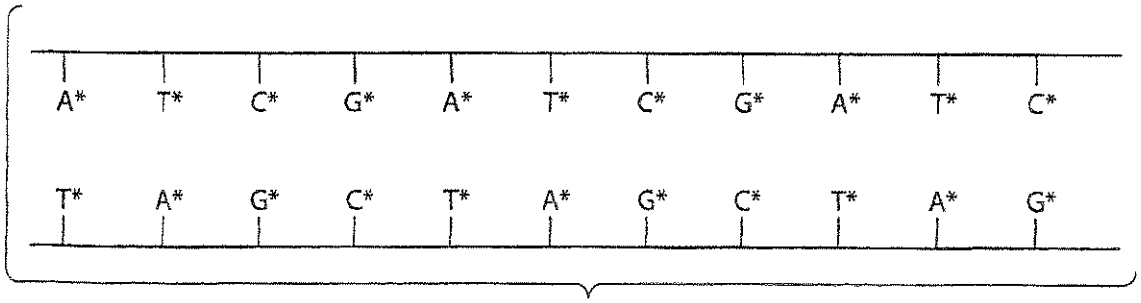

A* T* C* G* A* T* C* G* A* T* C*
T* A* G* C* T* A* G* C* T* A* G*

Fig. 6

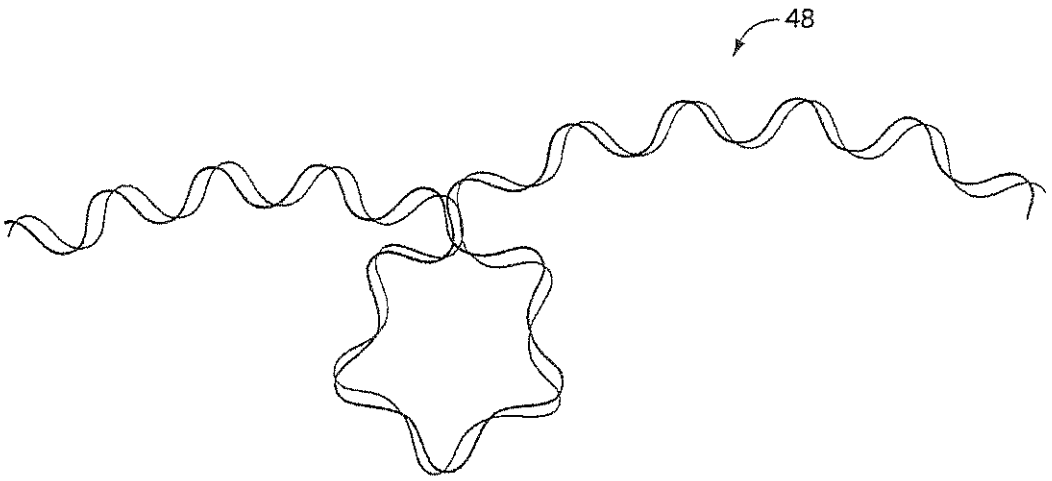


Fig. 7

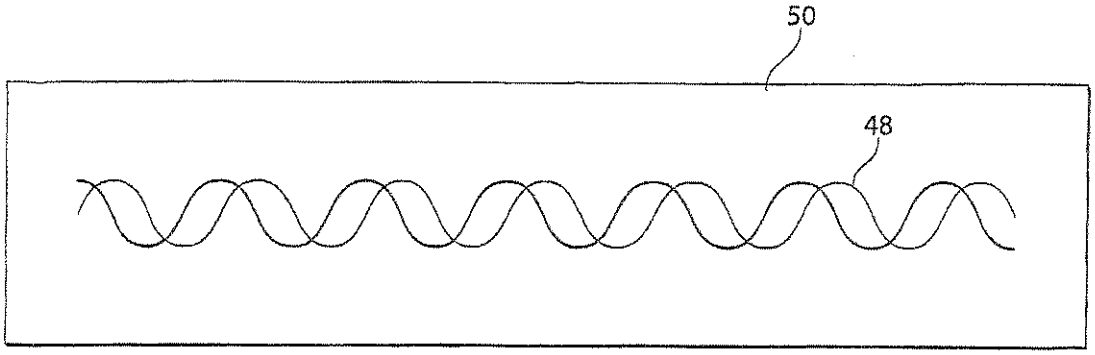


Fig. 8A

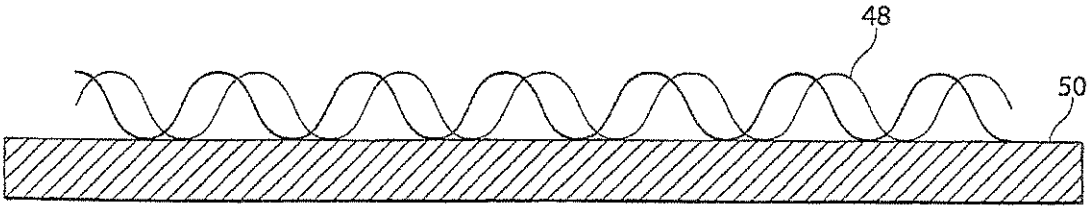


Fig. 8B

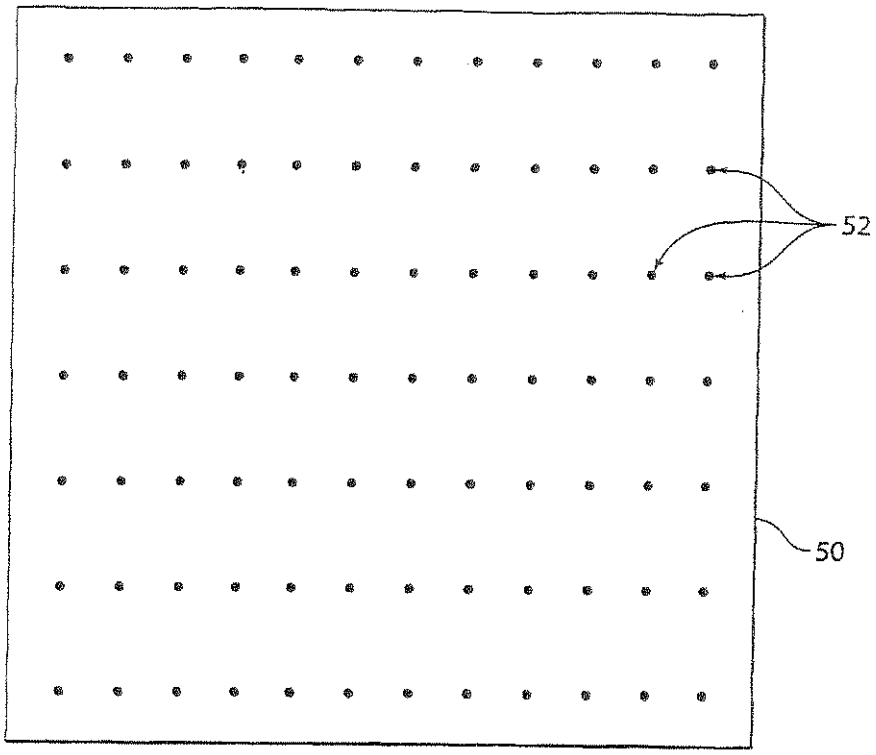


Fig. 9A

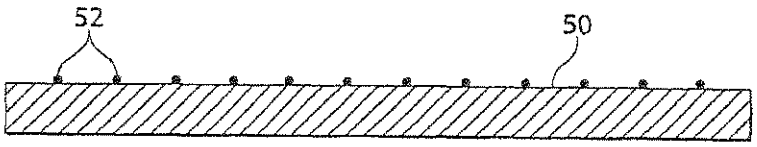


Fig. 9B

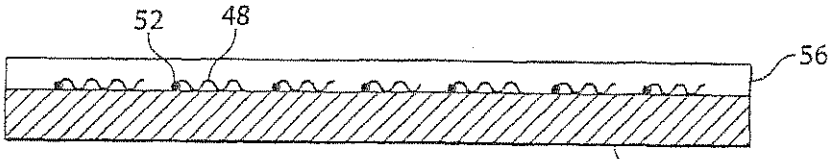


Fig. 10

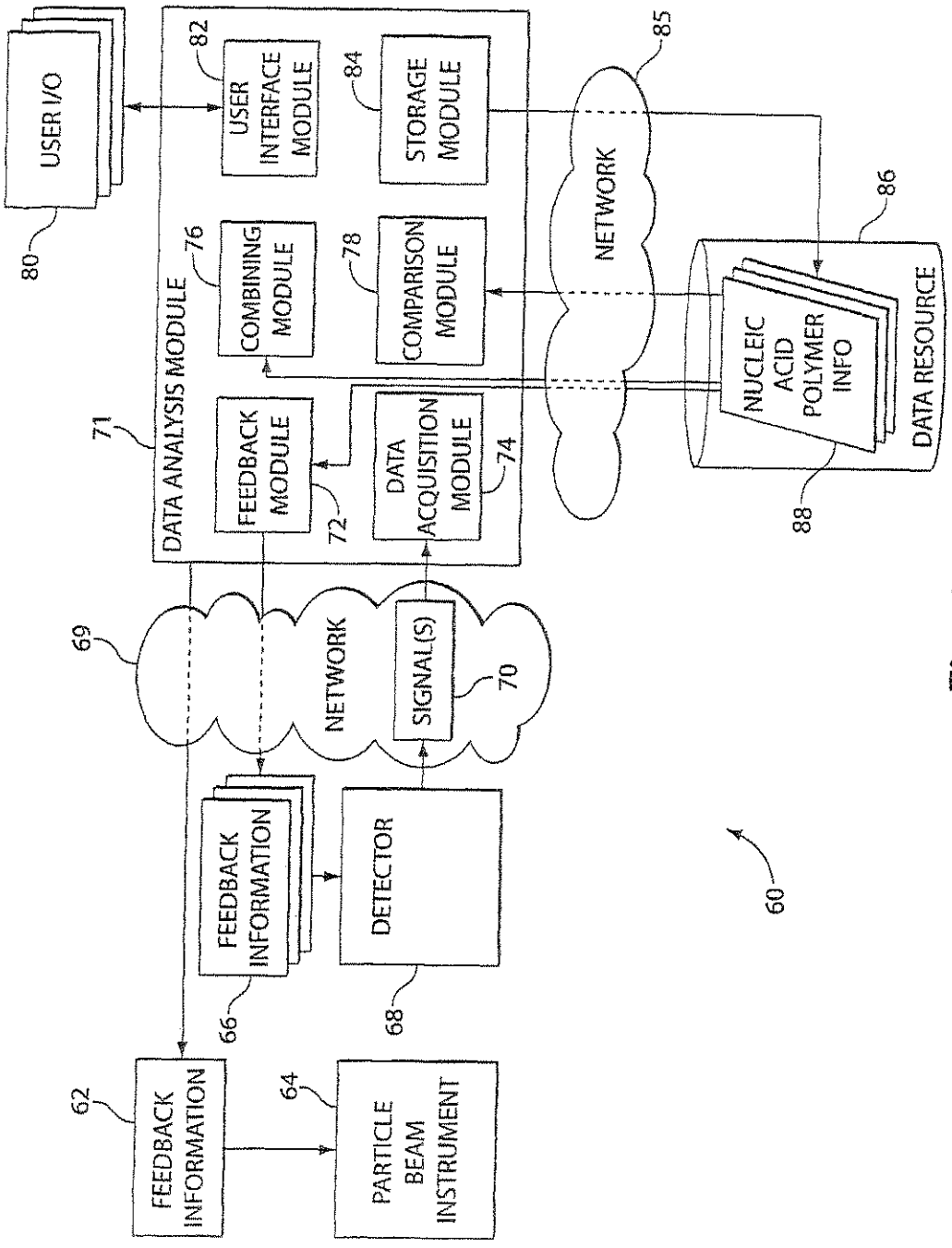

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82
USER INTERFACE MODULE
84
STORAGE MODULE
85
NETWORK
86
DATA RESOURCE
NUCLEIC ACID POLYMER INFO
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DATA ANALYSIS MODULE
71
76
COMBINING MODULE
78
COMPARISON MODULE
FEEDBACK MODULE
72
DATA ACQUISITION MODULE
74
69
NETWORK
SIGNAL(S)
70
FEEDBACK INFORMATION
66
DETECTOR
68
62
FEEDBACK INFORMATION
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PARTICLE BEAM INSTRUMENT
60

Fig. 11

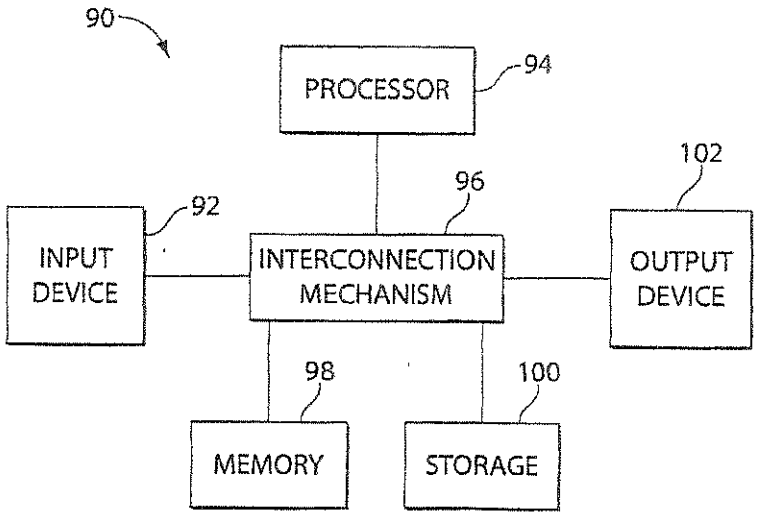

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PROCESSOR
94
102
96
92
INPUT DEVICE
INTERCONNECTION MECHANISM
OUTPUT DEVICE
98
100
MEMORY
STORAGE

Fig. 12

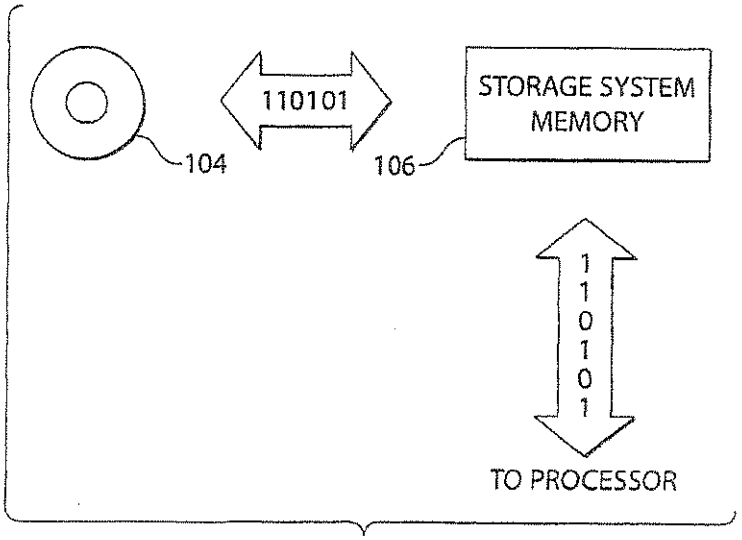

110101
STORAGE SYSTEM MEMORY
104
106
1 1 0 1 0 1
TO PROCESSOR

Fig. 13

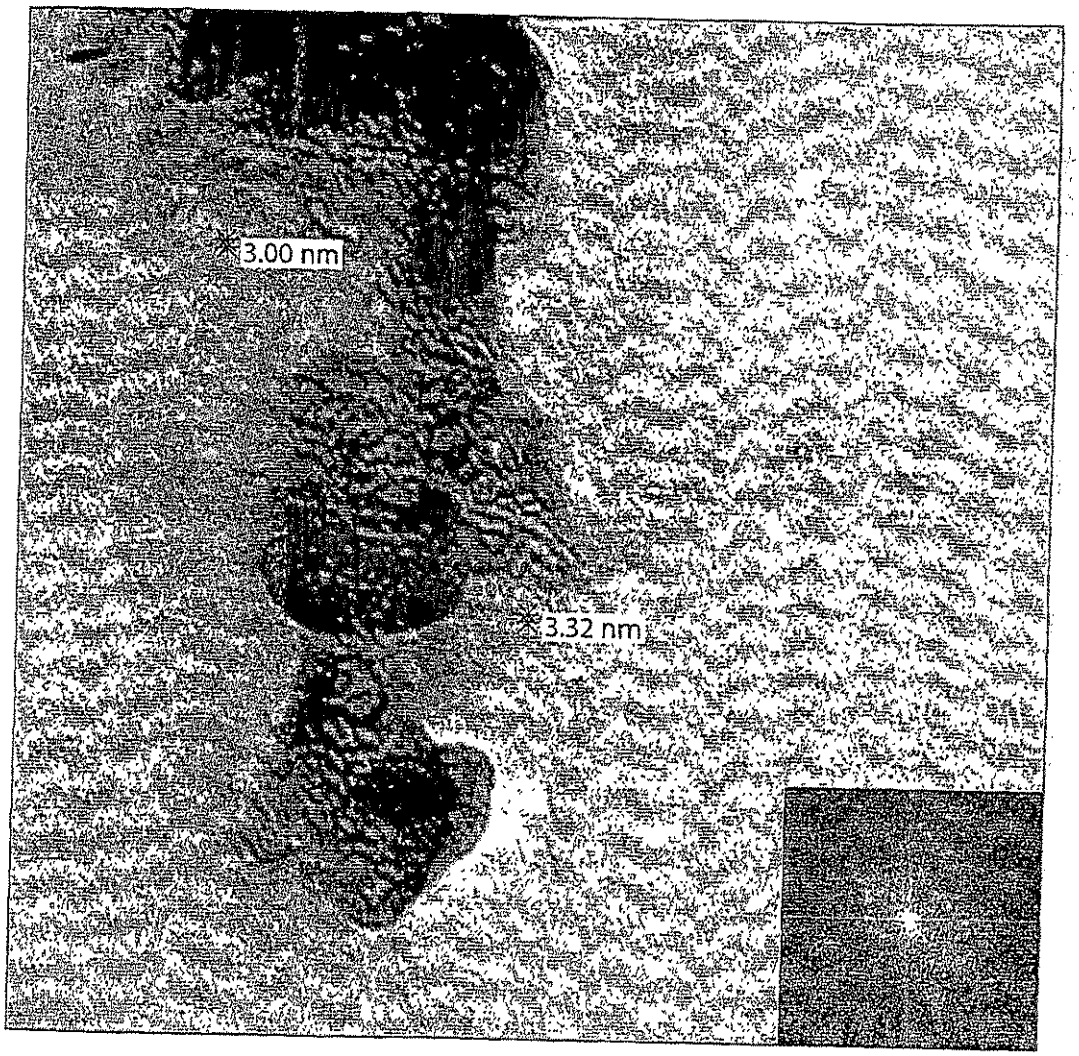

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Fig. 14

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