

FIG.1A

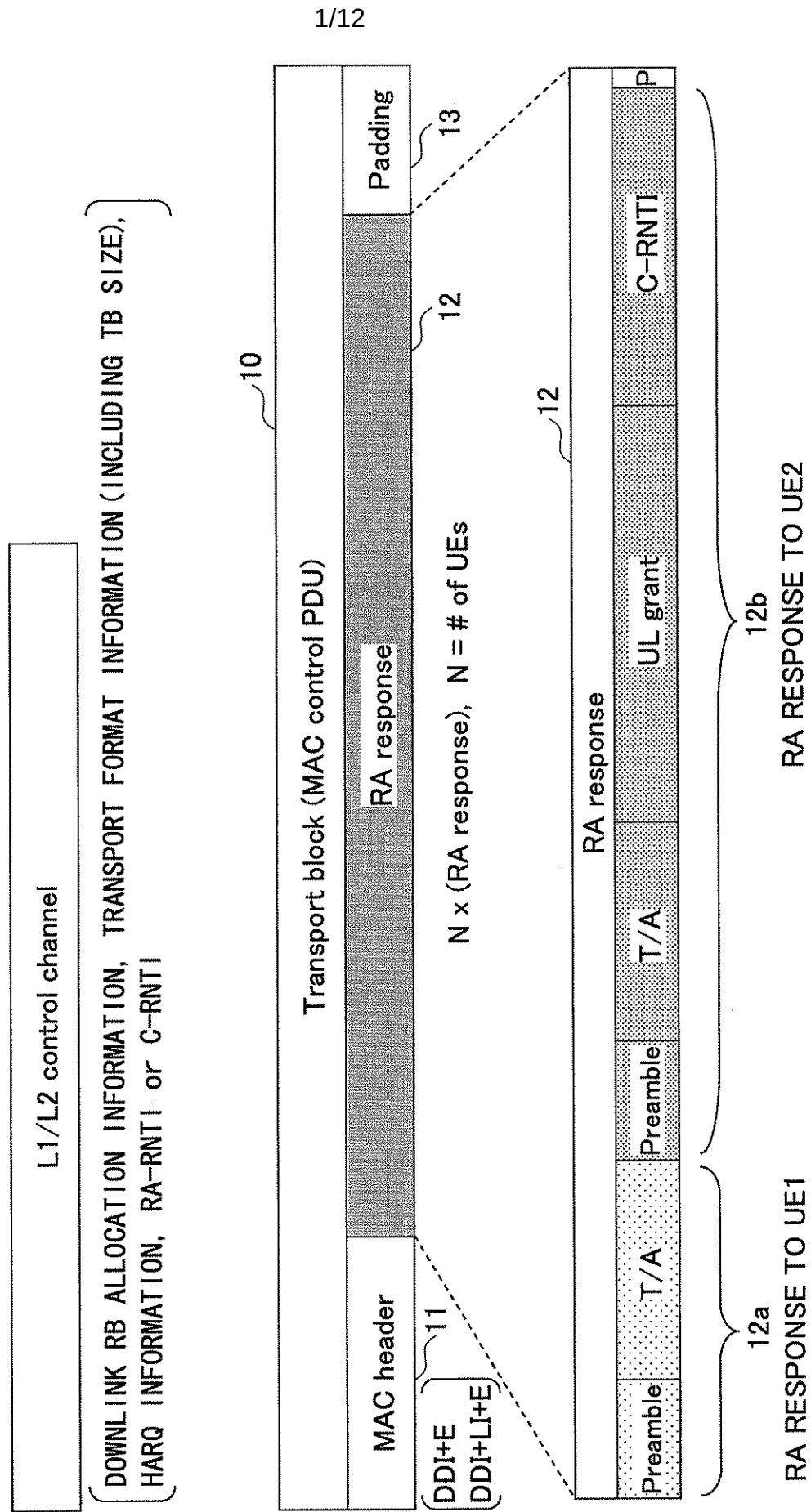


FIG.1B

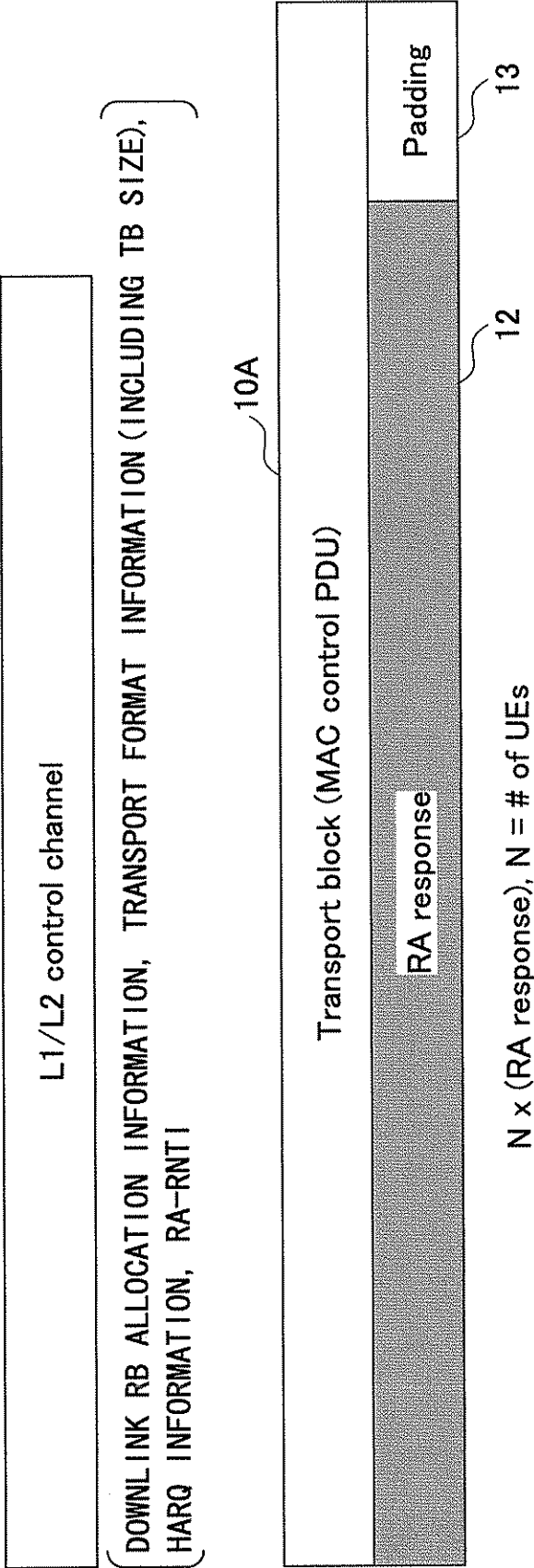


FIG.2

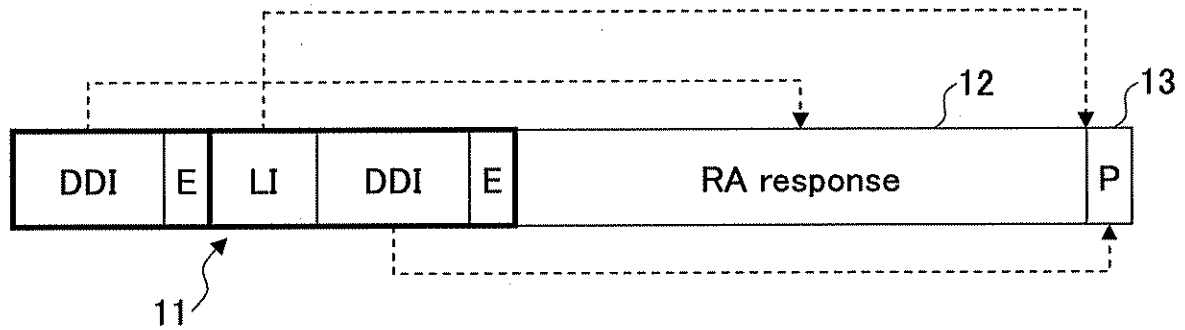


FIG.3

DDI	Message
0000	DCCH
0001-1000	DTCH #1-#8
1001	CCCH
1010	MAC control block (dedicated)
1011	Padding
1100	MAC control block (common, variable length) e.g. RACH response
1101-1111	Reserved

FIG.4

RACH TRANSMIT OBJECTIVE	INFORMATION ELEMENT TRANSMITTED IN RA response	NUMBER OF INFORMATION BITS
INITIAL ACCESS	Preamble SEQUENCE	X bits
	Timing advance information	
	UL grant information	
	C-RNTI	
UPLINK SYNCHRONIZATION ESTABLISHMENT REQUEST	Preamble SEQUENCE	Y bits
	Timing advance	
HANDOVER	Preamble SEQUENCE	Z bits
	Timing advance information	
	UL grant information	
SCHEDULING REQUEST	Preamble SEQUENCE	W bits
	Timing advance information	
	UL grant information	

FIG.5

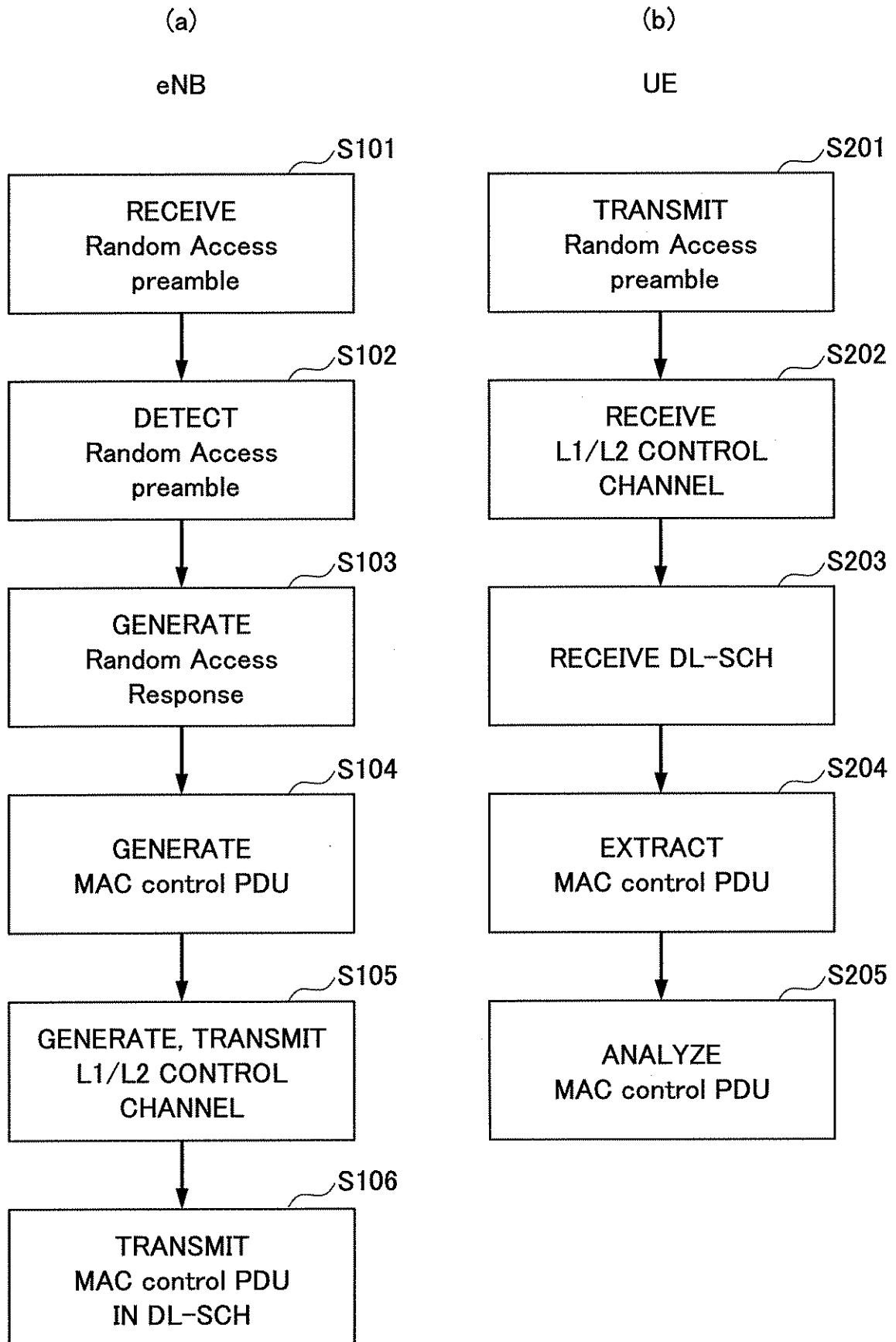


FIG. 6

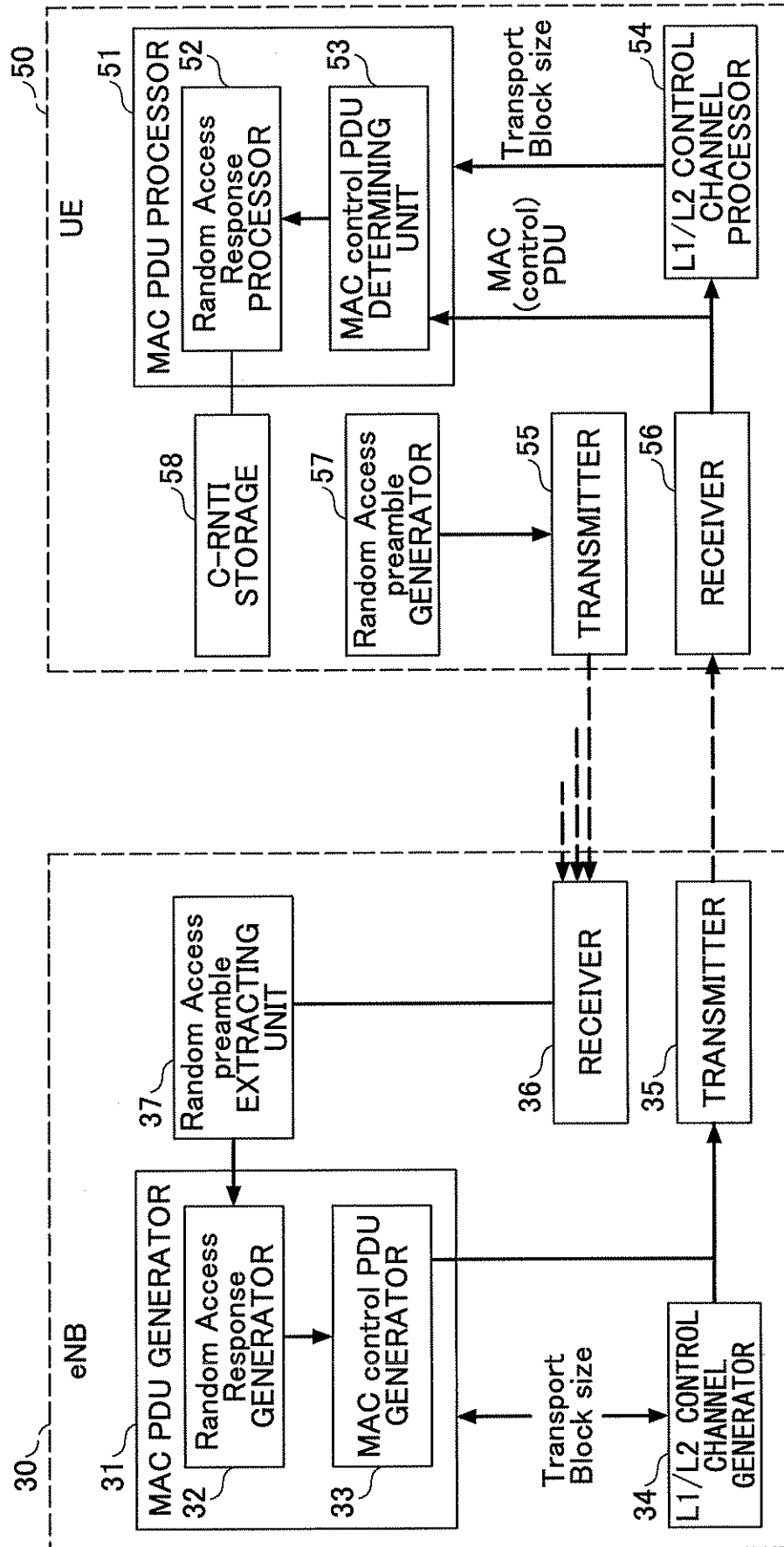


FIG.7A

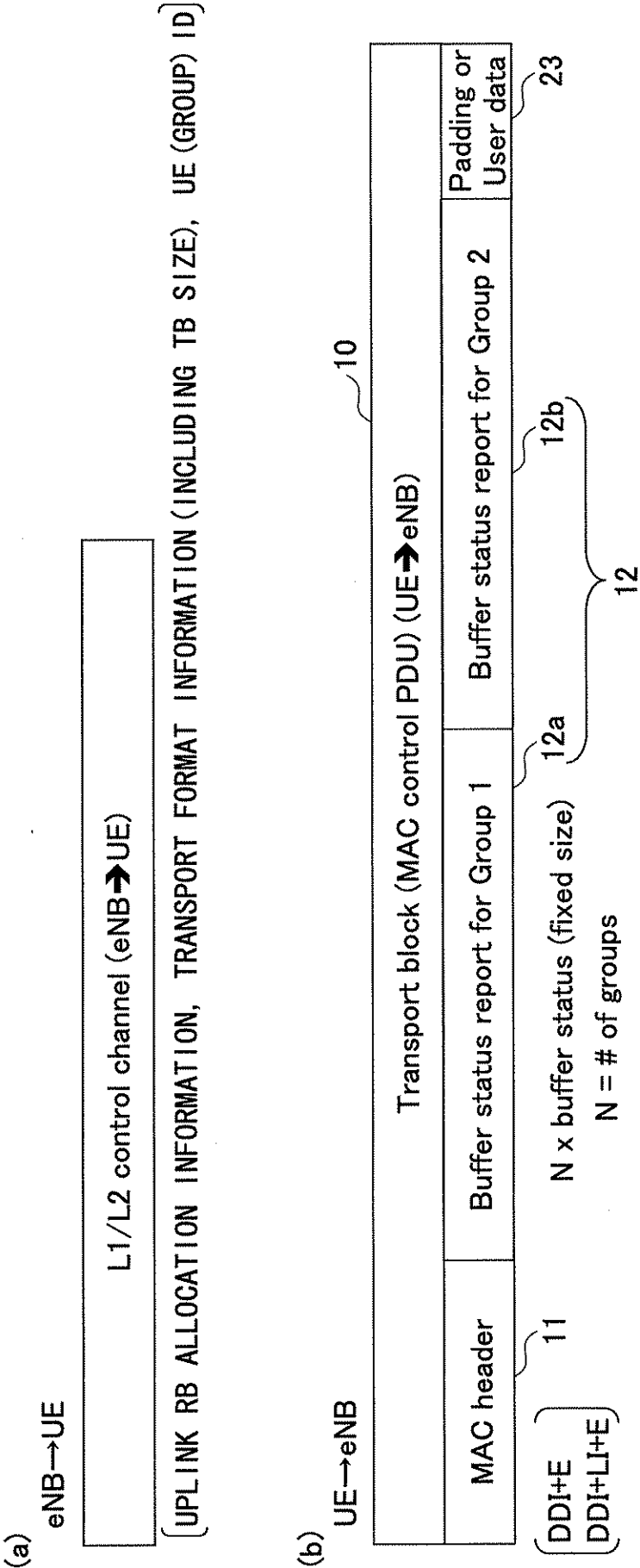


FIG.7B

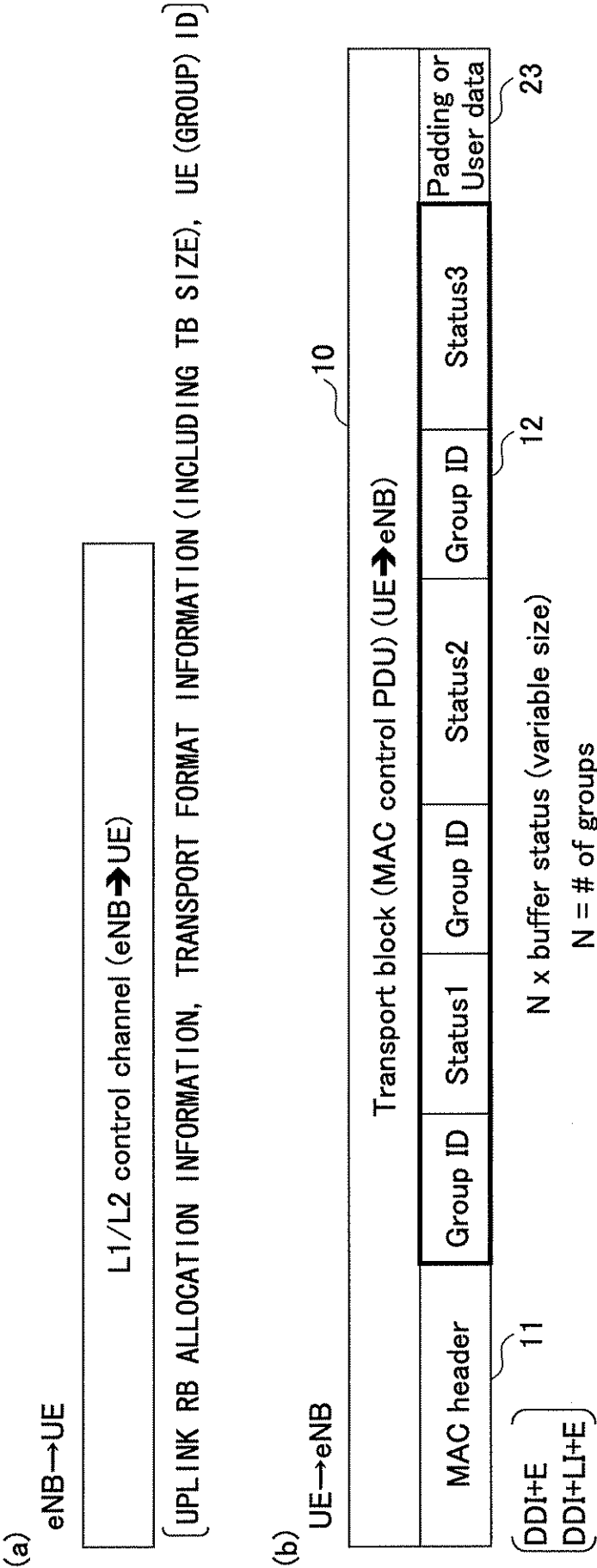


FIG.8A

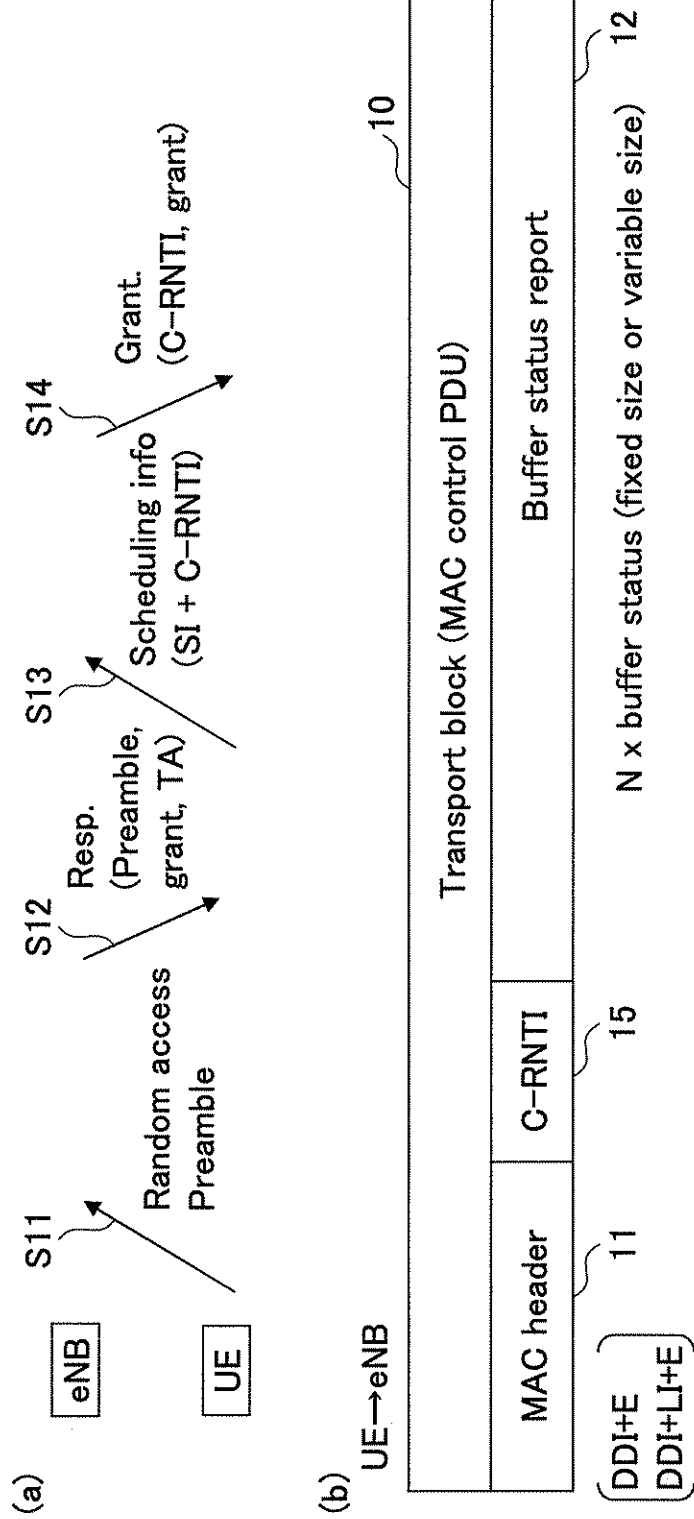


FIG.8B

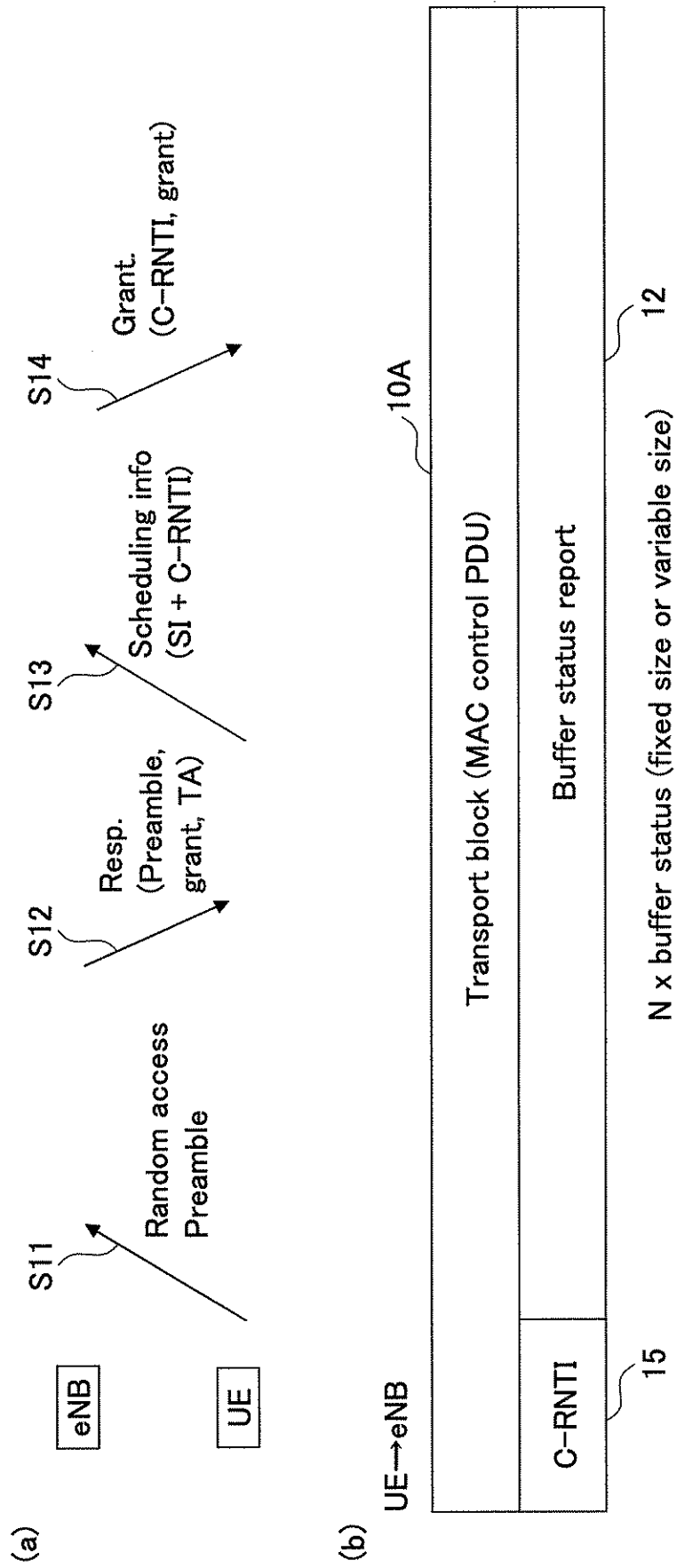


FIG.9

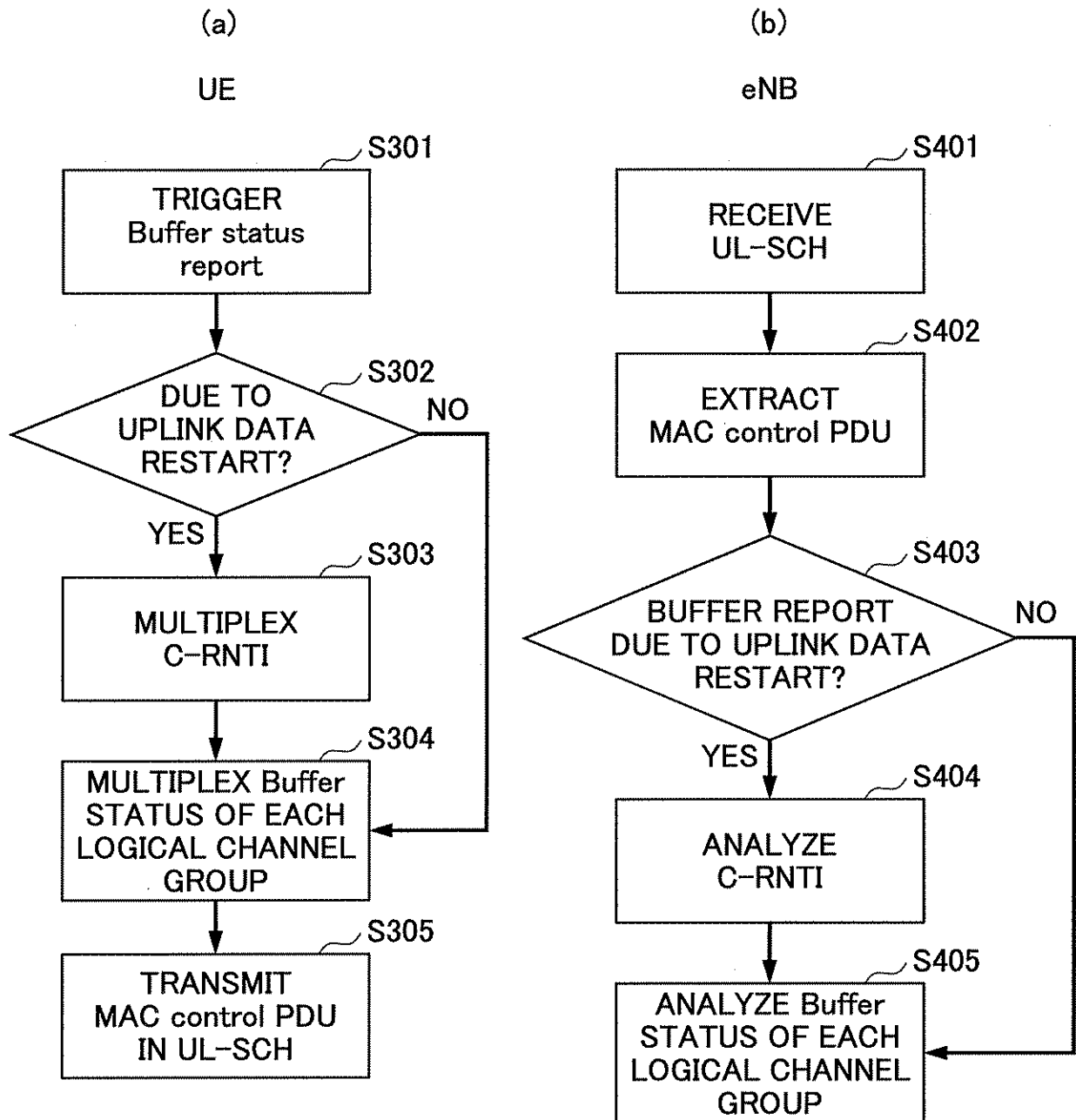


FIG.10

