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ABSTRACT OF THE DISCLOSURE

A compensating circuit for compensating positive-negative asymmetry of an input signal having an amplitude exhibiting a positive-negative asymmetry with reference to a given level. Either one of the input signal or a signal obtained by multiplying the input signal by a given number is selected to be outputted as a compensated signal when the input signal has a level higher than a threshold level or alternatively when the input signal has a level lower than the threshold level. Distortion of signal waveforms ascribable to non-linearity of an MR head can be compensated satisfactorily with an extremely simplified and inexpensive circuit structure.

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