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(71) Applicant: **QUALCOMM INCORPORATED** [US/US];
Attn: International IP Administration, 5775 Morehouse Drive, San Diego, California 92121-1714 (US).

(72) Inventors: **KARCZEWICZ, Marta**; 5775 Morehouse Drive, San Diego, California 92121-1714 (US). **COBAN, Muhammed Zeyd**; 5775 Morehouse Drive, San Diego, California 92121-1714 (US). **DONG, Jie**; 5775 Morehouse Drive, San Diego, California 92121-1714 (US).

(74) Agent: **JOSEPH, Jeffrey R.**; Shumaker & Sieffert, P.A., 1625 Radio Drive, Suite 100, Woodbury, Minnesota 55125 (US).

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(54) Title: REGULAR CODED BIN REDUCTION FOR COEFFICIENT CODING USING THRESHOLD

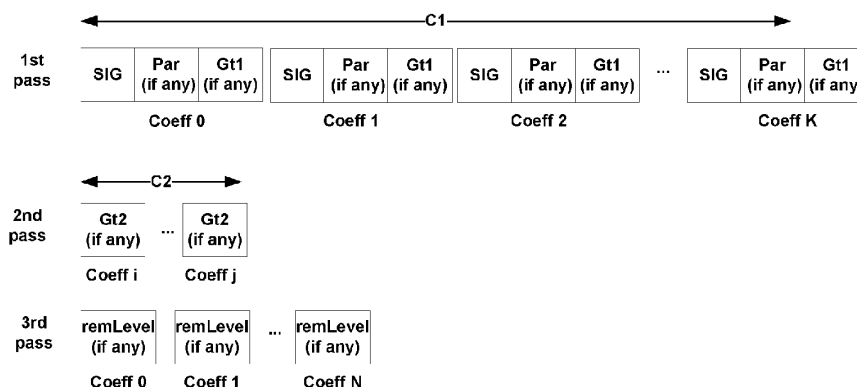


FIG. 5

(57) Abstract: A video decoder can be configured to determine a size of a transform unit, wherein the transform unit comprises N coefficient groups and each coefficient group comprises M coefficients, wherein M and N are integer values; determine a regular bin count threshold for the transform unit based on the size of the transform unit; context decode syntax elements for the transform unit until the first number of regular coded bins is reached; in response to reaching the first number of regular coded bins, bypass decode additional syntax elements of the transform unit; determine values for a first set of coefficients of the transform unit based on the context decoded syntax elements; and determine values for a second set of coefficients of the transform unit based on the additional syntax elements.

[Continued on next page]

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