

ABSTRACT

5 A frequency detector of a phase-lock-loop circuit is
used for measuring a frequency error between a frequency of an
output signal of an oscillator and a frequency of a synchronizing
signal. When the frequency error in each of 32 periods of the
10 synchronizing signal exceeds a predetermined magnitude, the
phase-lock-loop circuits begins operating in a coarse frequency
correction mode. As long as the 32 periods have not lapsed, the
phase-lock-loop circuit operates in an idle mode of operation and
the oscillator is not corrected. As a result, during vertical retrace,
when equalizing pulses occur, the phase-lock-loop circuit is not
15 disturbed by a large frequency error.

This Sheet Has Been Replaced

