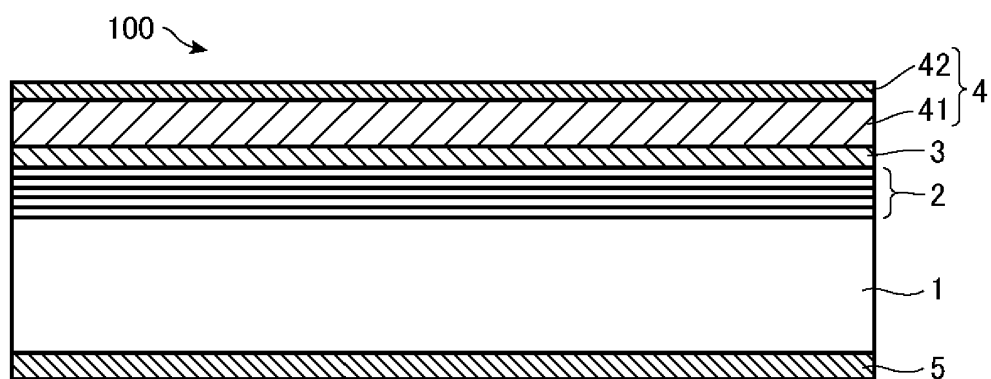


[図1]



[図2]

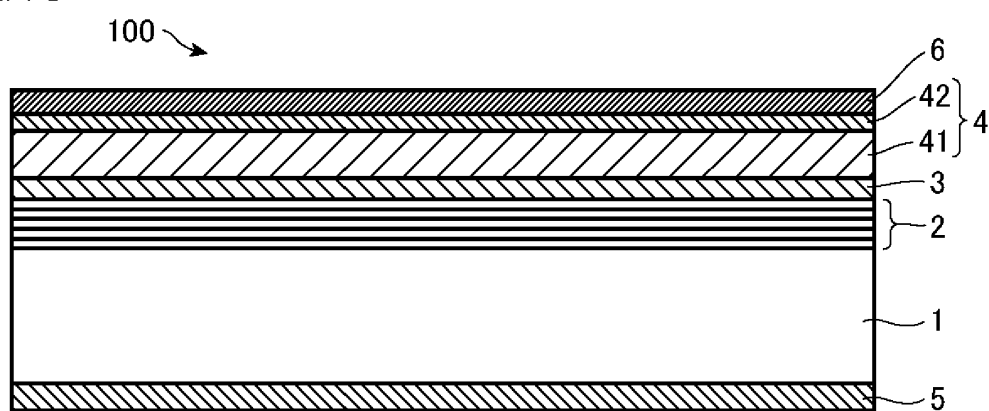
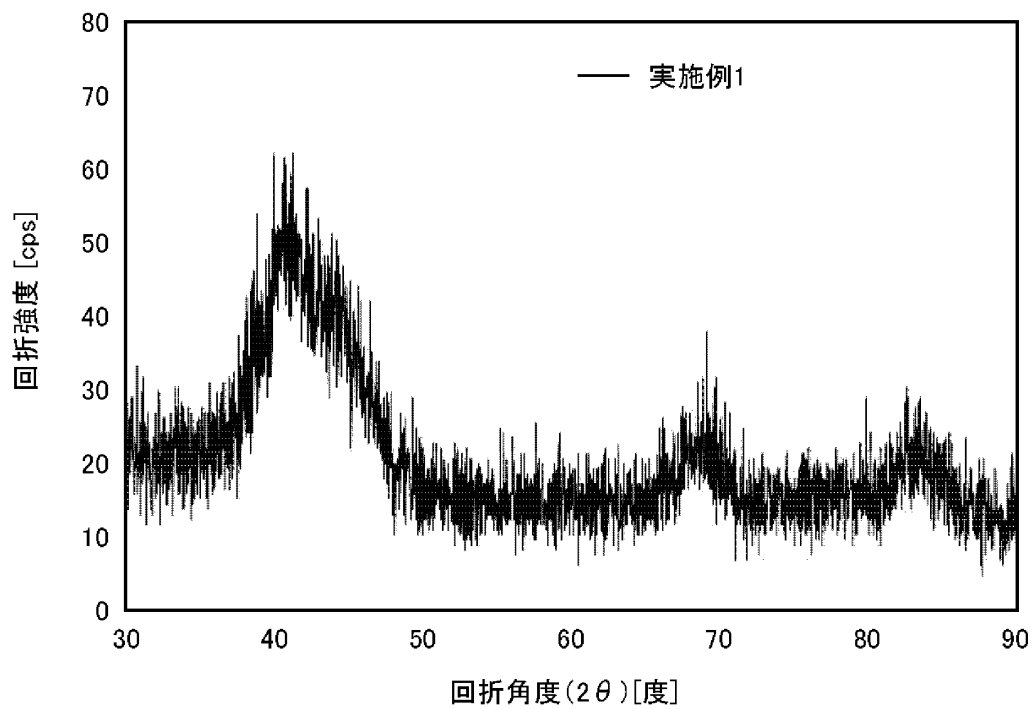


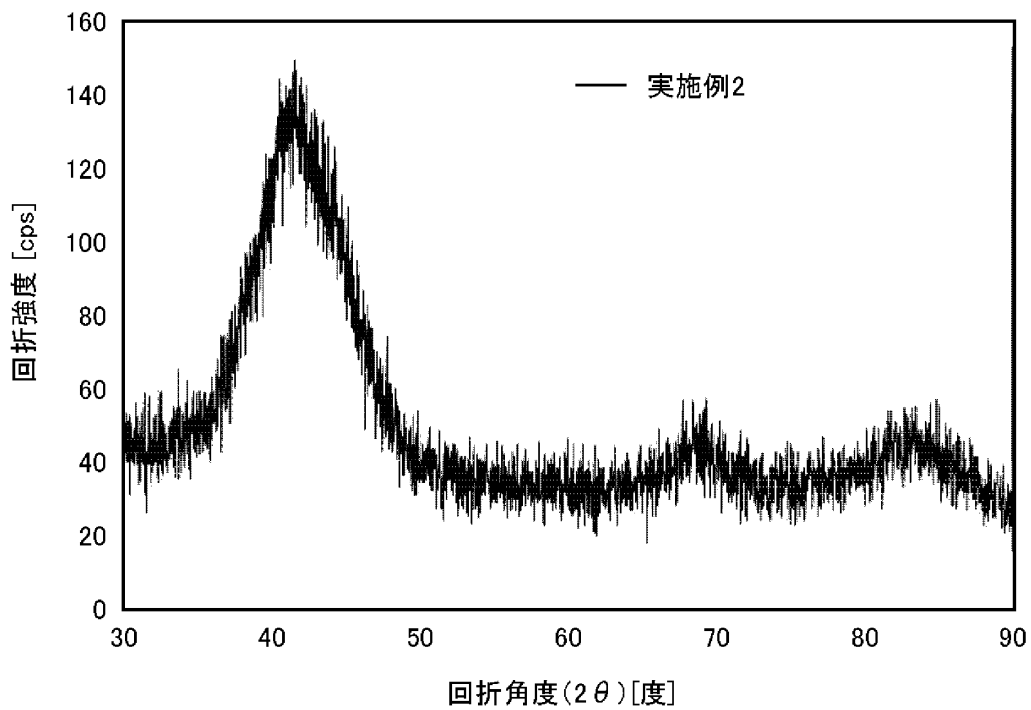
Figure 1 consists of six cross-sectional views (a) through (f) of a semiconductor device, showing the progression of its fabrication. The device is labeled 100 in (a) and 200 in (f).

- (a) The device 100 is shown in cross-section. It has a substrate 1 with a bottom layer 5. A central layer 1 is present. A top layer 2 is added. A top layer 3 is added. A patterned layer 4 (containing 41 and 42) is added. A top layer 6 is added.
- (b) The device 100 is shown in cross-section. It has a substrate 1 with a bottom layer 5. A central layer 1 is present. A top layer 2 is added. A top layer 3 is added. A patterned layer 4 (containing 41 and 42) is added. A top layer 6 is added. A top layer 8 is added.
- (c) The device 100 is shown in cross-section. It has a substrate 1 with a bottom layer 5. A central layer 1 is present. A top layer 2 is added. A top layer 3 is added. A patterned layer 4 (containing 41 and 42) is added. A top layer 6 is added. A top layer 8a is added.
- (d) The device 100 is shown in cross-section. It has a substrate 1 with a bottom layer 5. A central layer 1 is present. A top layer 2 is added. A top layer 3 is added. A patterned layer 4 (containing 41 and 42) is added. A top layer 6a is added. A top layer 8a is added. A patterned layer 4a (containing 41a and 42a) is added.
- (e) The device 100 is shown in cross-section. It has a substrate 1 with a bottom layer 5. A central layer 1 is present. A top layer 2 is added. A top layer 3 is added. A patterned layer 4 (containing 41 and 42) is added. A top layer 6a is added. A top layer 8a is added. A patterned layer 4a (containing 41a and 42a) is added.
- (f) The device 200 is shown in cross-section. It has a substrate 1 with a bottom layer 5. A central layer 1 is present. A top layer 2 is added. A top layer 3 is added. A patterned layer 4 (containing 41 and 42) is added. A top layer 6a is added. A top layer 8a is added. A patterned layer 4a (containing 41a and 42a) is added.

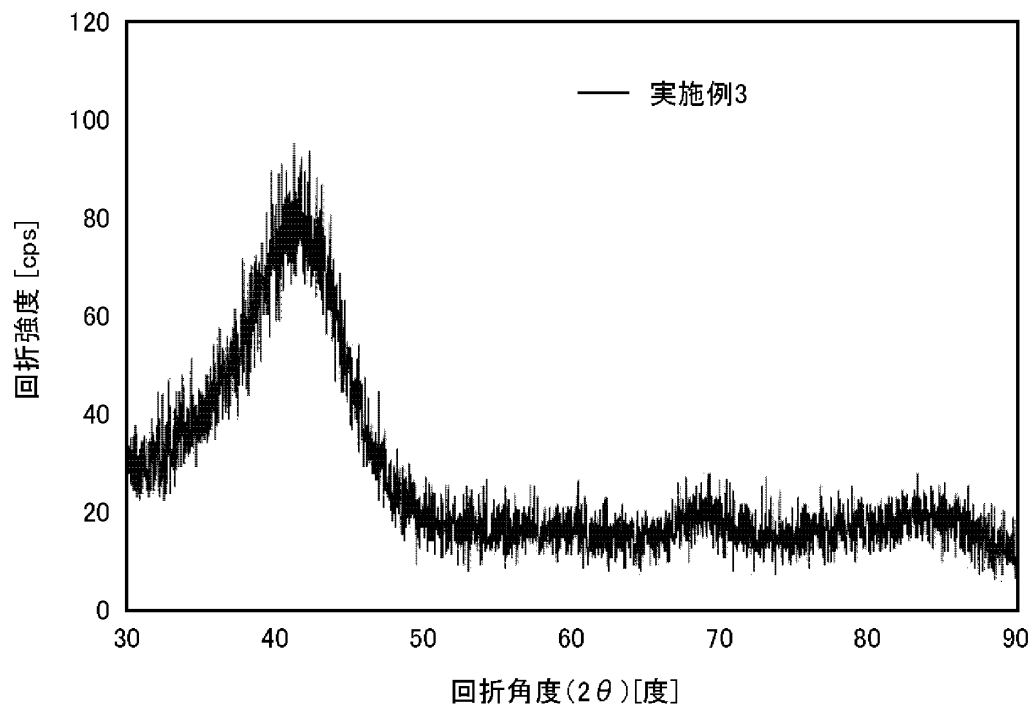
[図4]



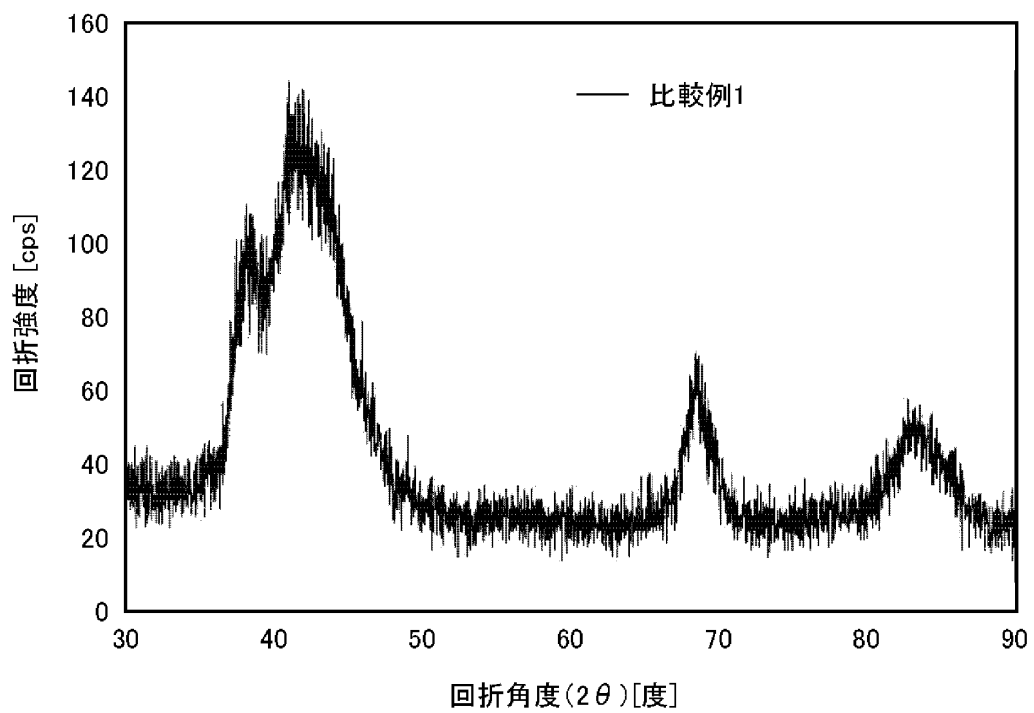
[図5]



[図6]



[図7]



[図8]

