COMPUTER ARCHITECTURE THAT ENABLES OPERATING CIRCUITRY AND PROGRAMS OF AN ASSOCIATED MASS STORAGE PERIPHERAL DEVICE TO BE LOCATED REMOTELY FROM THE DEVICE

ABSTRACT OF THE DISCLOSURE

A computer architecture for use in a host computer with an associated mass storage peripheral device of the type having a computer readable medium, a sensor for detecting information on the medium, and at least a data amplifier for amplifying the information detected on the medium to produce an amplified data signal has an interface between the mass storage peripheral device and the host computer and at least a controller circuit for the mass storage peripheral device in the host computer for receiving the information detected on the medium for use in the computer. Preferably, the controller circuit delivers the information onto a bus mastering bus, such as a PCI or 1394 type bus, in the host computer.

FIGURE 1