

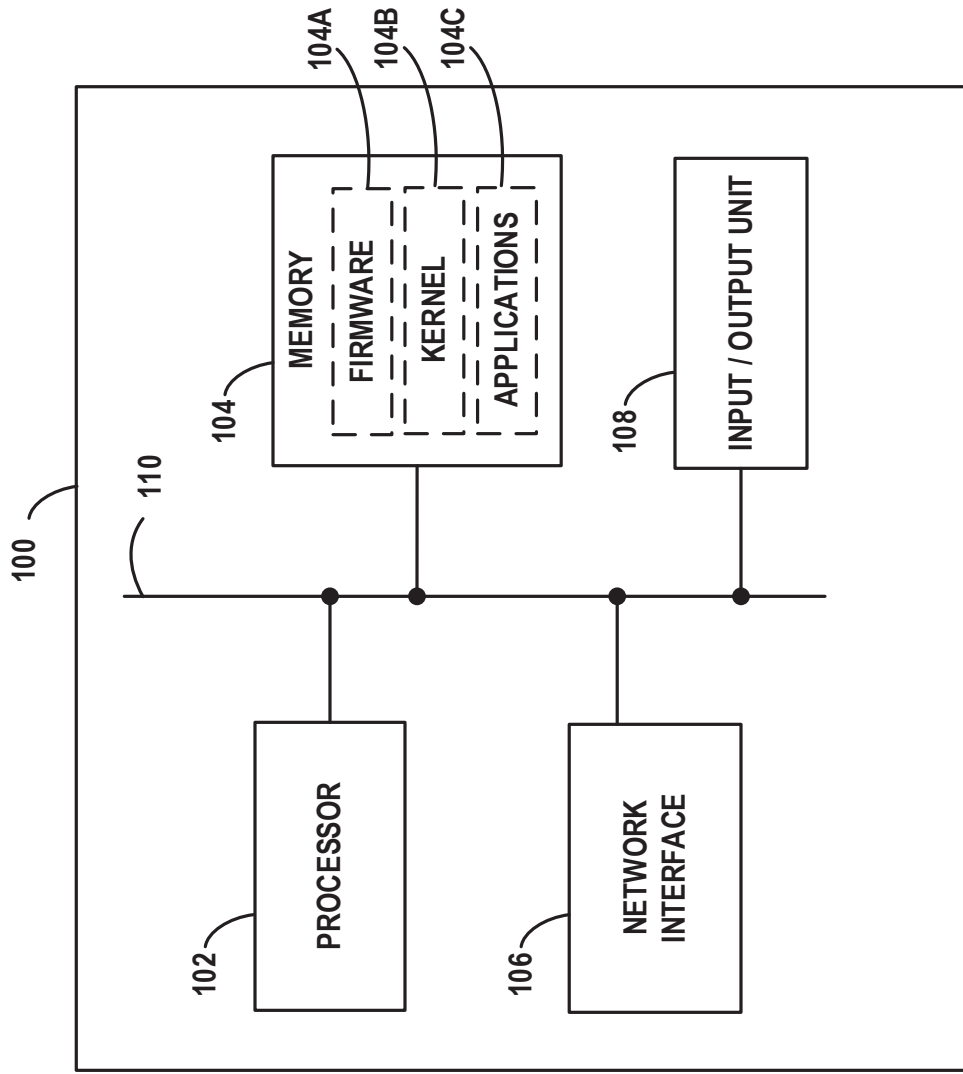


FIG. 1

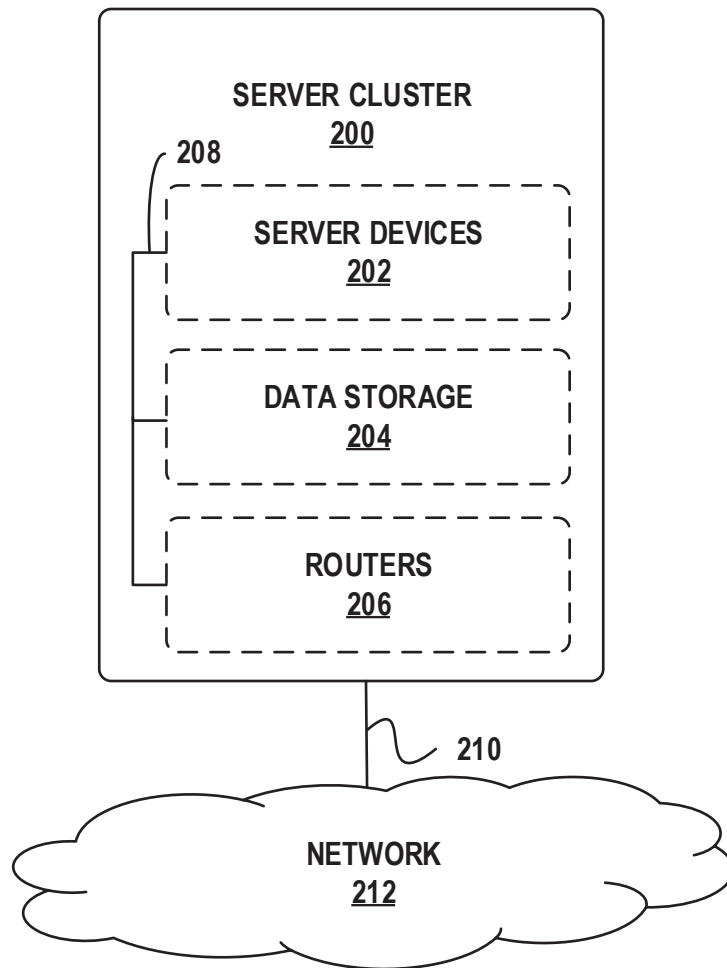


FIG. 2

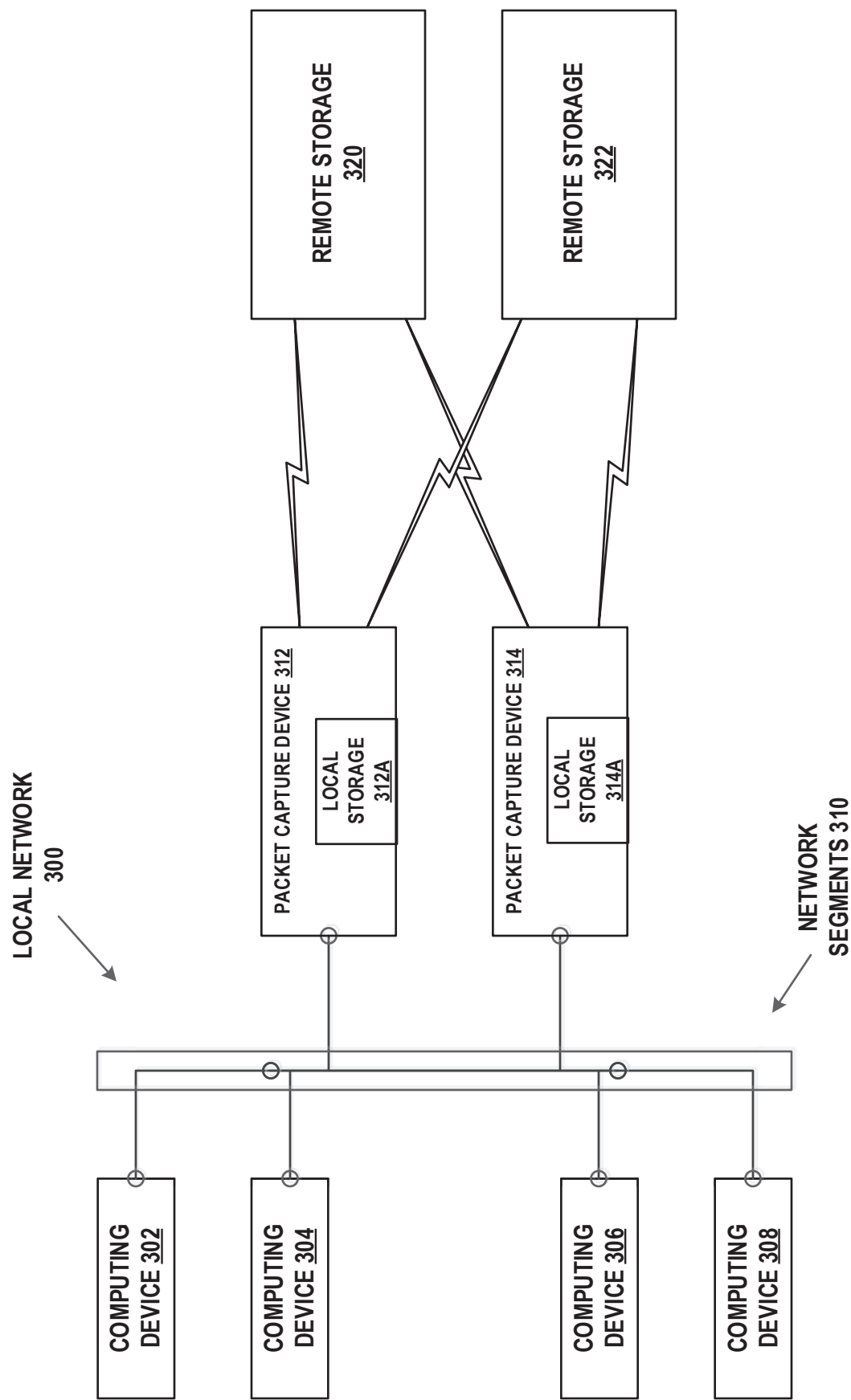


FIG. 3

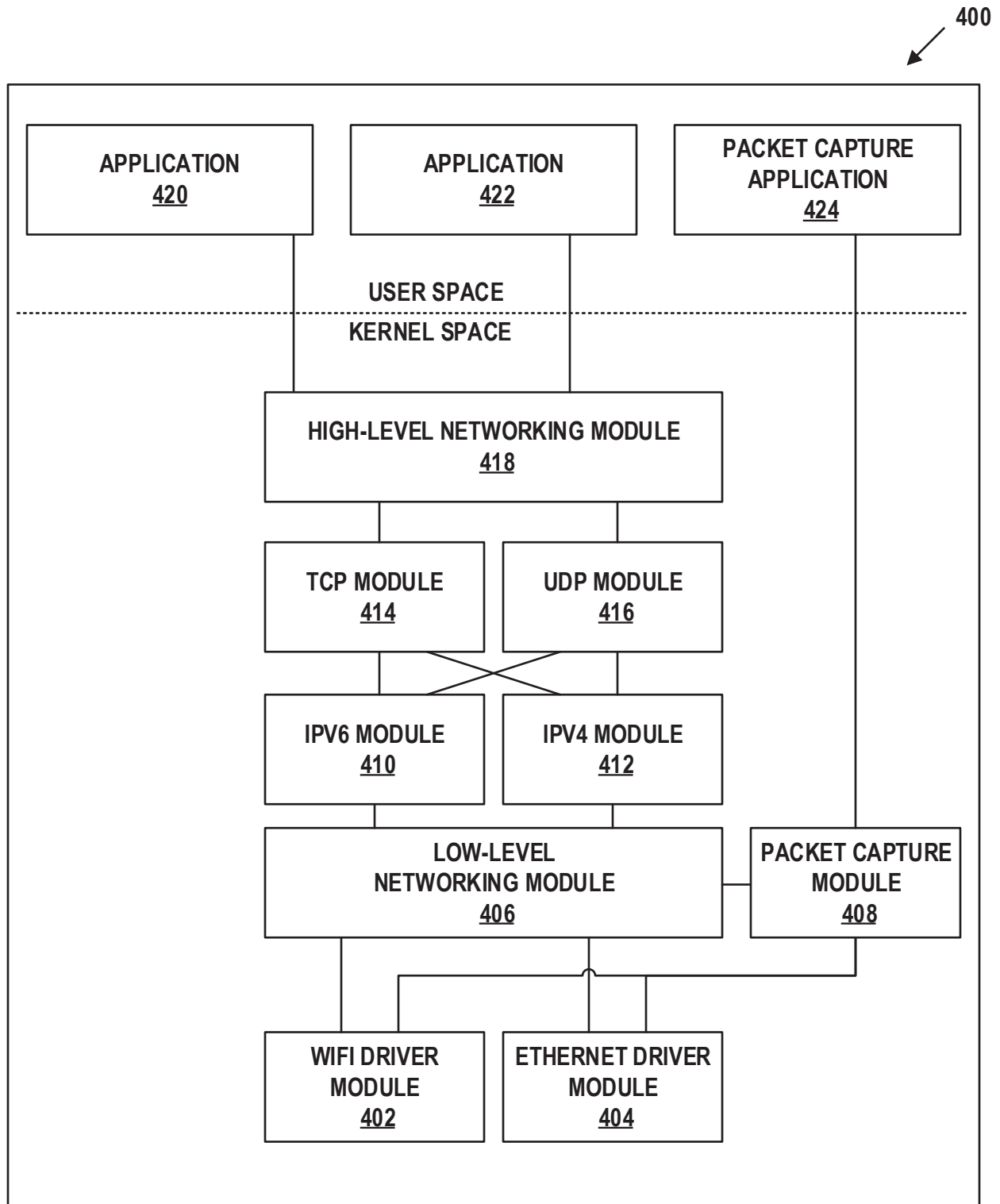


FIG. 4

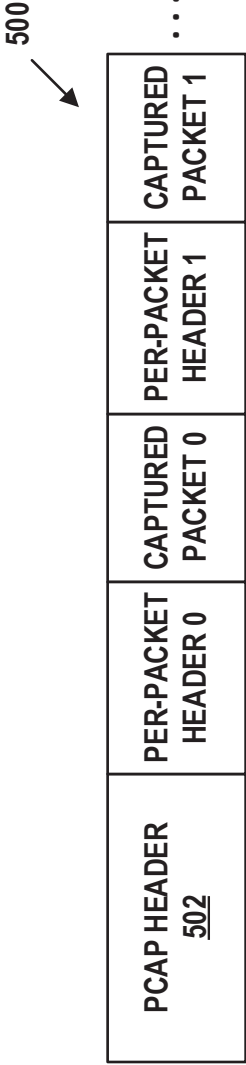
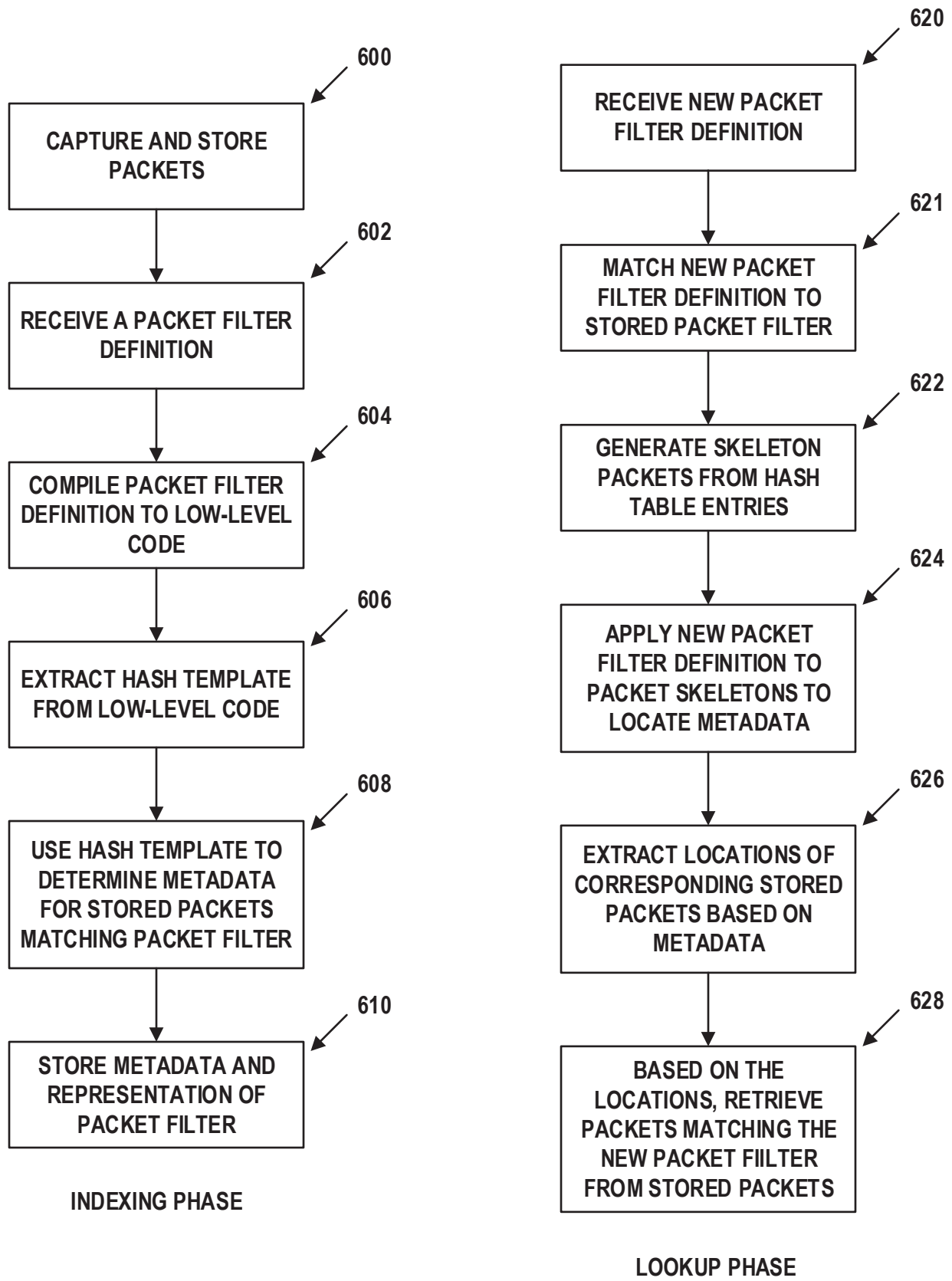


FIG. 5A



FIG. 5B

FIG. 5C

**FIG. 6**

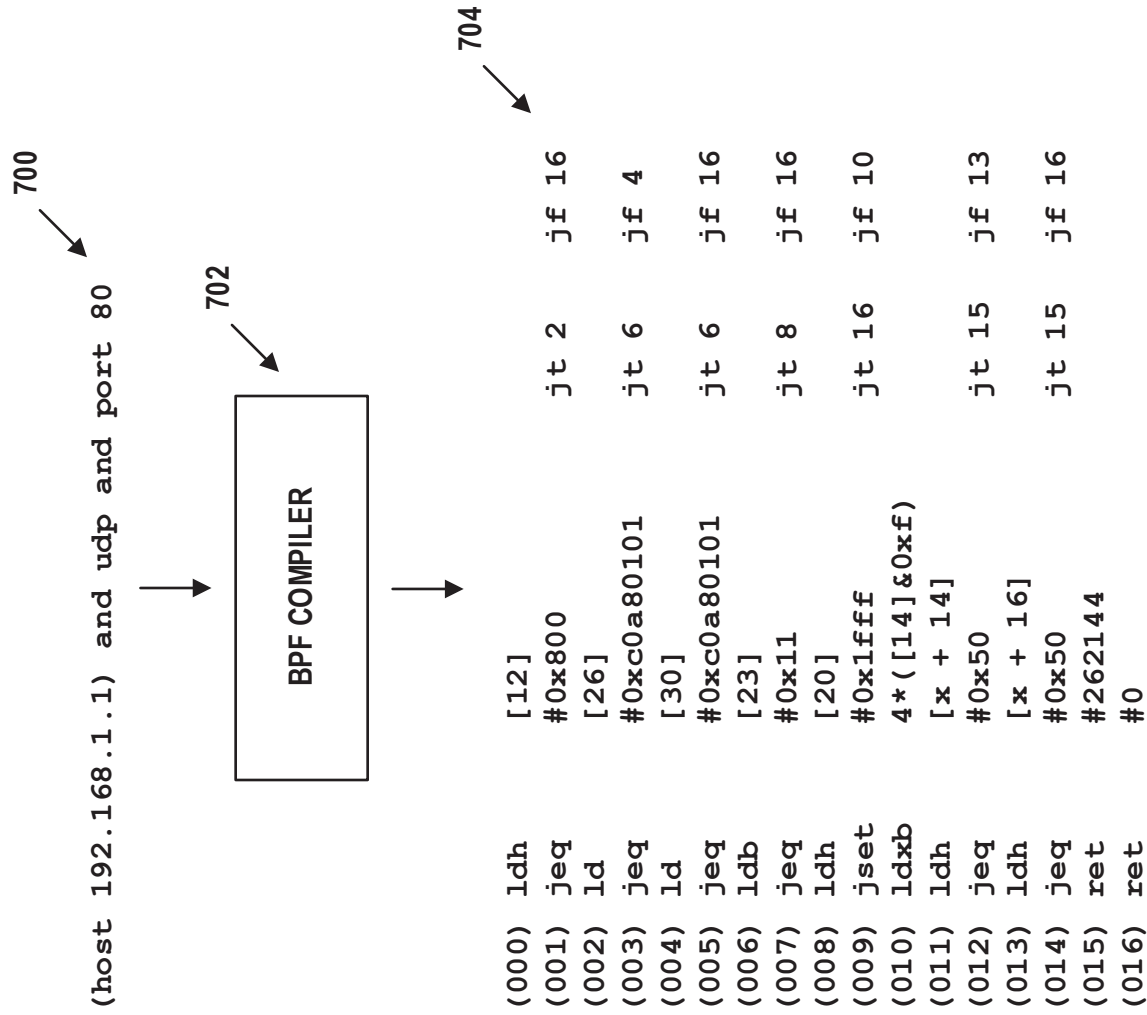
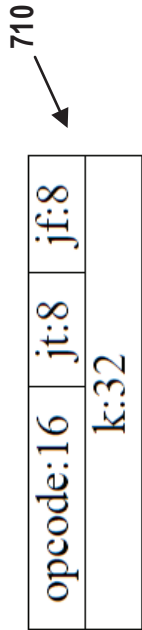


FIG. 7A



710

<i>opcodes</i>	<i>addr modes</i>				
ldb	[k]		[x+k]		
ldh	[k]		[x+k]		
ld	#k	#len	M[k]	[k]	[x+k]
ldx	#k	#len	M[k]	4 * ([k] & 0xf)	
st	M[k]				
stx	M[k]				
jmp	L				
jeq	#k, Lt, Lf				
jgt	#k, Lt, Lf				
jge	#k, Lt, Lf				
jset	#k, Lt, Lf				
add	#k				x
sub	#k				x
mul	#k				x
div	#k				x

</

712

714

FIG. 7B

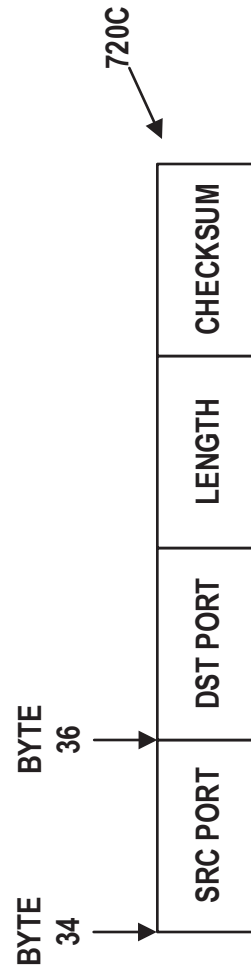
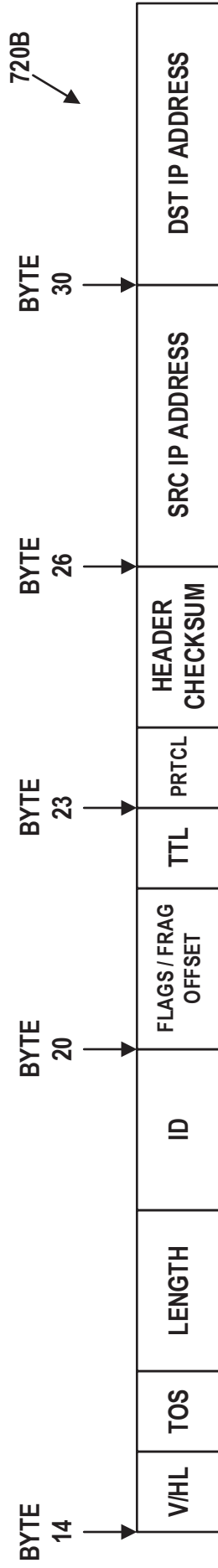
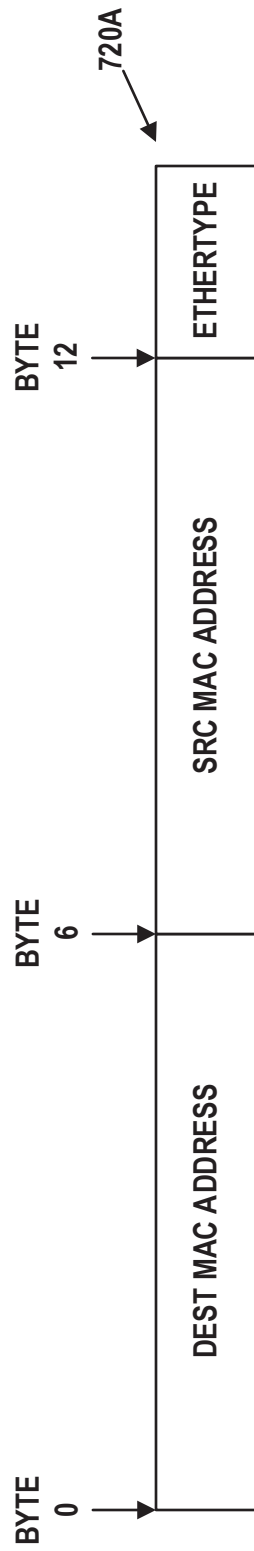


FIG. 7C

(000)	ldh	[12]	Load 2 bytes at offset 12
(002)	ld	[26]	Load 4 bytes at offset 26
(004)	ld	[30]	Load 4 bytes at offset 30
(006)	ldb	[23]	Load 1 byte at offset 23
(008)	ldh	[20]	Load 2 bytes at offset 20
(010)	ldxb	$4 * ([14] \& 0xf)$	Load 1 byte at offset 14 as modified into x
(011)	ldh	$[x + 14]$	Load 2 bytes at offset x+14
(013)	ldh	$[x + 16]$	Load 2 bytes at offset x+16

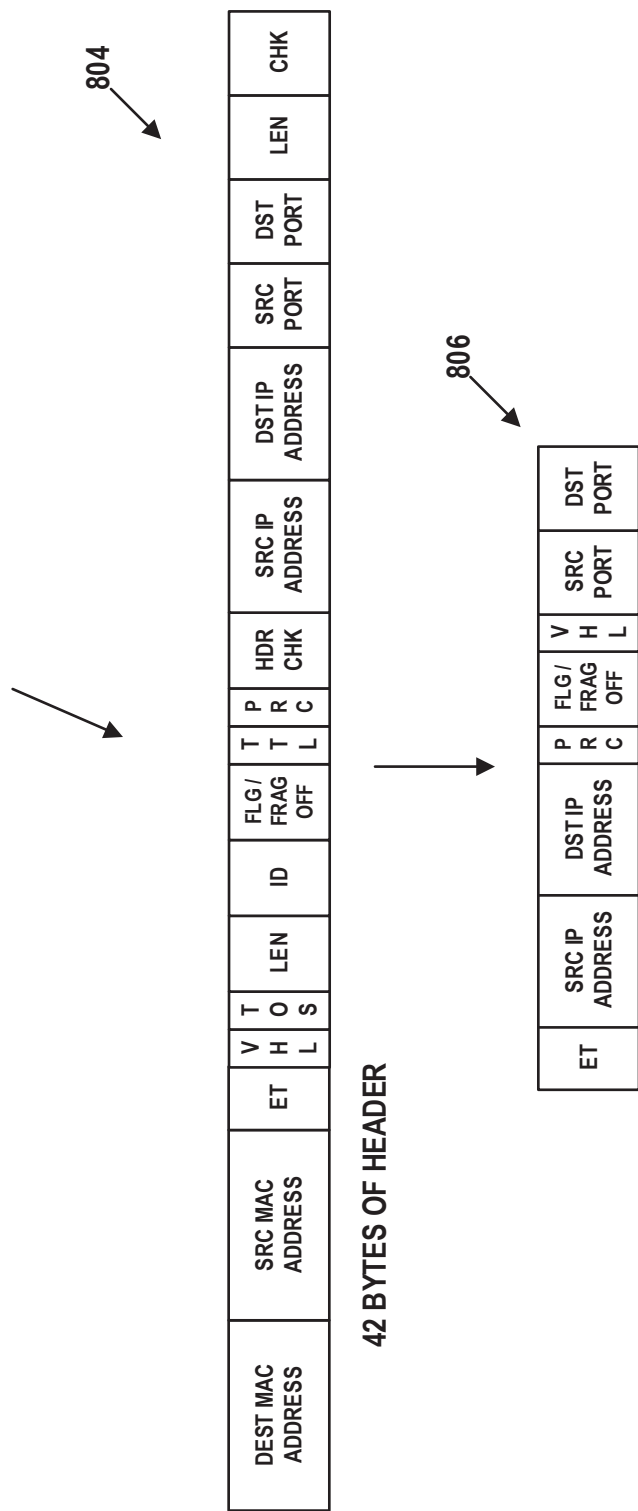


FIG. 8A

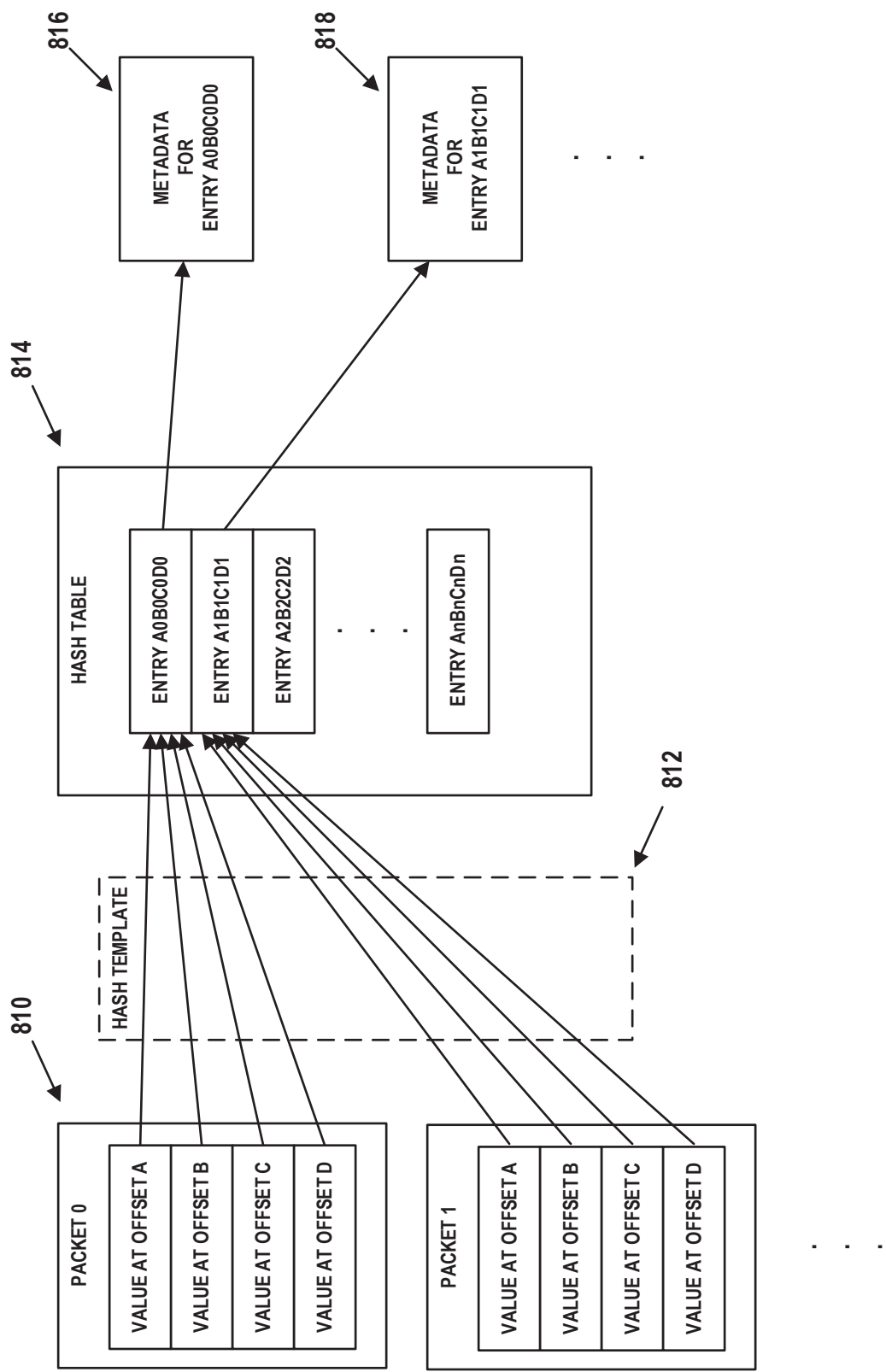
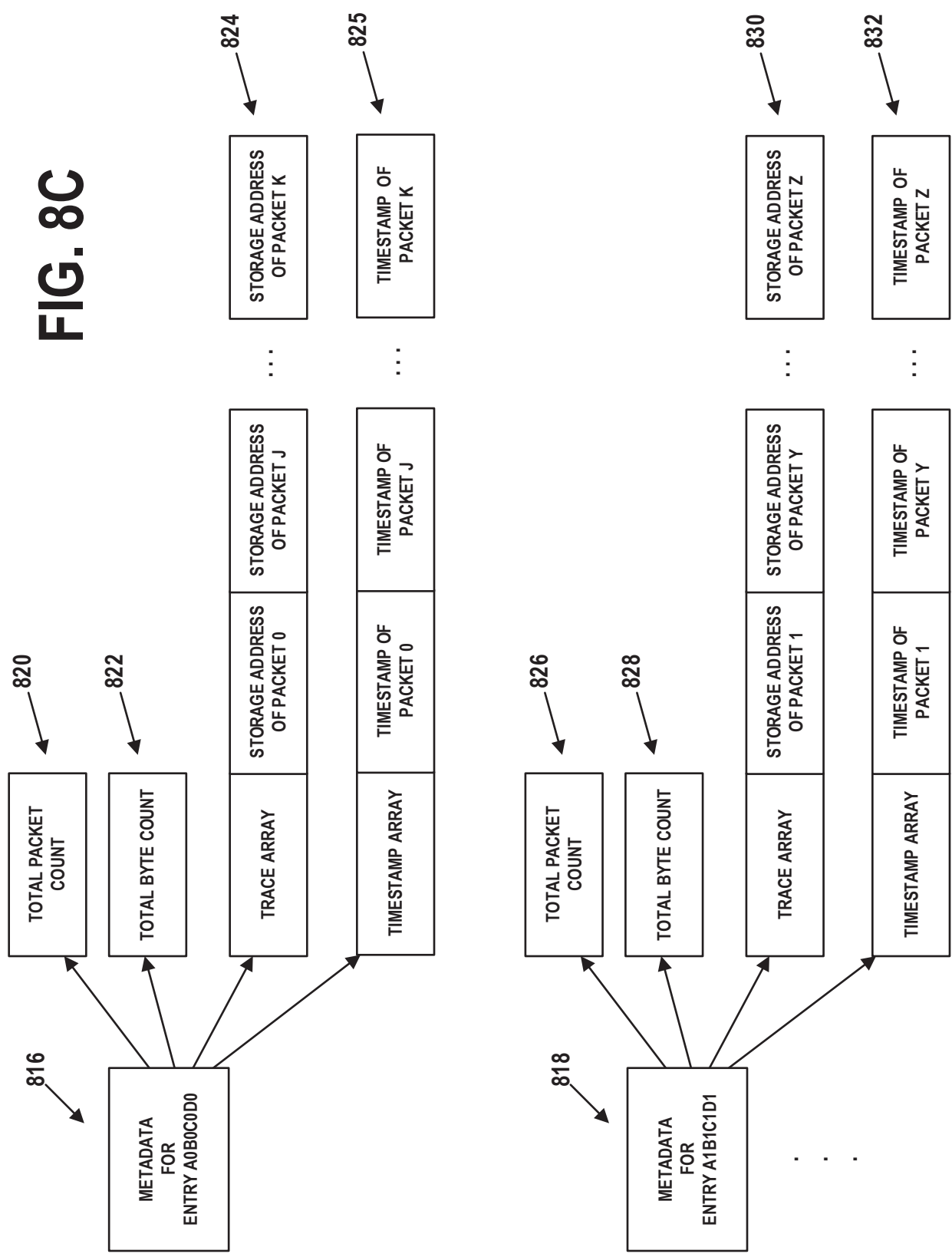


FIG. 8B

FIG. 8C



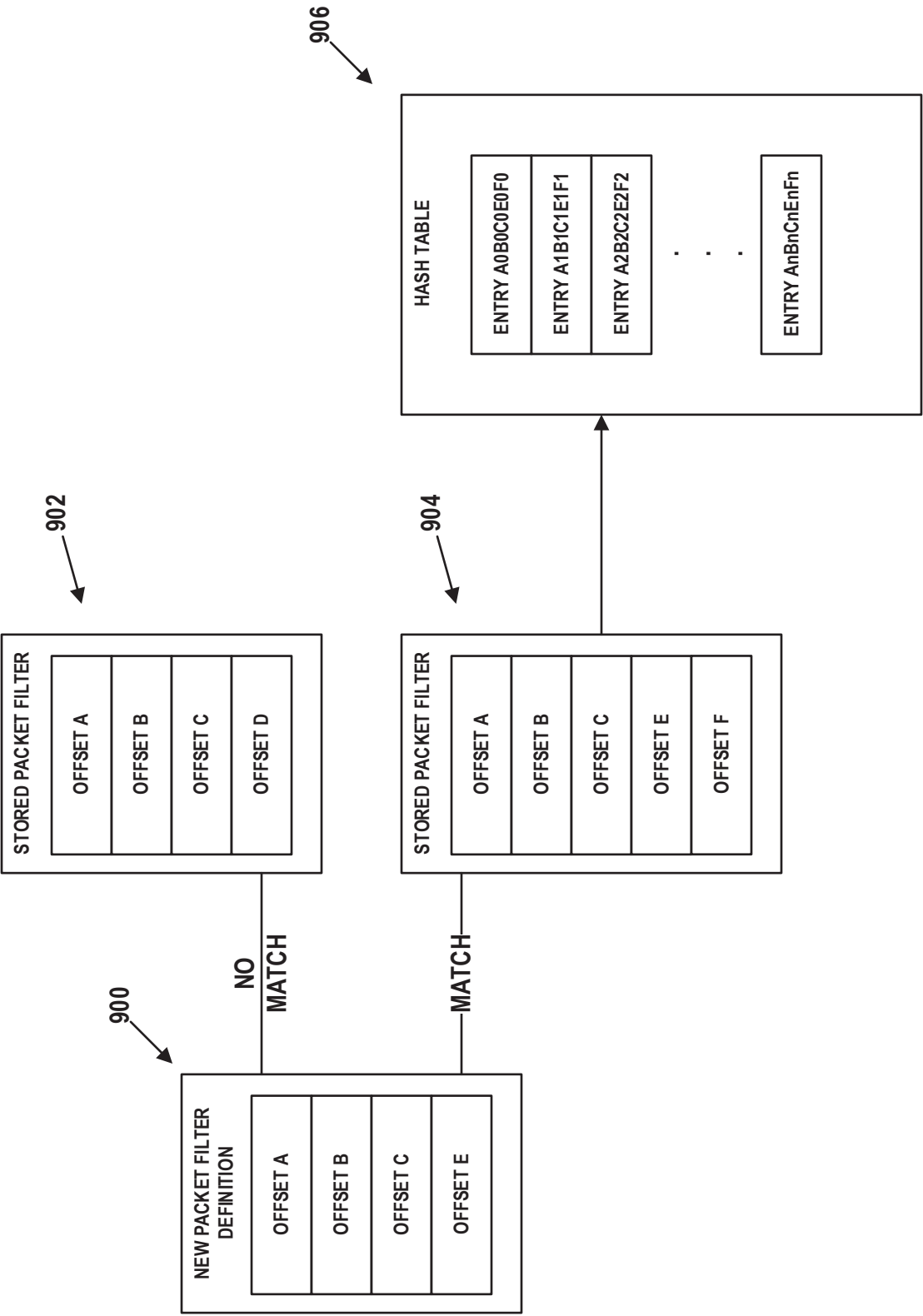


FIG. 9A

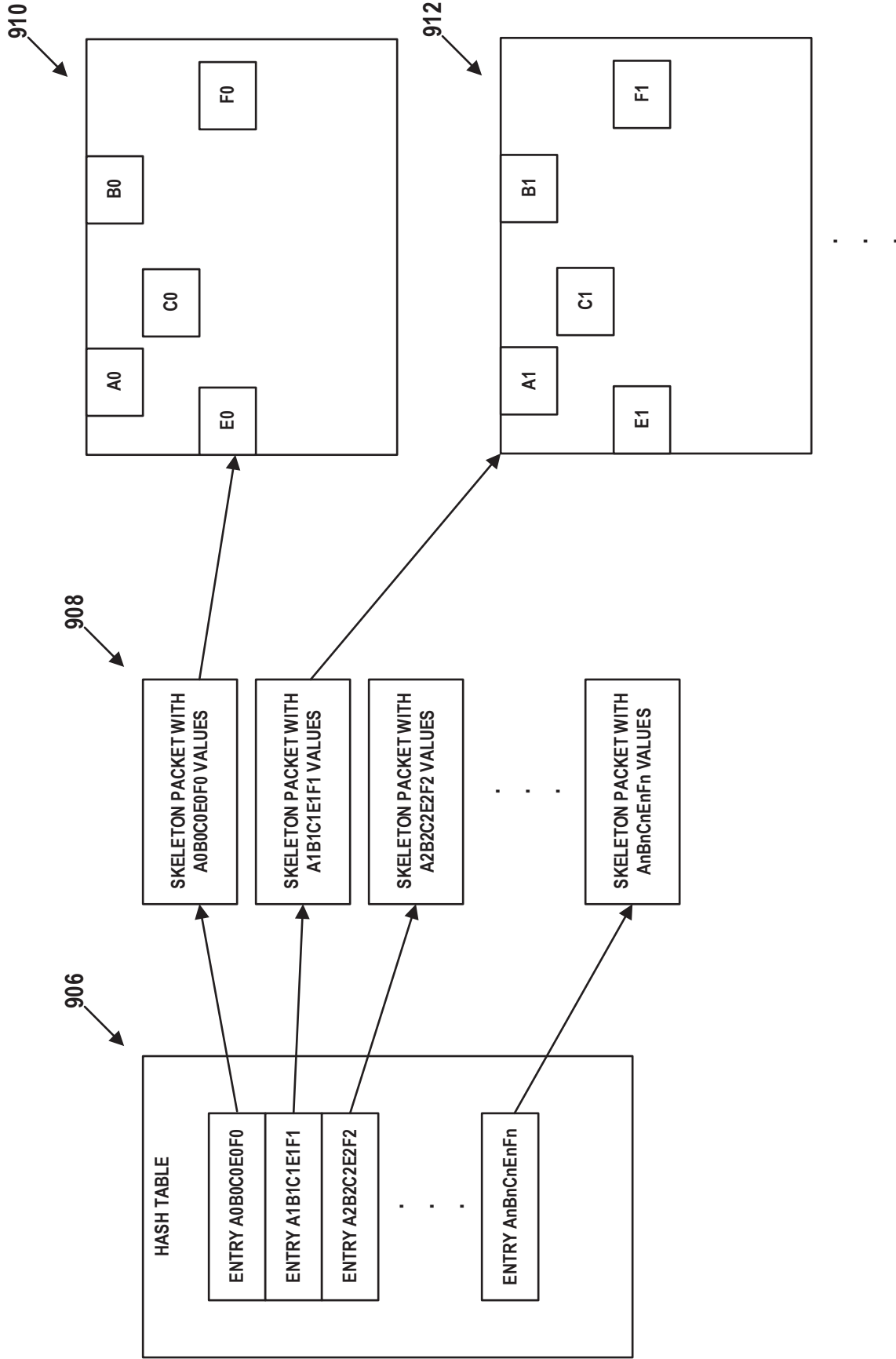


FIG. 9B

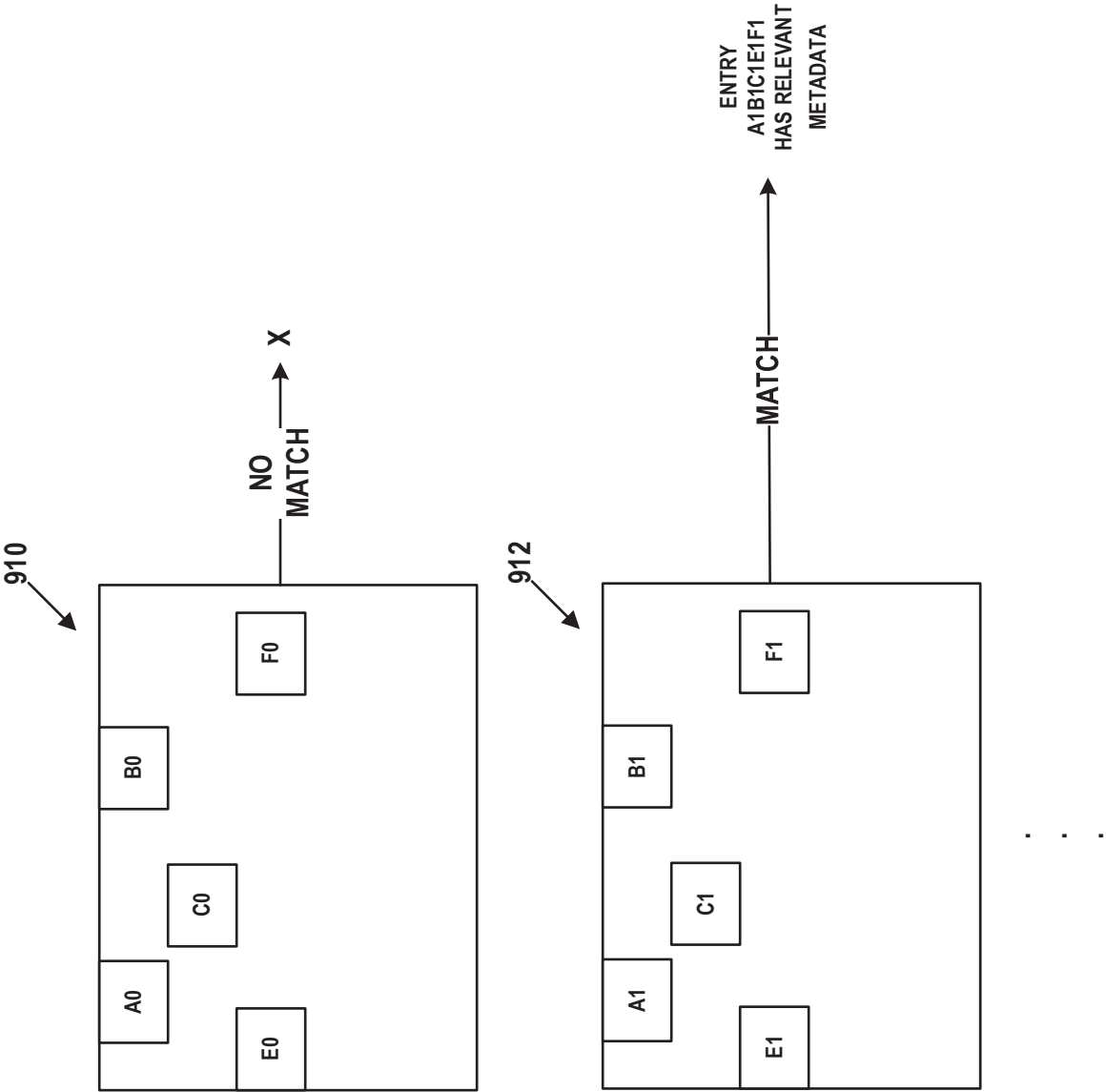


FIG. 9C

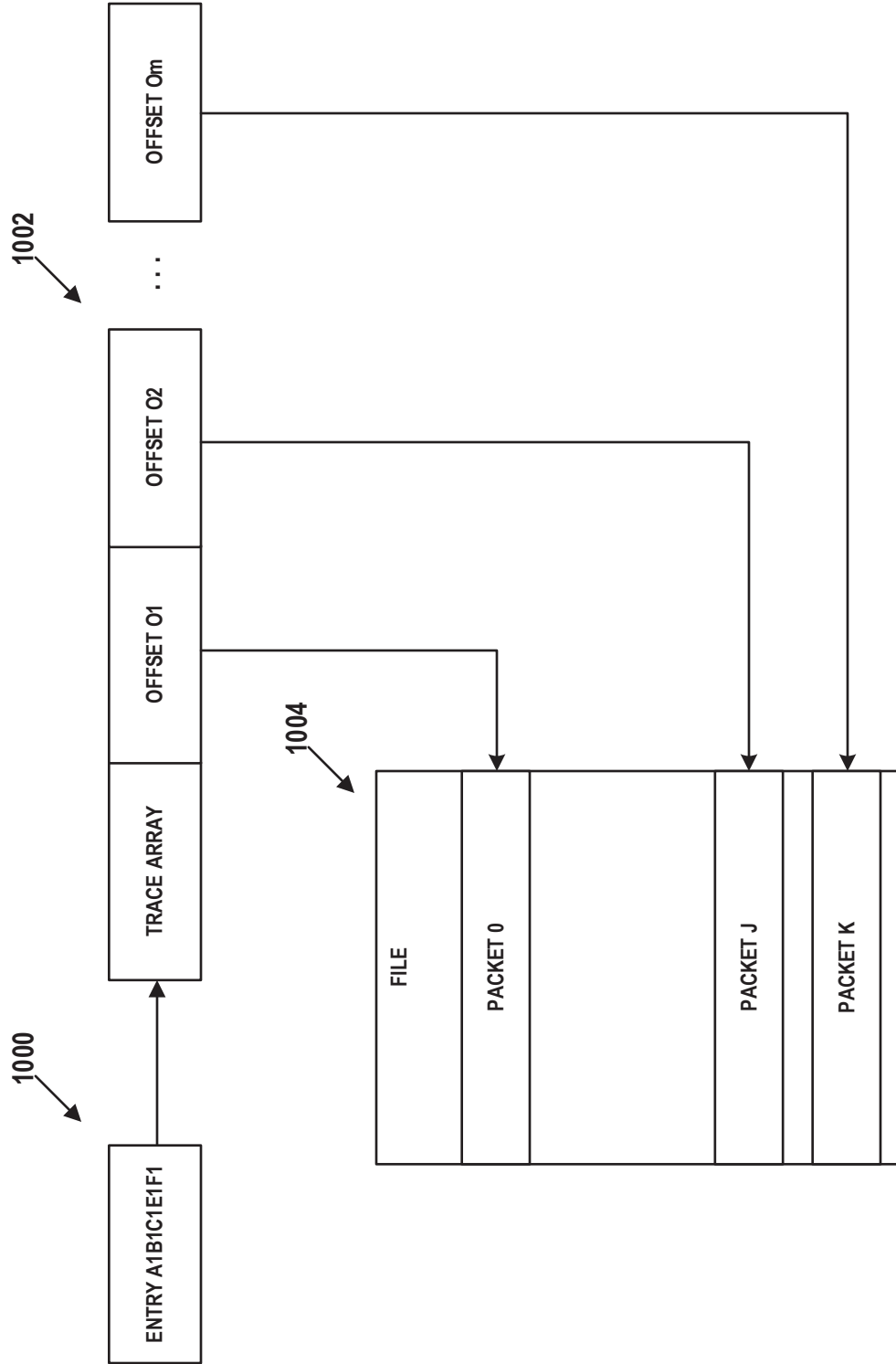


FIG. 10A

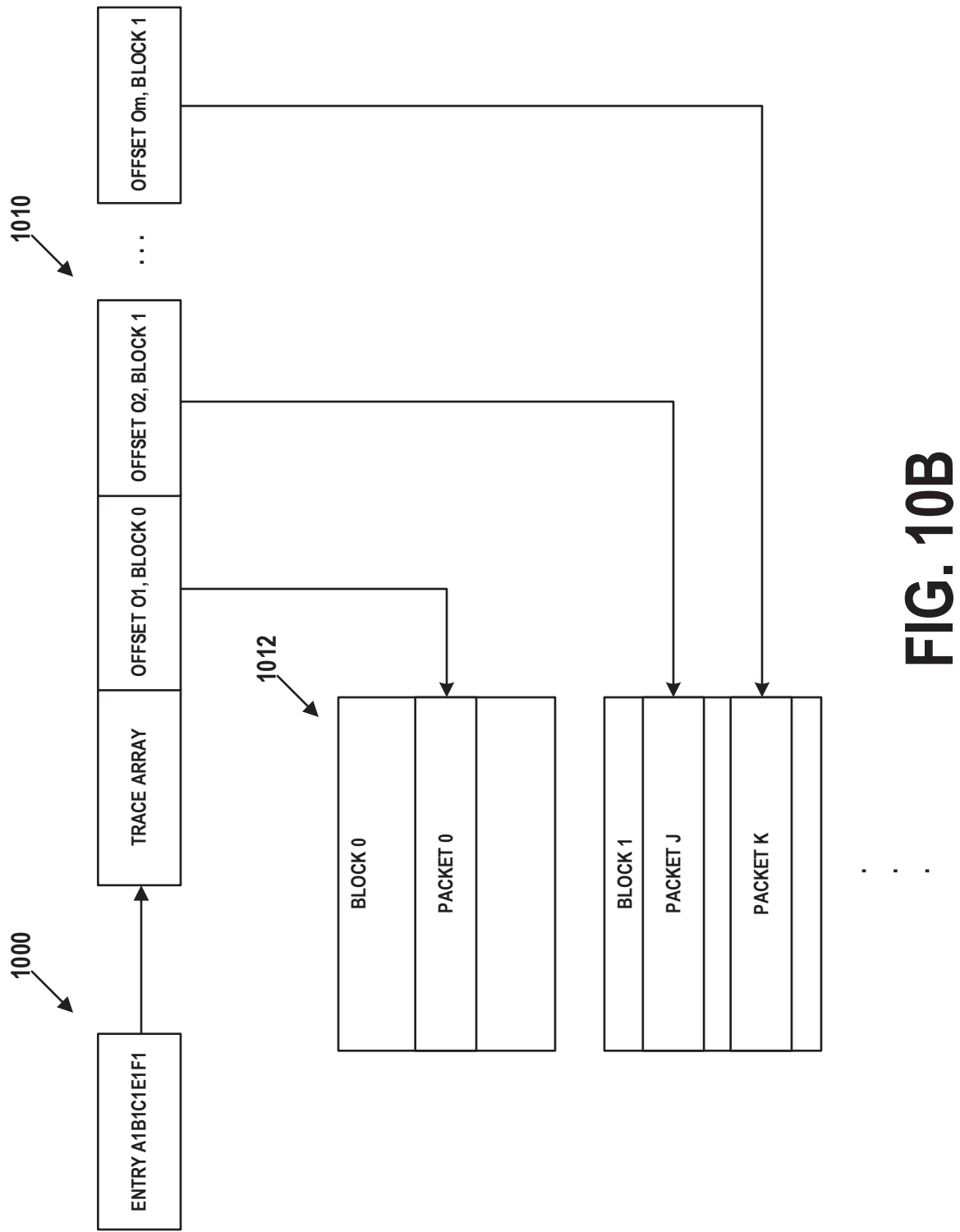


FIG. 10B

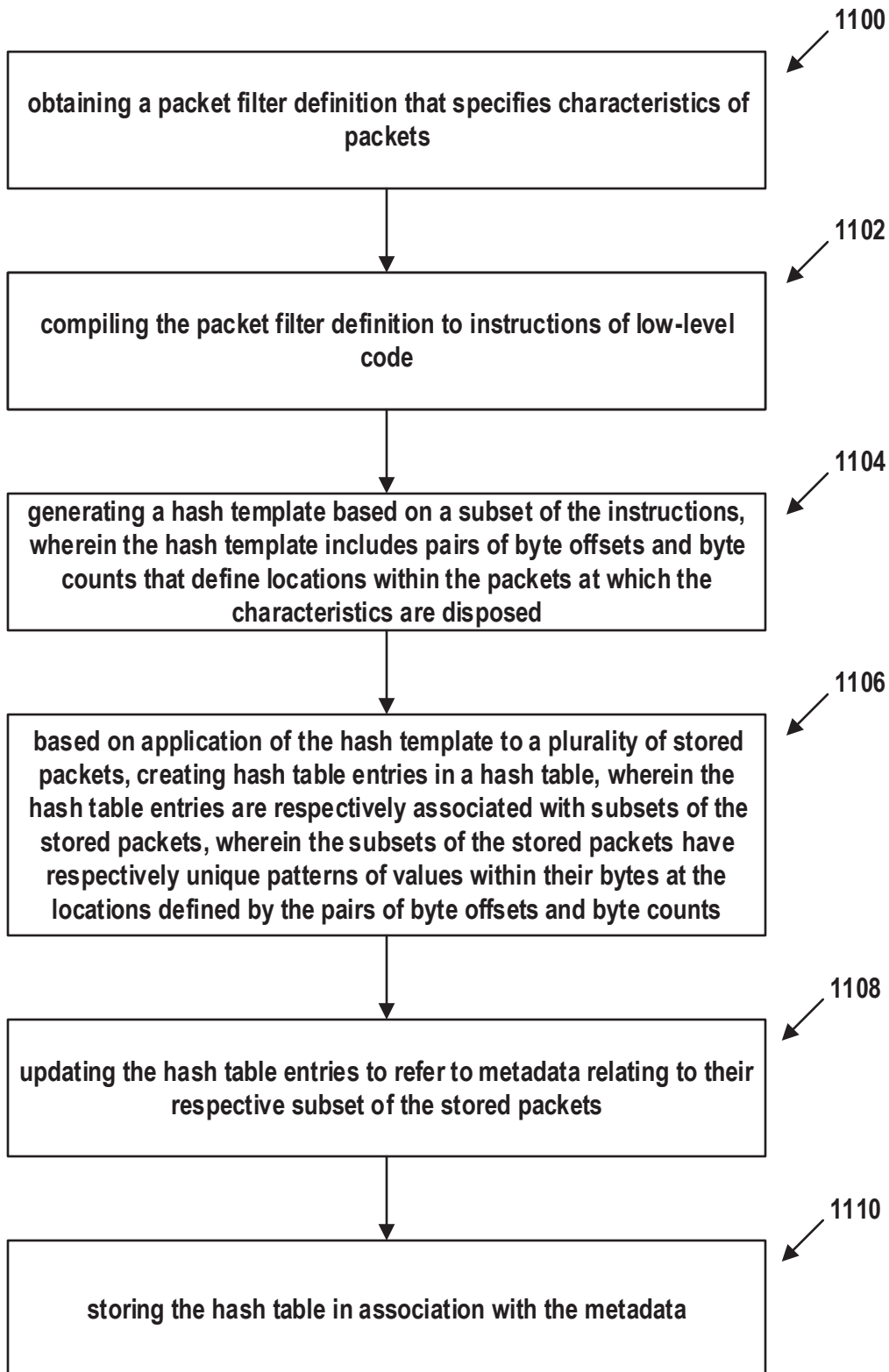


FIG. 11A

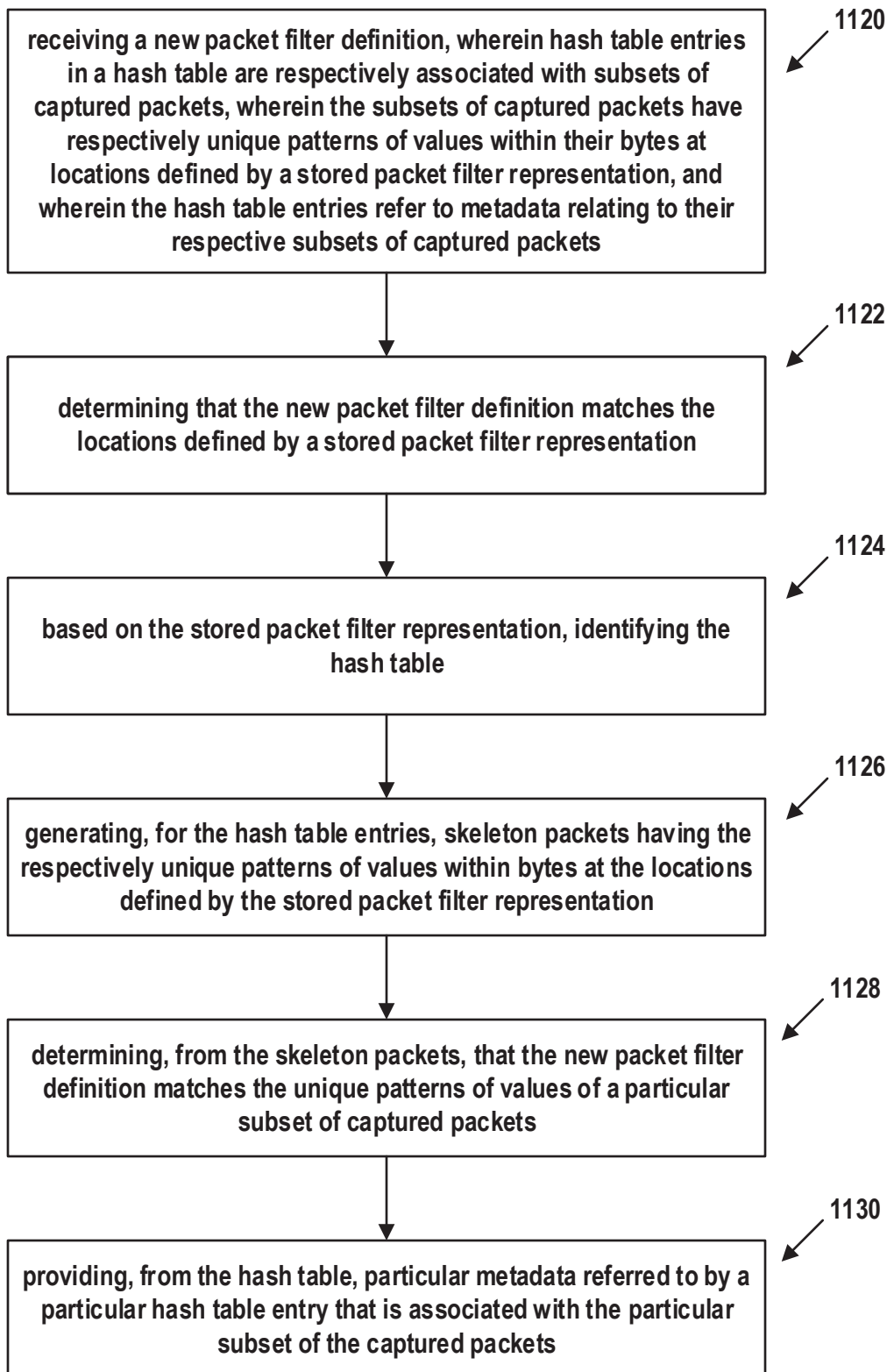


FIG. 11B