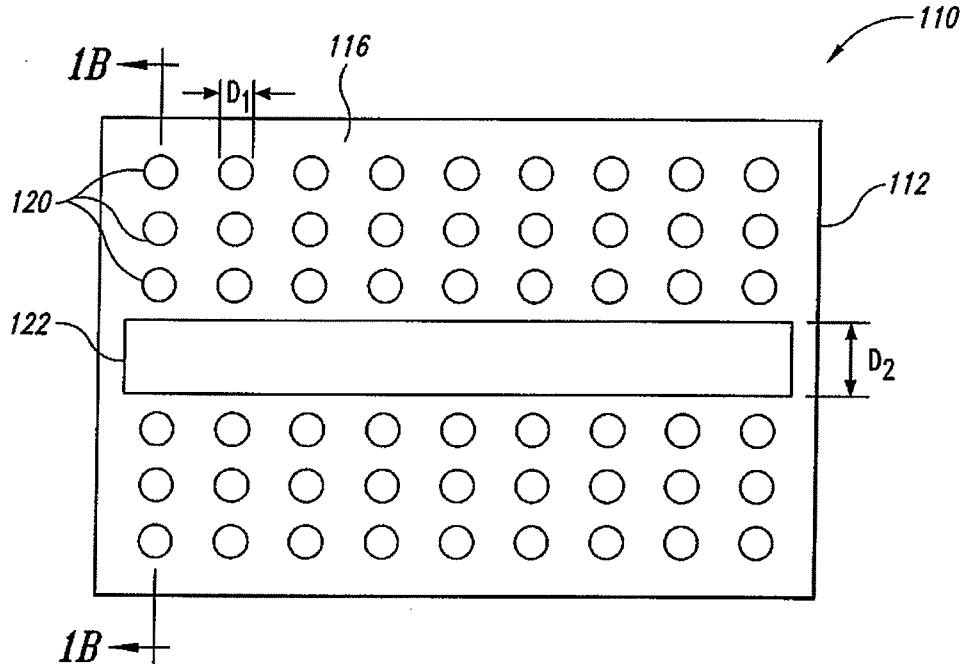

110
116
1B
D1
120
112
122
D2
1B

Fig. 1A

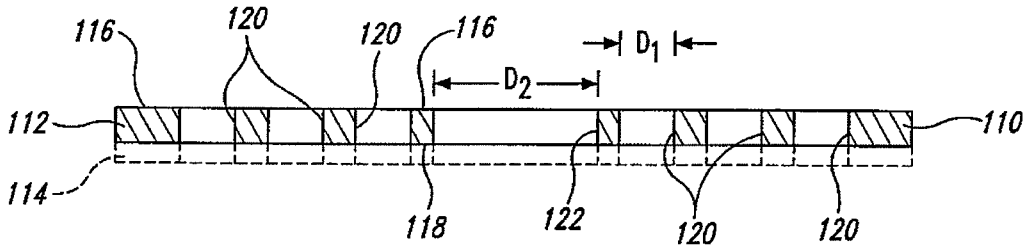

116
120
120
116
D1
D2
112
110
114
118
122
120
120

Fig. 1B

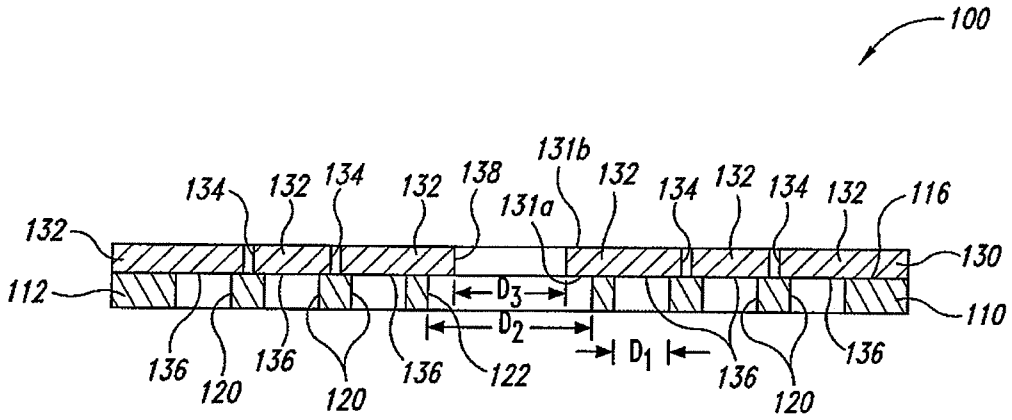

100
131b
134
132
134
132
138
131a
132
134
132
134
132
116
132
130
112
110
D_3
D_2
D_1
136
120
136
120
136
122
136
120
136

Fig. 2

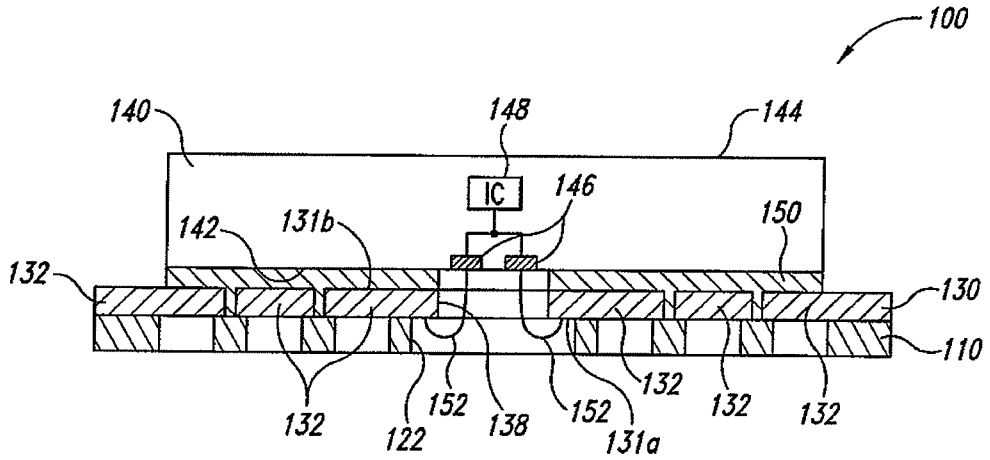

100
140
148
144
IC
146
142
131b
150
132
130
110
132
152
138
152
132
132
132
122
131a

Fig. 3

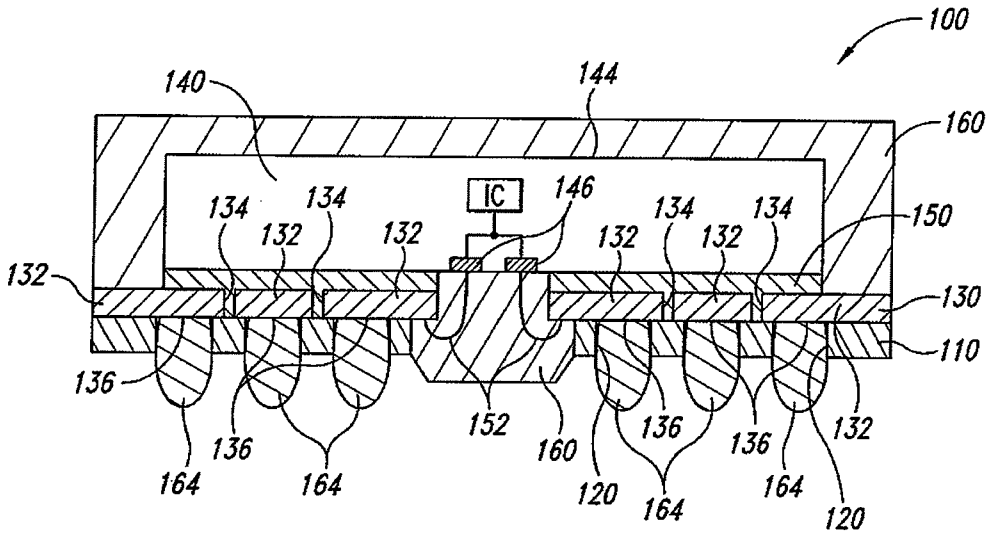

100
140
144
160
IC
146
134
134
132
132
132
134
134
132
150
132
130
110
136
136
136
152
160
136
136
132
164
164
120
164
164
120

Fig. 4

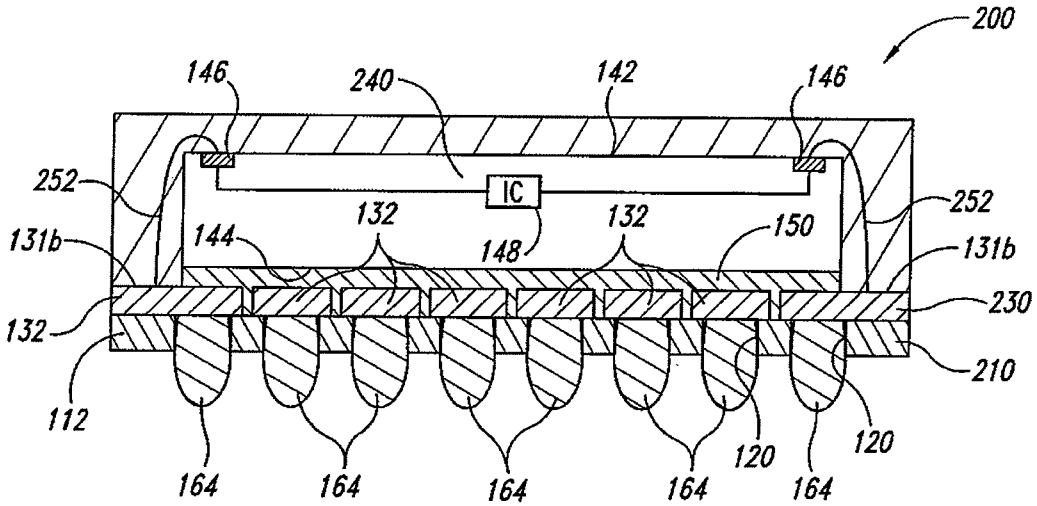

200
146
240
142
146
252
IC
252
132
132
131b
144
148
150
131b
230
132
210
112
164
164
164
164
120
120
164

Fig. 5

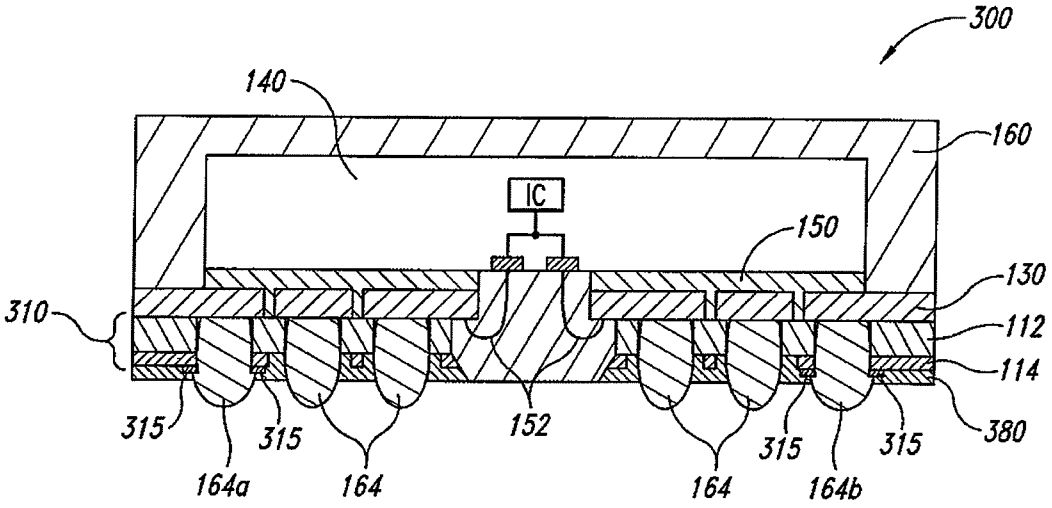

300
140
160
IC
150
130
310
112
114
315
315
152
315
315
380
164a
164
164
164b

Fig. 6

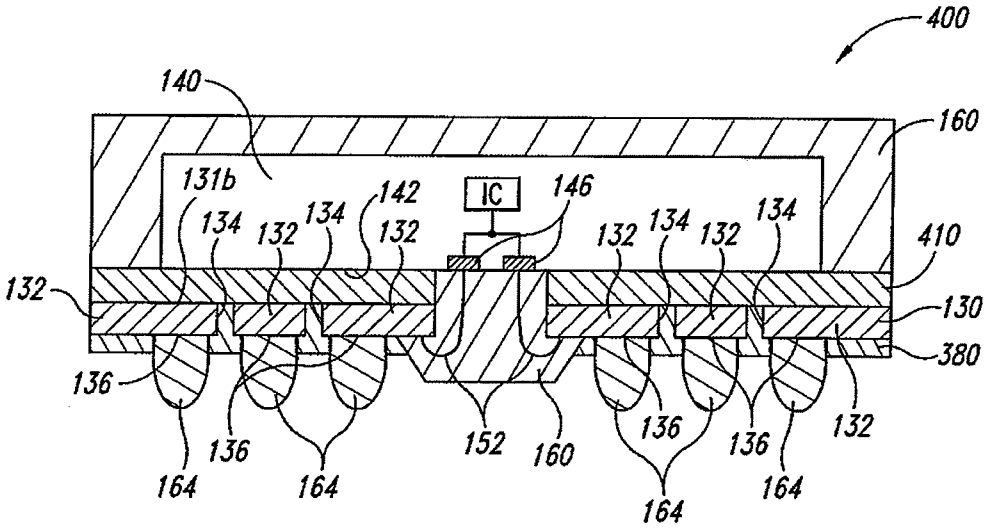

400
140
160
131b
IC
146
142
134
132
134
132
132
134
132
134
410
132
130
380
136
136
152
160
136
136
132
164
164
164
164

Fig. 7

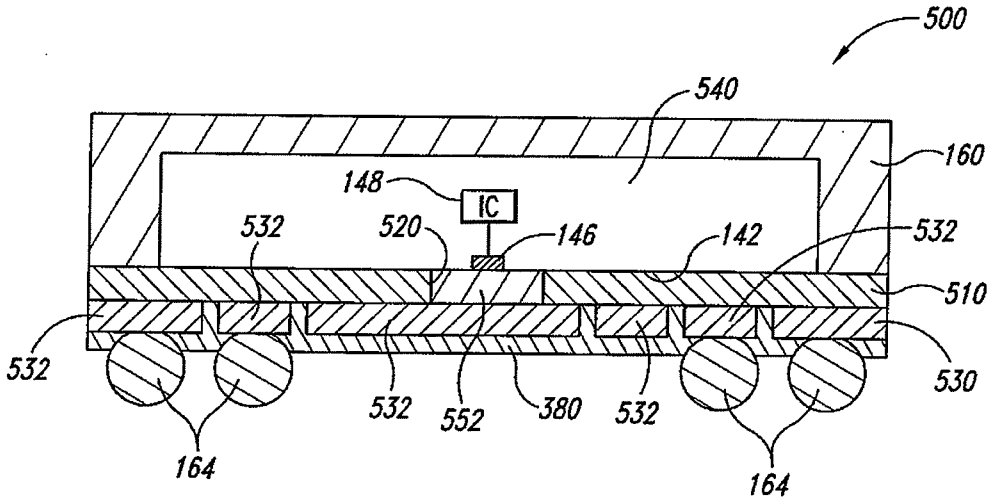

500
540
160
148
IC
532
520
146
142
532
510
532
530
532
552
380
532
164
164

Fig. 8

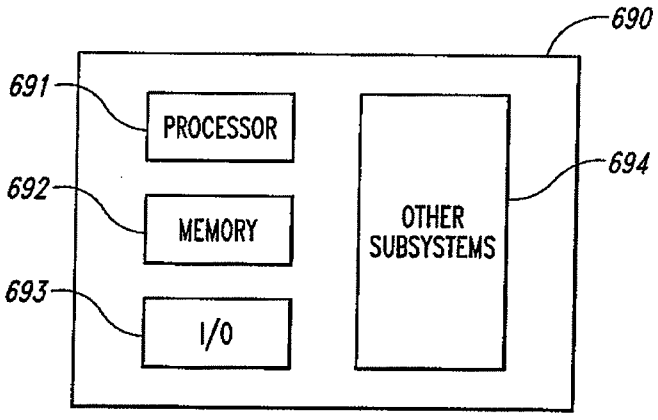

690
691
PROCESSOR
692
MEMORY
693
I/O
OTHER SUBSYSTEMS
694

Fig. 9