

ABSTRACT

Disclosed is an electronic device including a plurality of antennas, a plurality of radio frequency circuits, a first transceiver, a second transceiver, a switching circuit configured to connect the plurality of radio frequency circuits to the first transceiver or the second transceiver, a processor, and a memory. The electronic device may be configured to switch a receiving path by connecting at least one of a plurality of receiving paths to another transceiver based on reception performance of the plurality of receiving paths.

(Fig.2)