

PROCESSING APPARATUS

ABSTRACT

A processing apparatus has a holding unit that includes a transparent plate having an upper surface for holding a wafer thereon and a frame supporting the transparent plate thereon. An image capturing assembly includes an upper image capturing unit that can be positioned above the transparent plate and a lower image capturing unit that can be positioned below the transparent plate, so that the upper image capturing unit and the lower image capturing unit can be positioned in vertically sandwiching relation to the transparent plate. A controller has a storage section that stores therein a corrective function for correcting the brightness levels of brightness gradations of a first image of the wafer captured by the lower image capturing unit positioned below the transparent plate.

[FIG. 4]