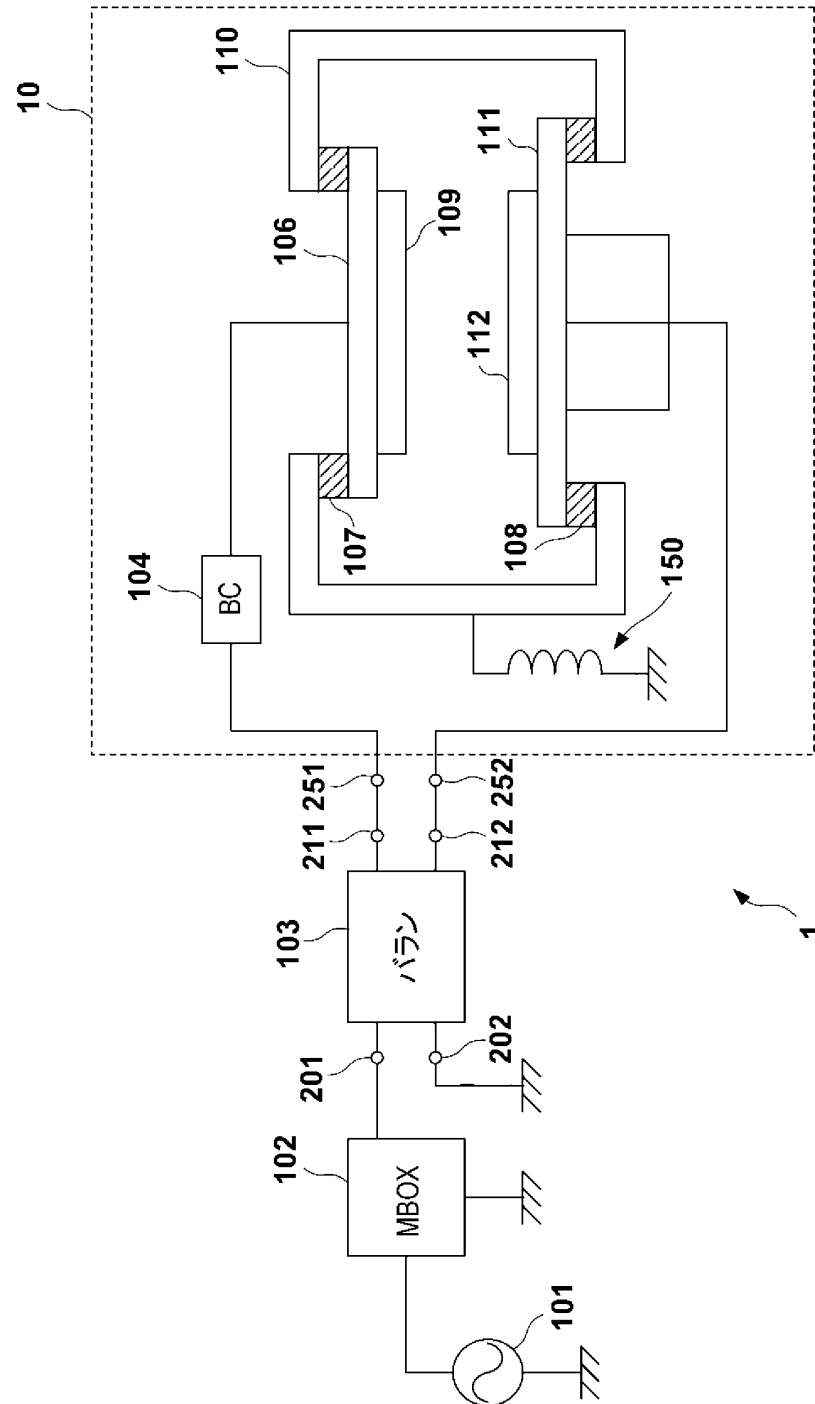
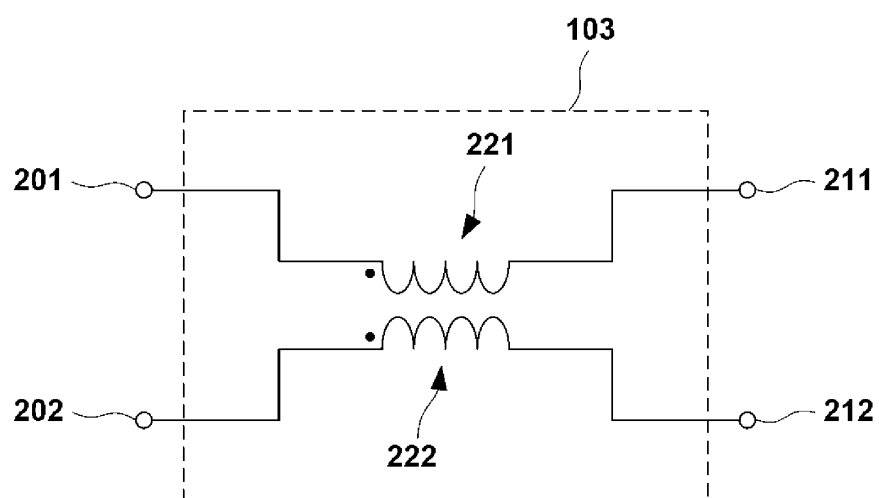


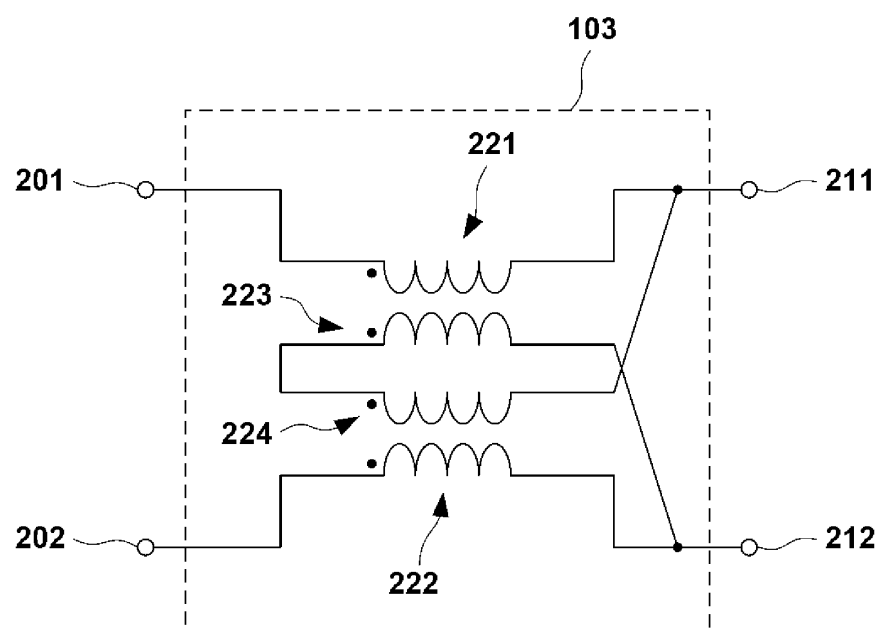
[図1]



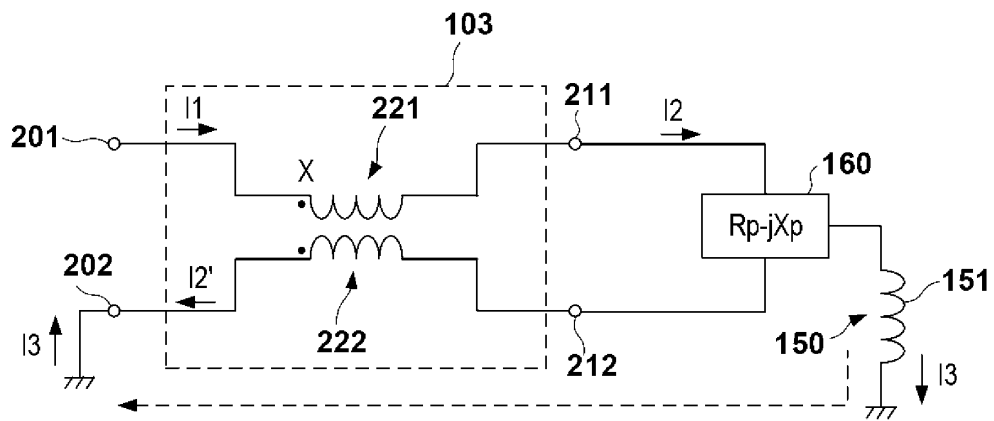
[図2A]



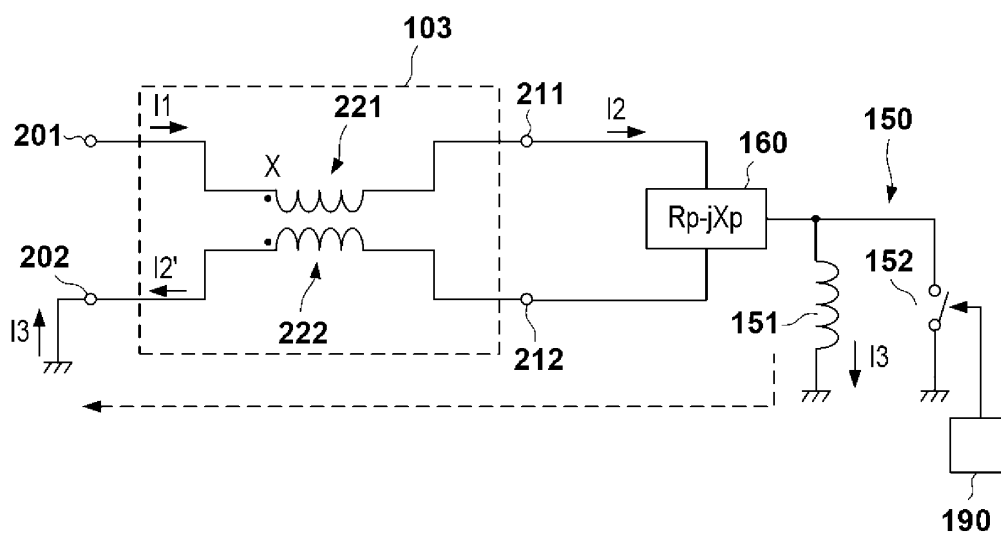
[図2B]



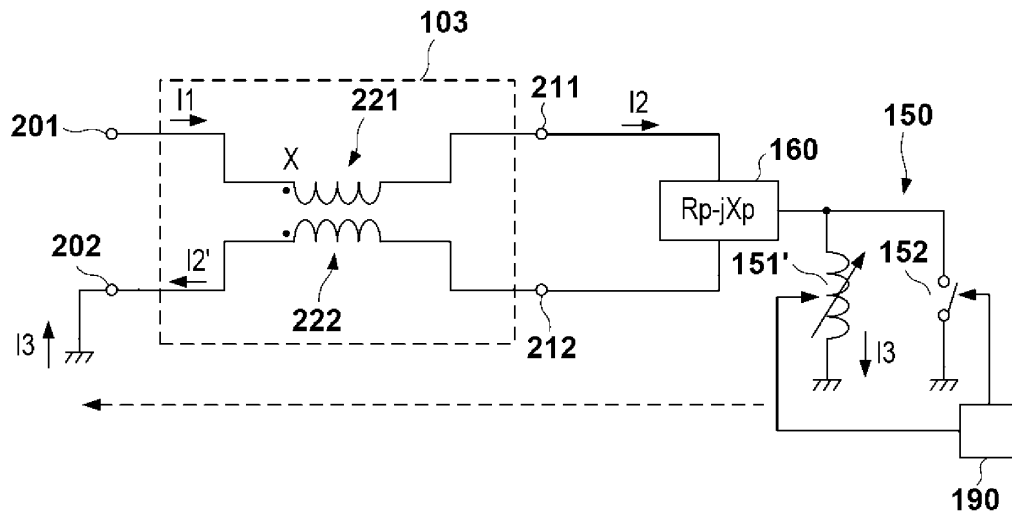
[図3A]



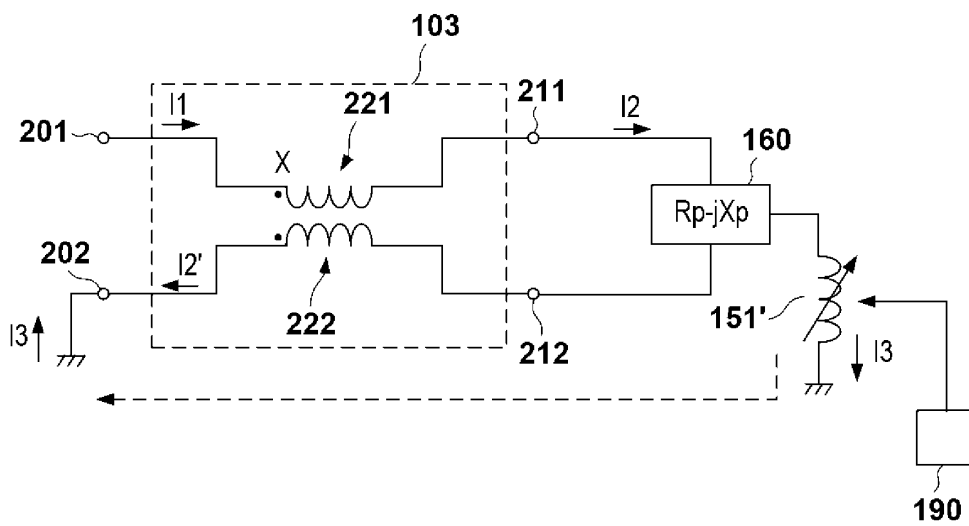
[図3B]

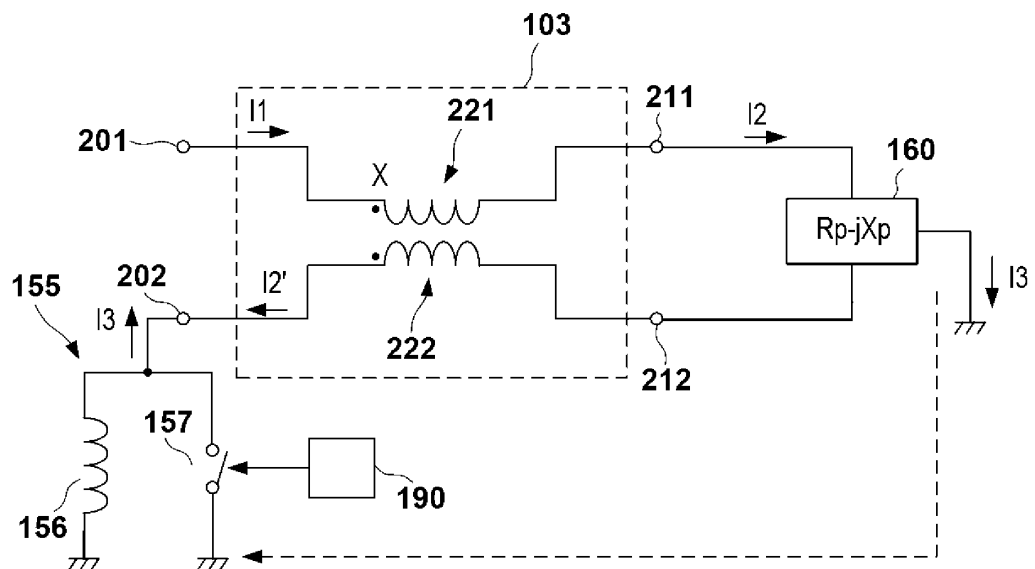


[図4A]



[図4B]





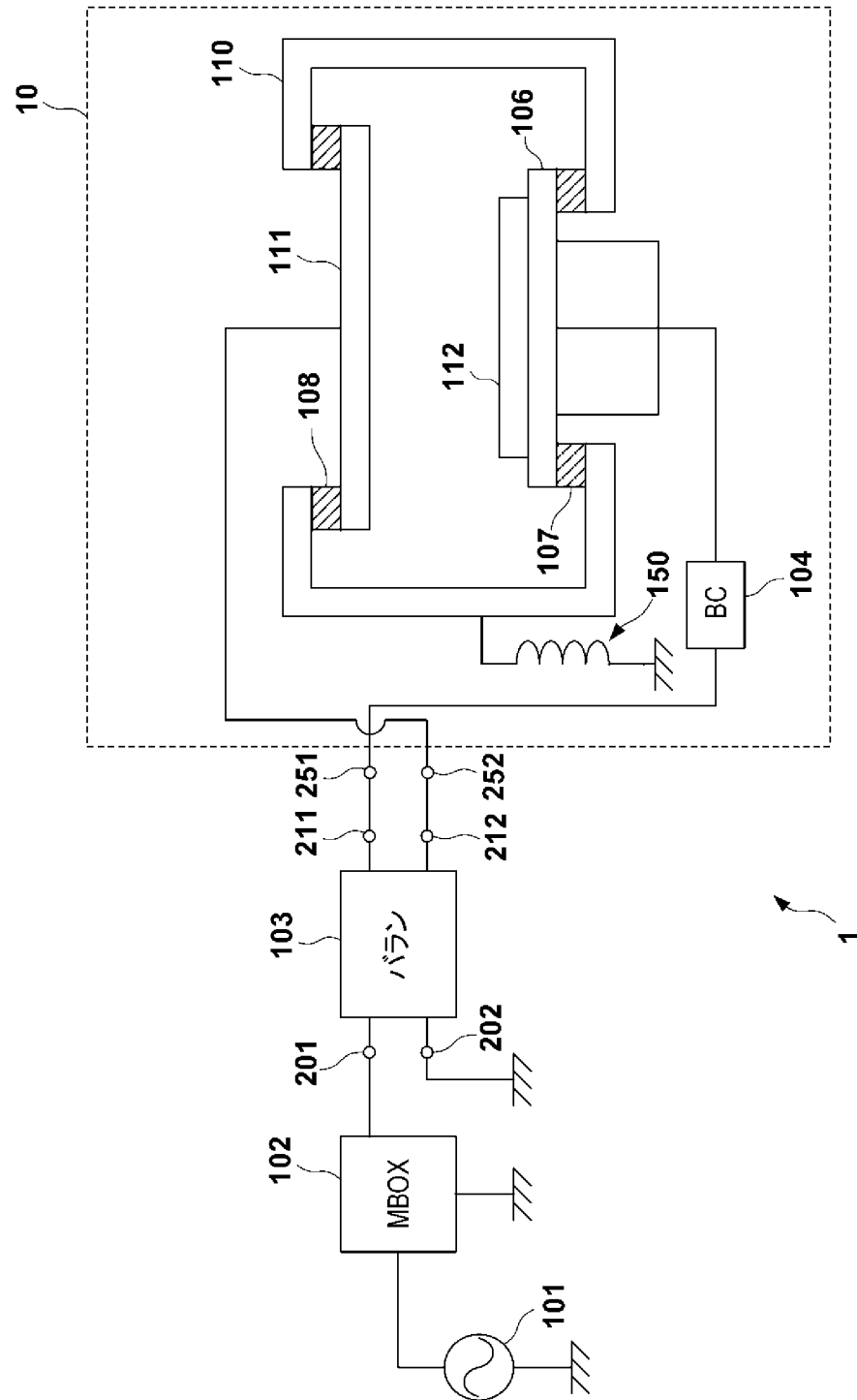
The diagram illustrates a power supply system for a power semiconductor device. A transformer 155 is connected to a power source 190. The primary winding 156' of the transformer is connected to the power source 190. The secondary winding 156 is connected to a switch 157. The switch 157 is controlled by a control signal 153. The output of the switch 157 is connected to a power semiconductor device 160. The power semiconductor device 160 is represented by a block with the label $R_p - jX_p$. The input current to the power semiconductor device is I_2 , and the output current is I_3 . The power semiconductor device 160 is connected to a ground symbol. A dashed box 103 encloses the power semiconductor device 160 and the switch 157. The transformer 155 is also enclosed in a dashed box. The power source 190 is connected to the primary winding 156' of the transformer 155. The control signal 153 is connected to the switch 157. The output current I_3 is shown flowing from the power semiconductor device 160 to the ground symbol.

[illegible]

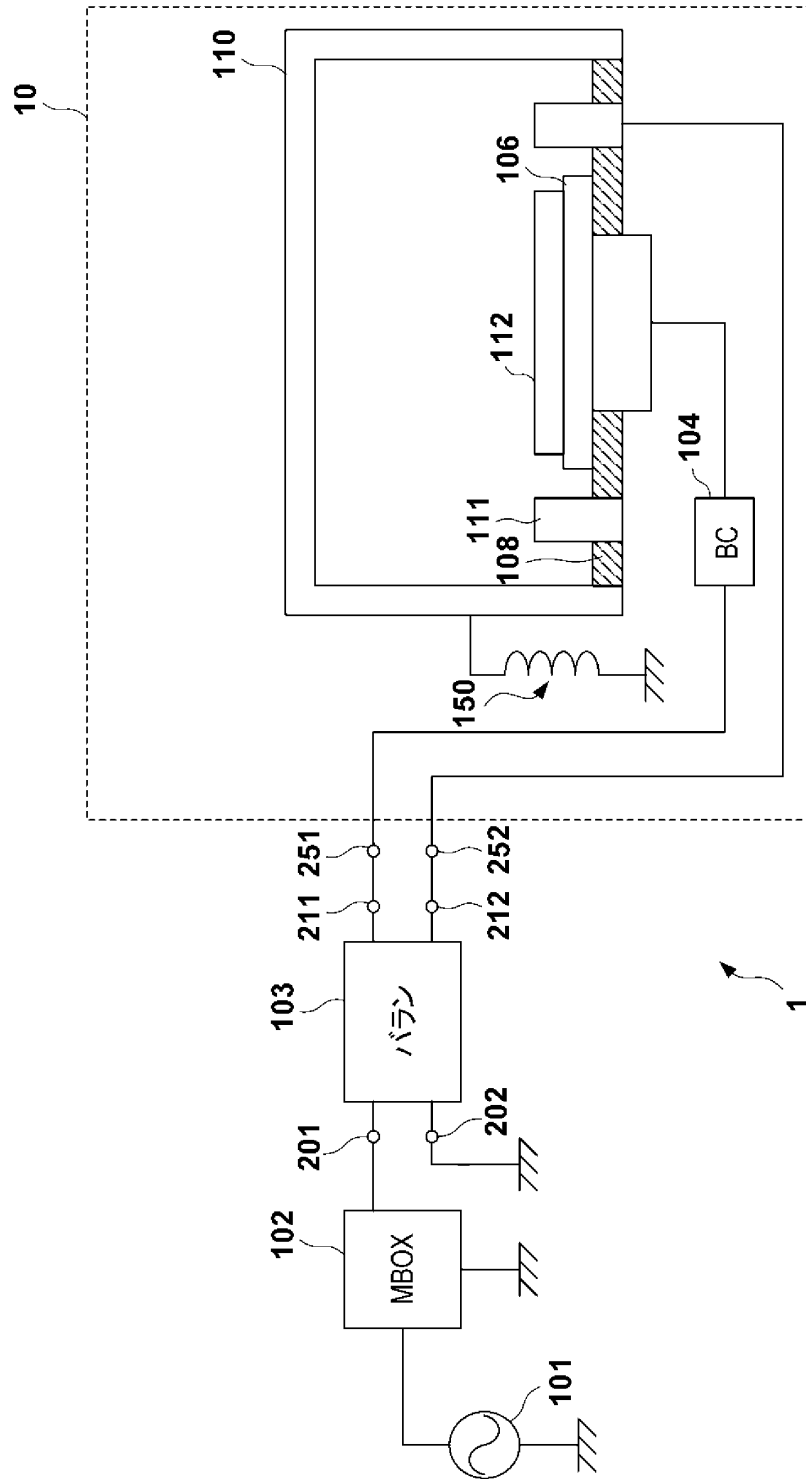
[図7]

	インダクタあり	インダクタなし
I1	9.2	8.3
I2'	8.6	5.8
I3	0.06	2.1
ISO	-43.1	-8.8

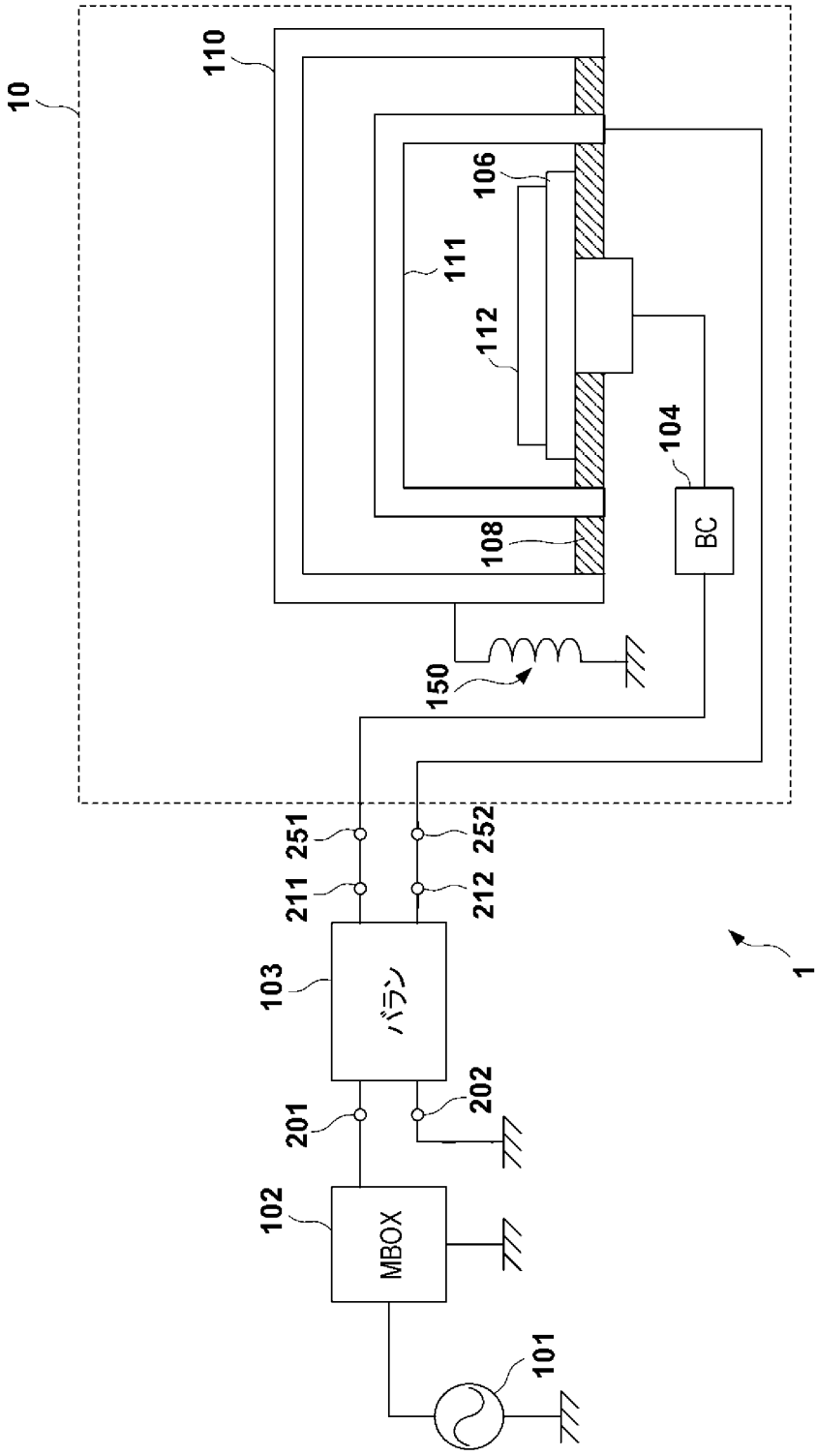
[図8]



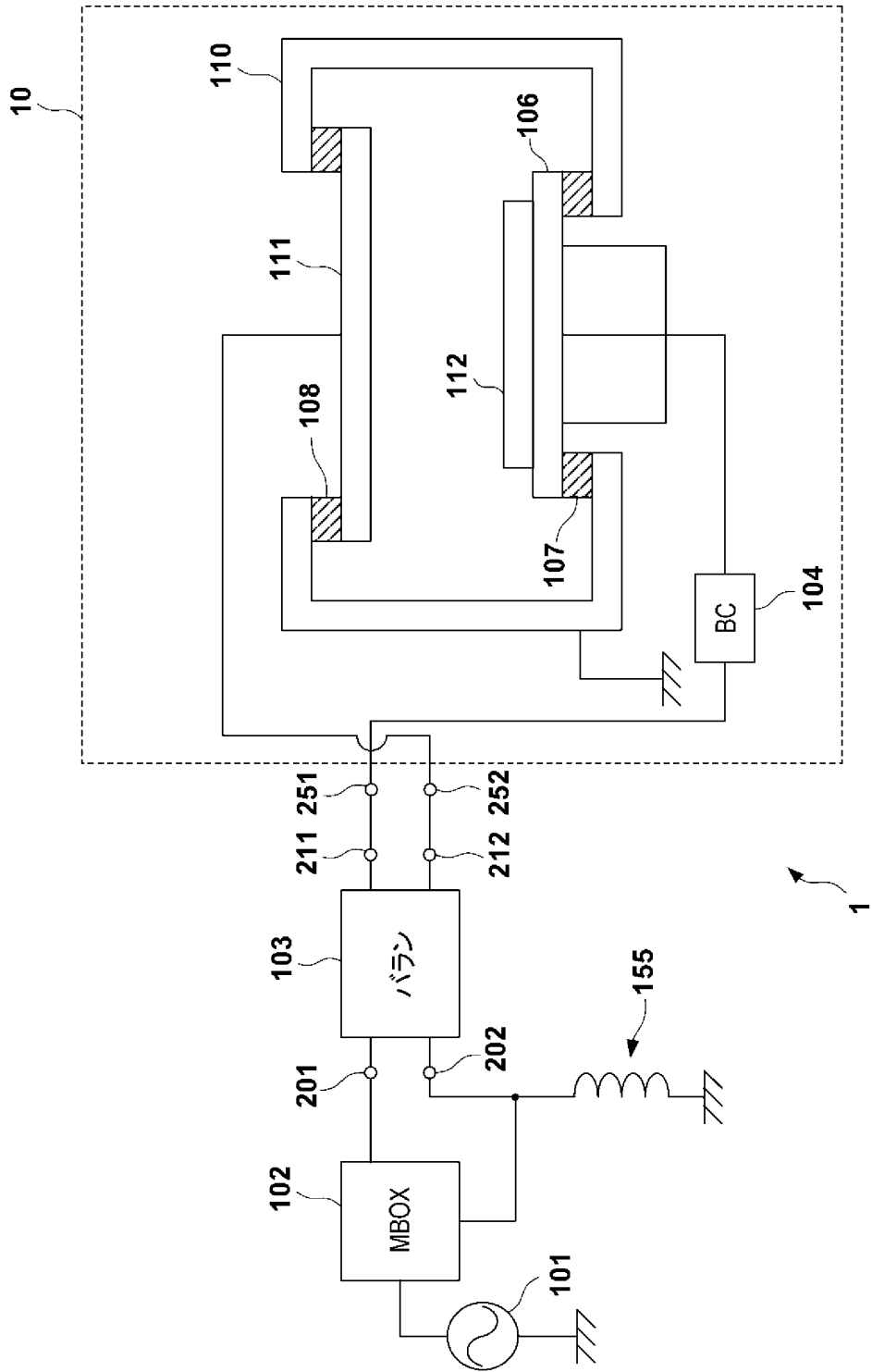
[図9]



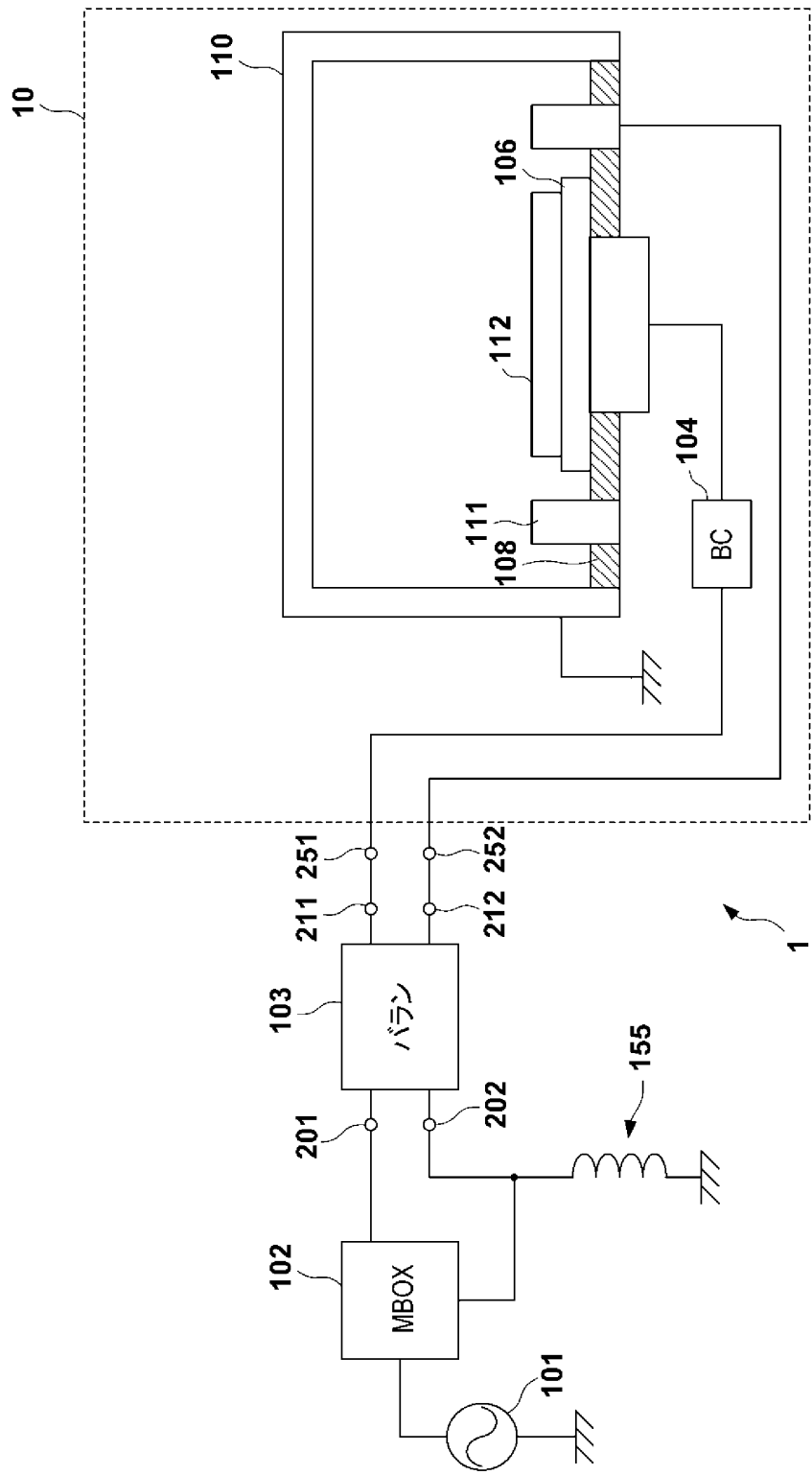
[図10]



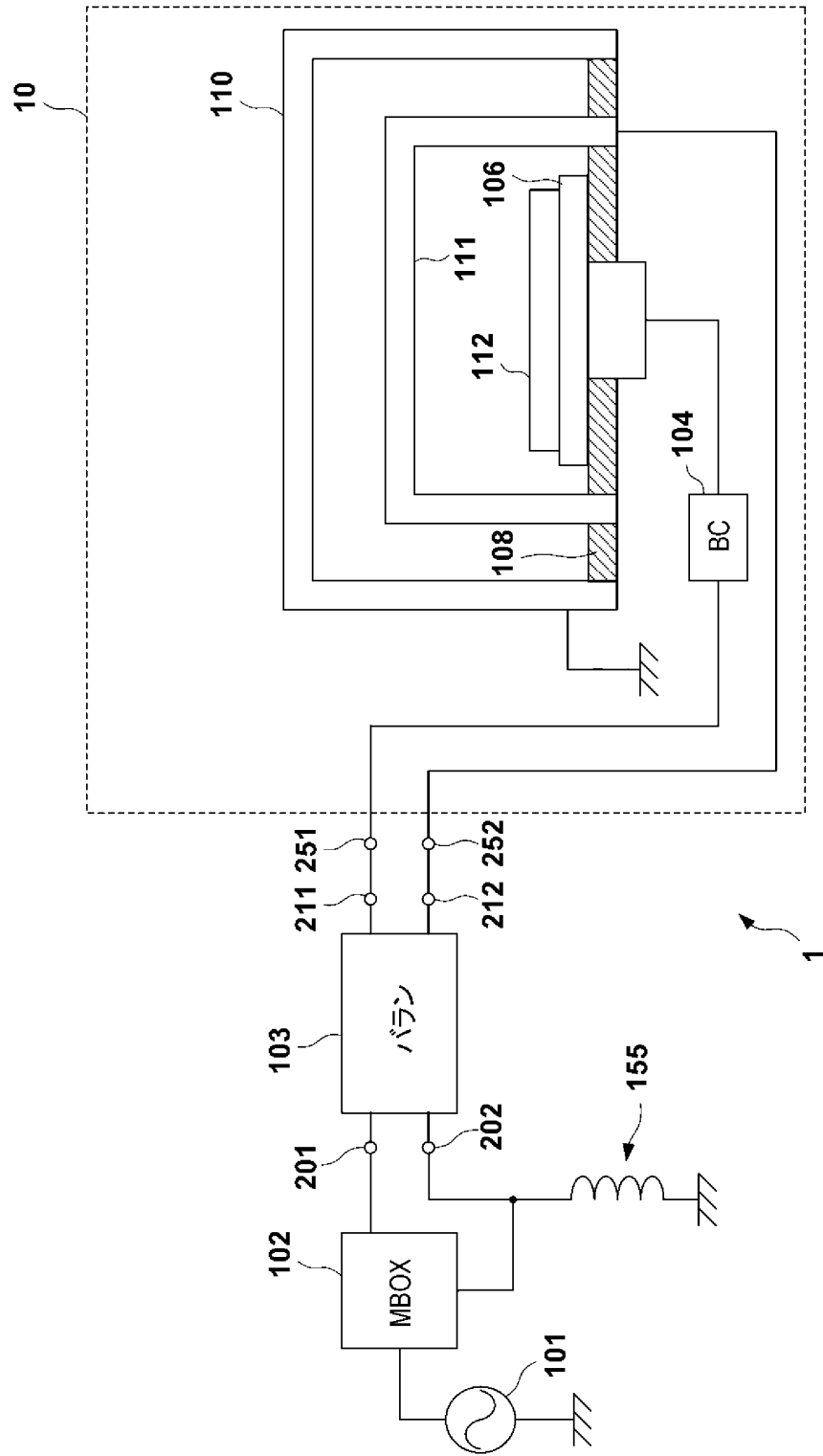
[図12]



[図13]



[図14]



[図15]

