

Fig. 1

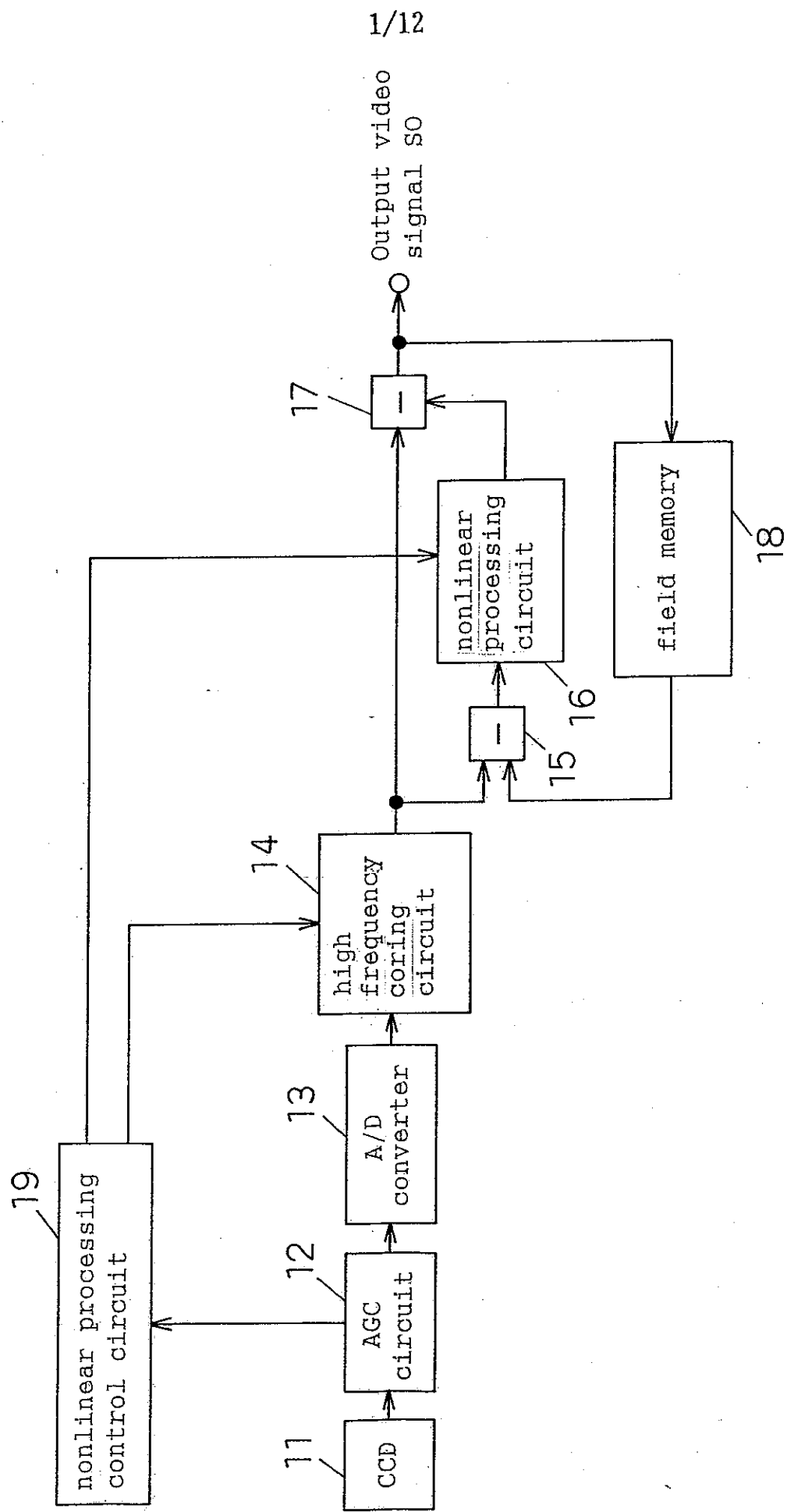


Fig. 2

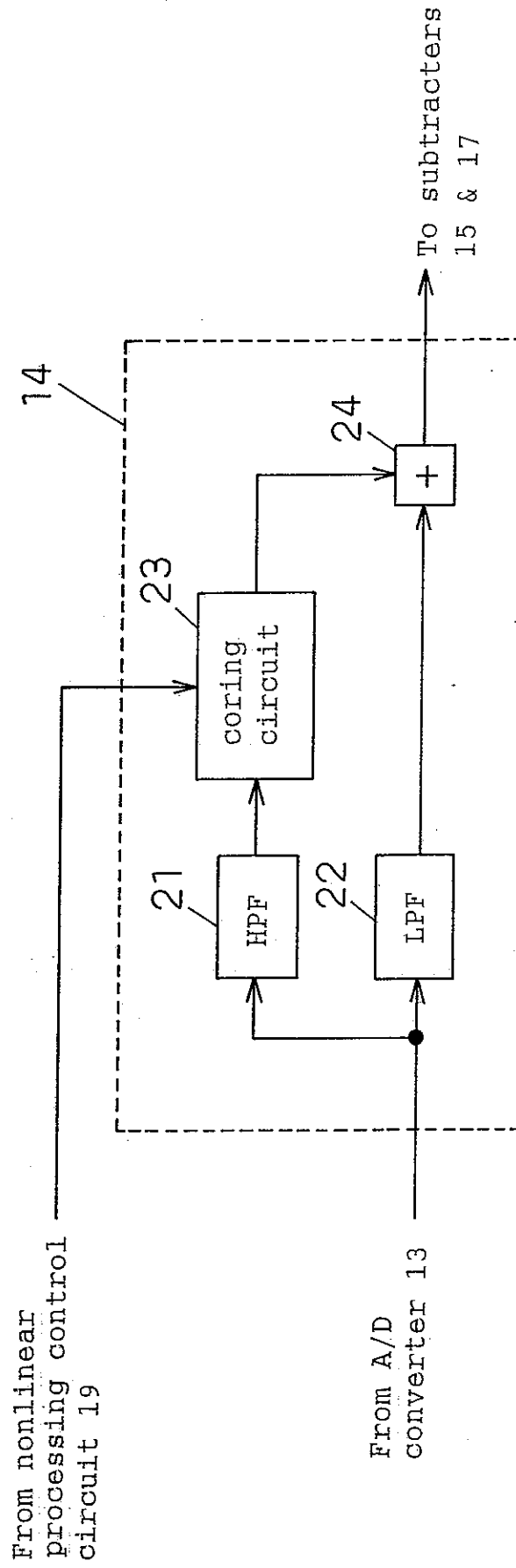


Fig. 3 (a)

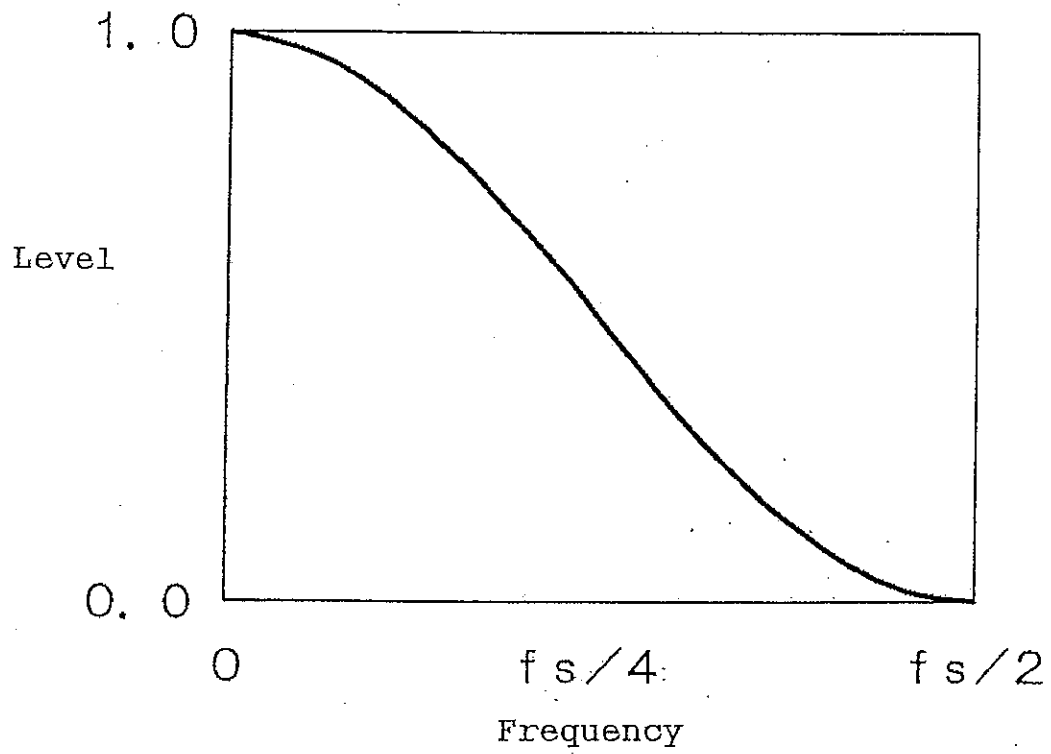
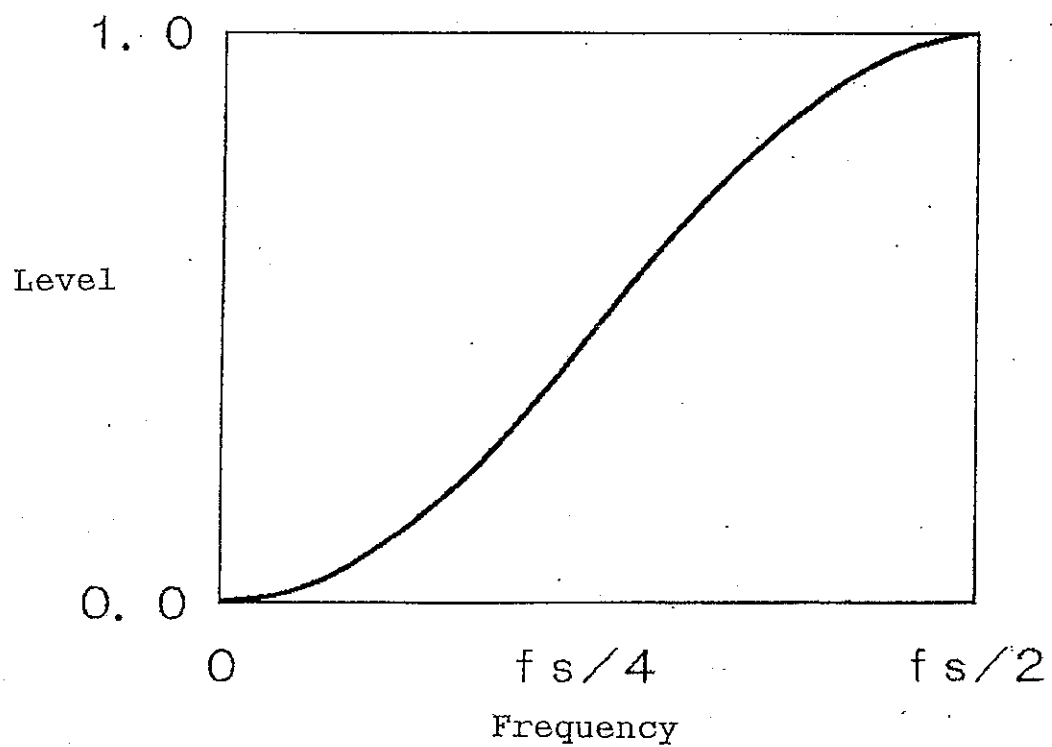


Fig. 3 (b)



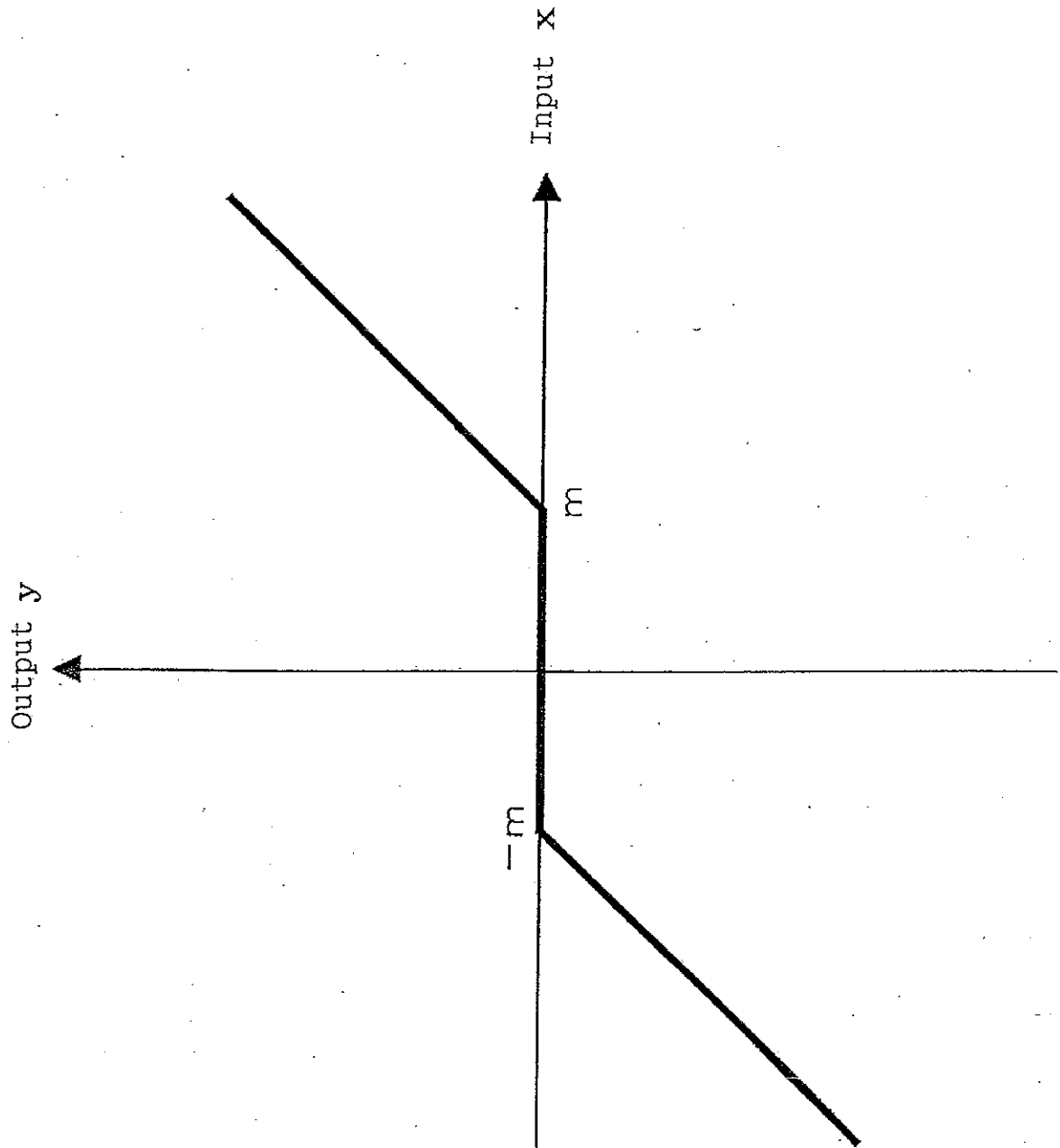


Fig. 4

Fig. 5 (a)

CCD output
Signal level

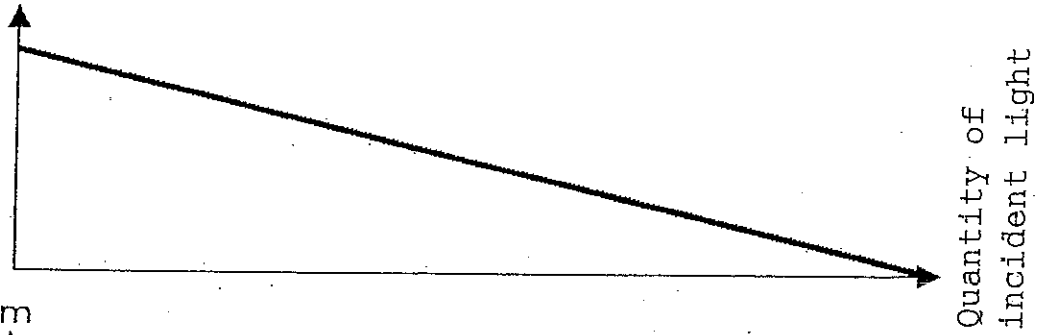


Fig. 5 (b)

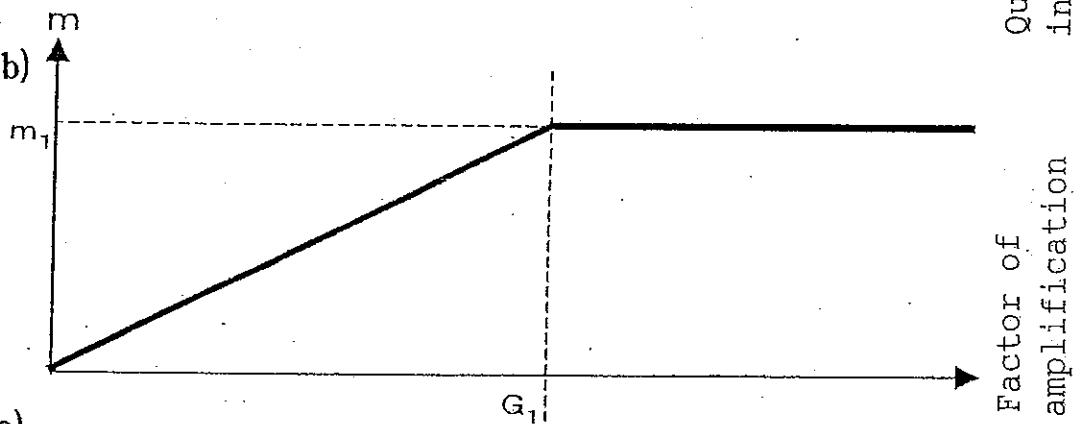


Fig. 5 (c)

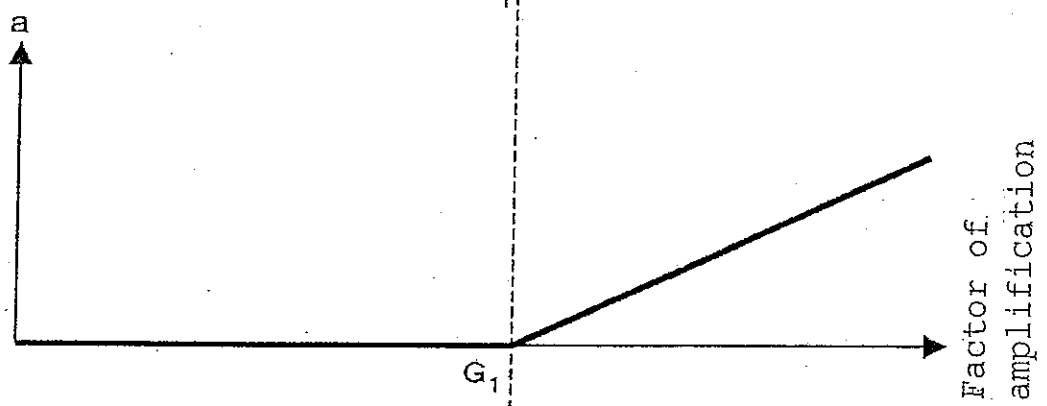


Fig. 5 (d)

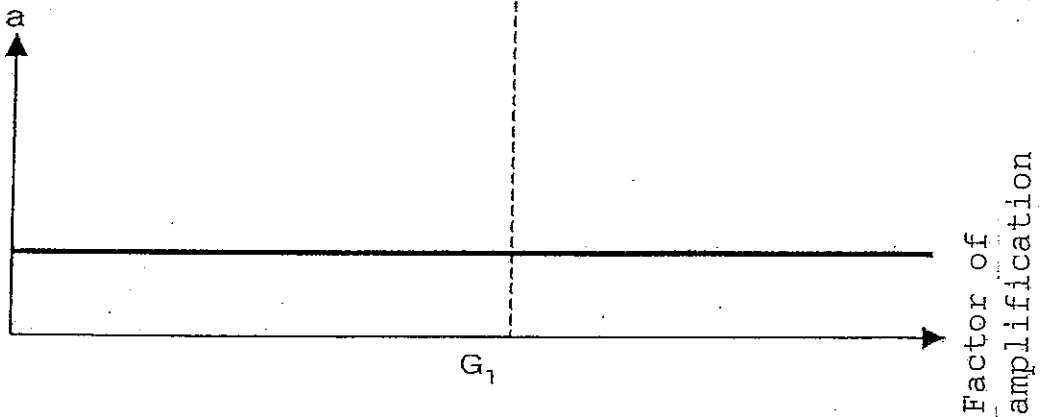


Fig. 6

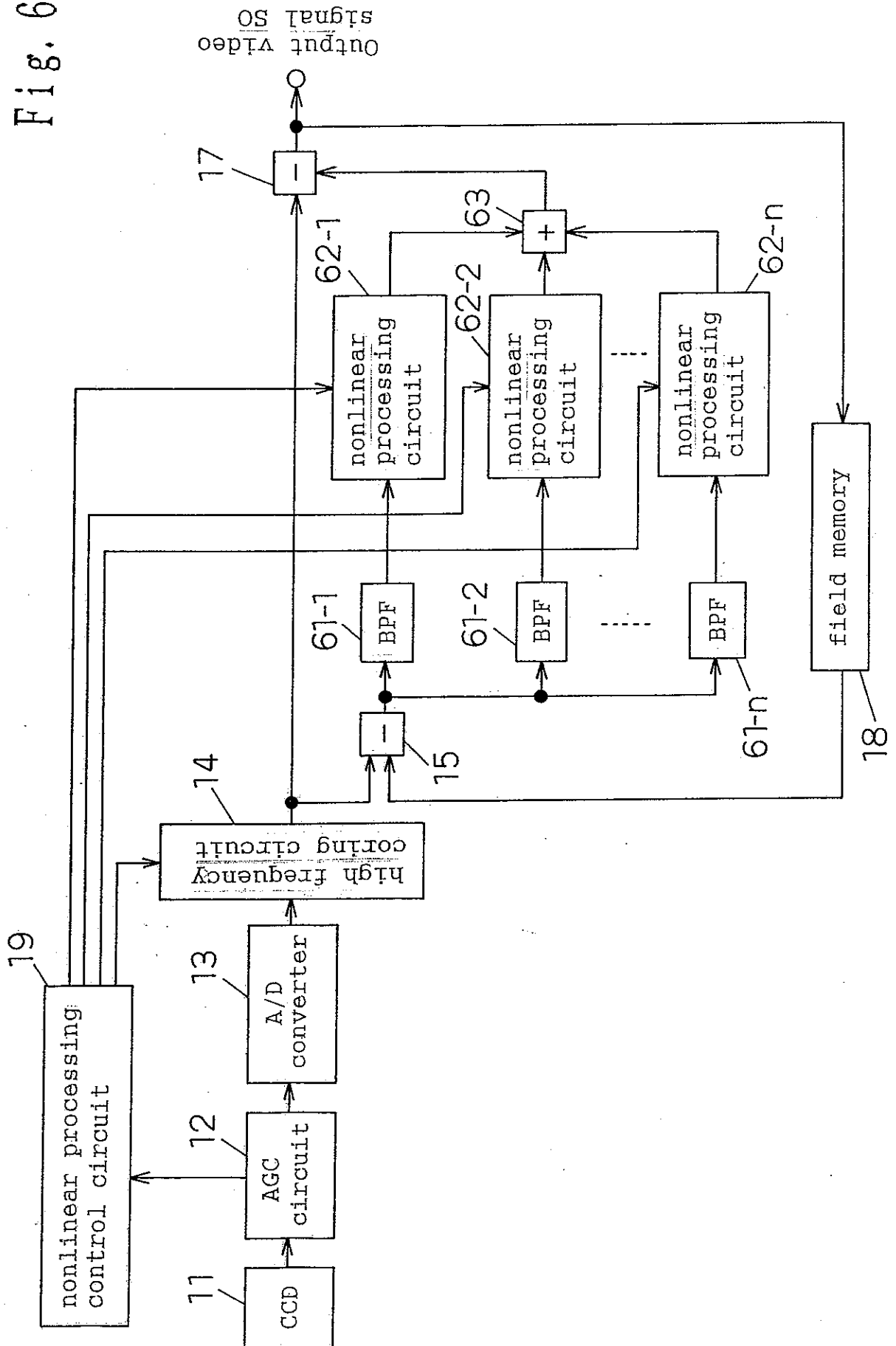


Fig. 7 (a) m

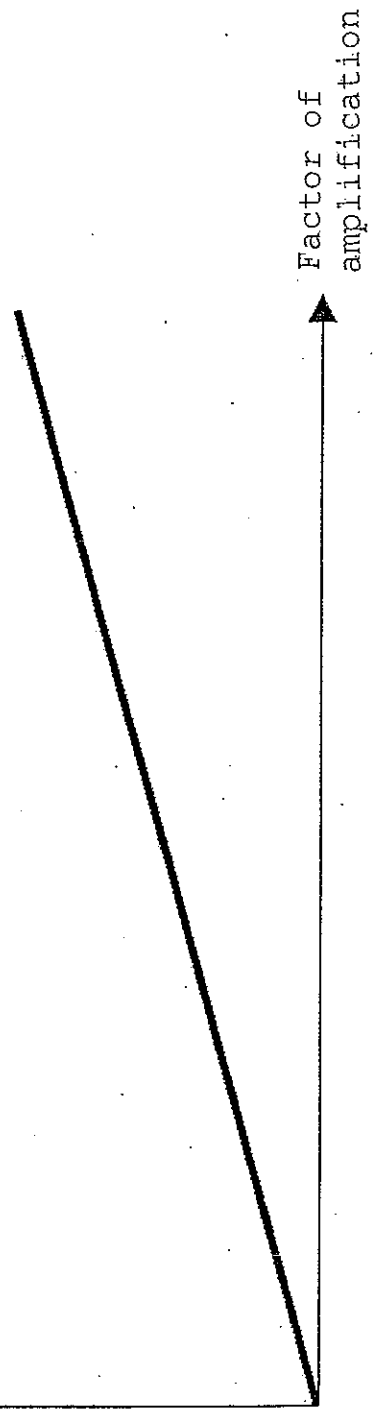
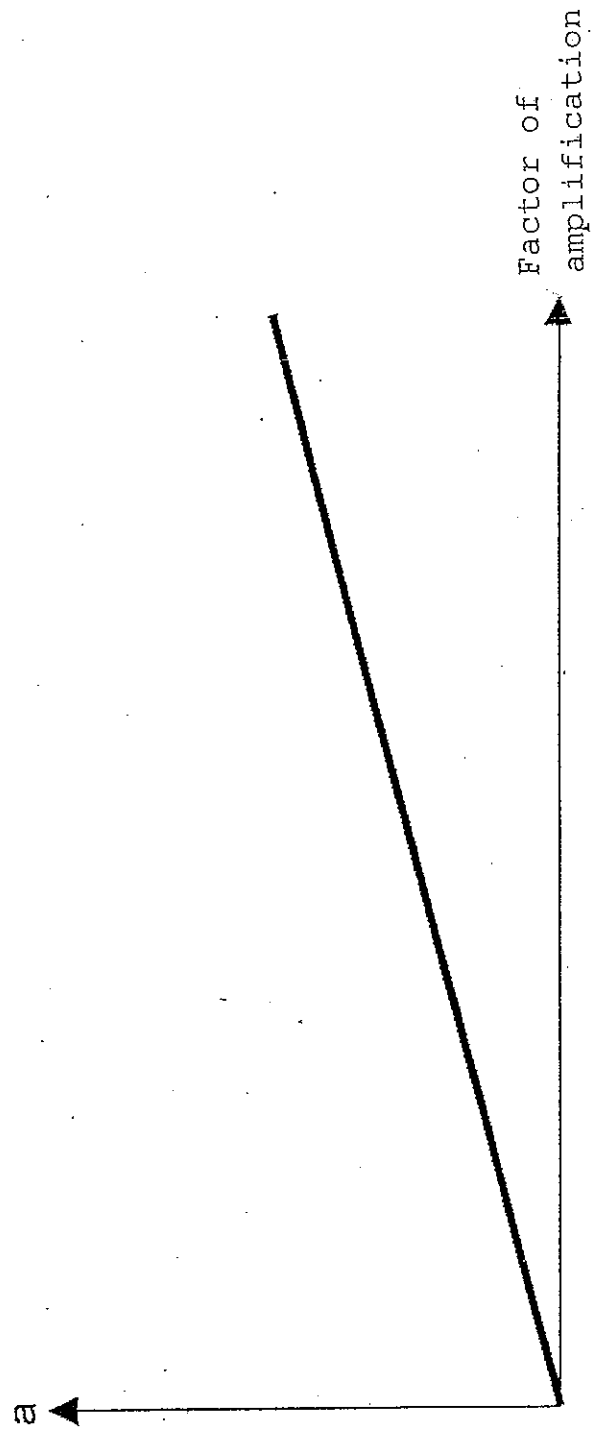


Fig. 7 (b)



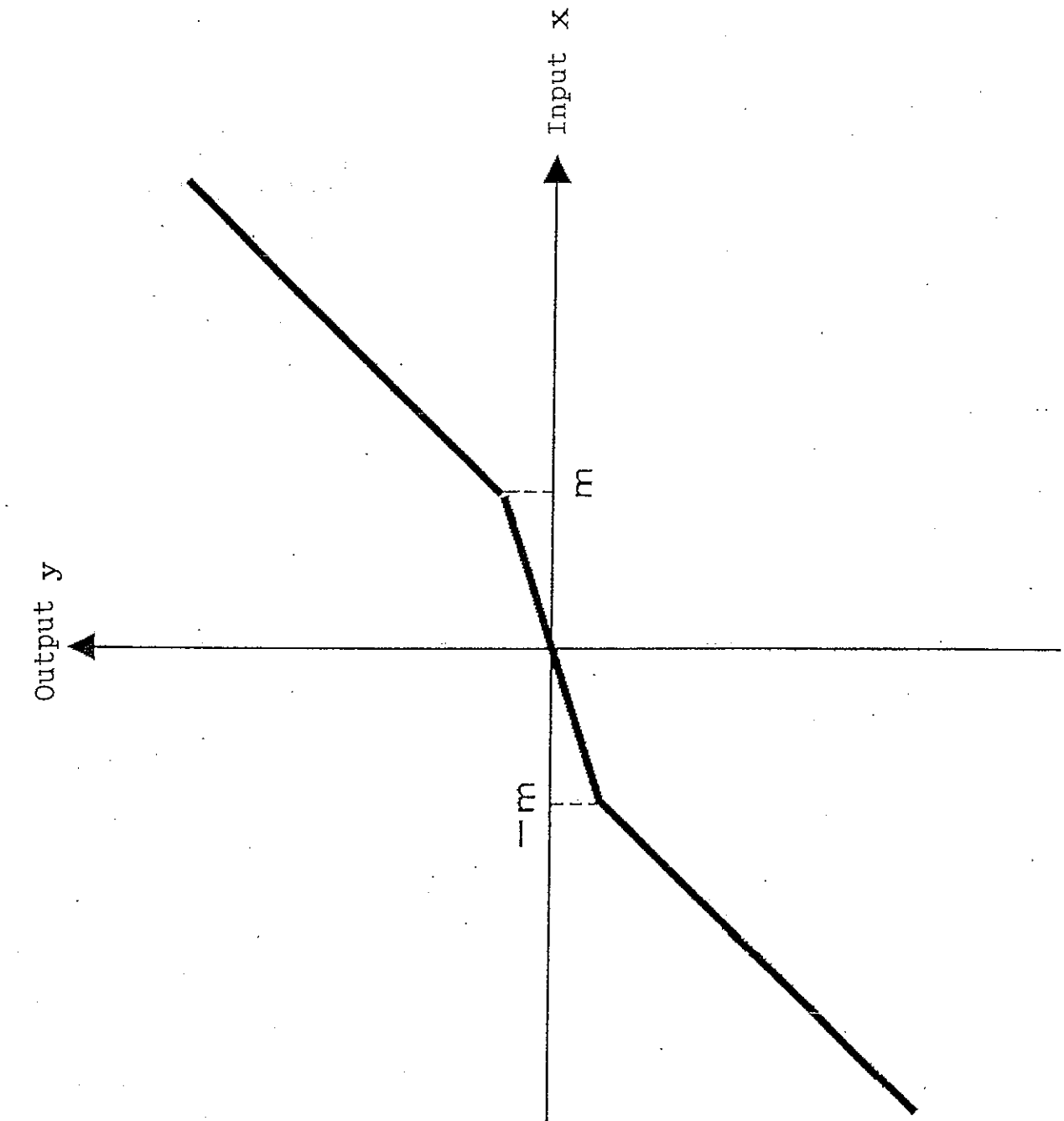


Fig. 9

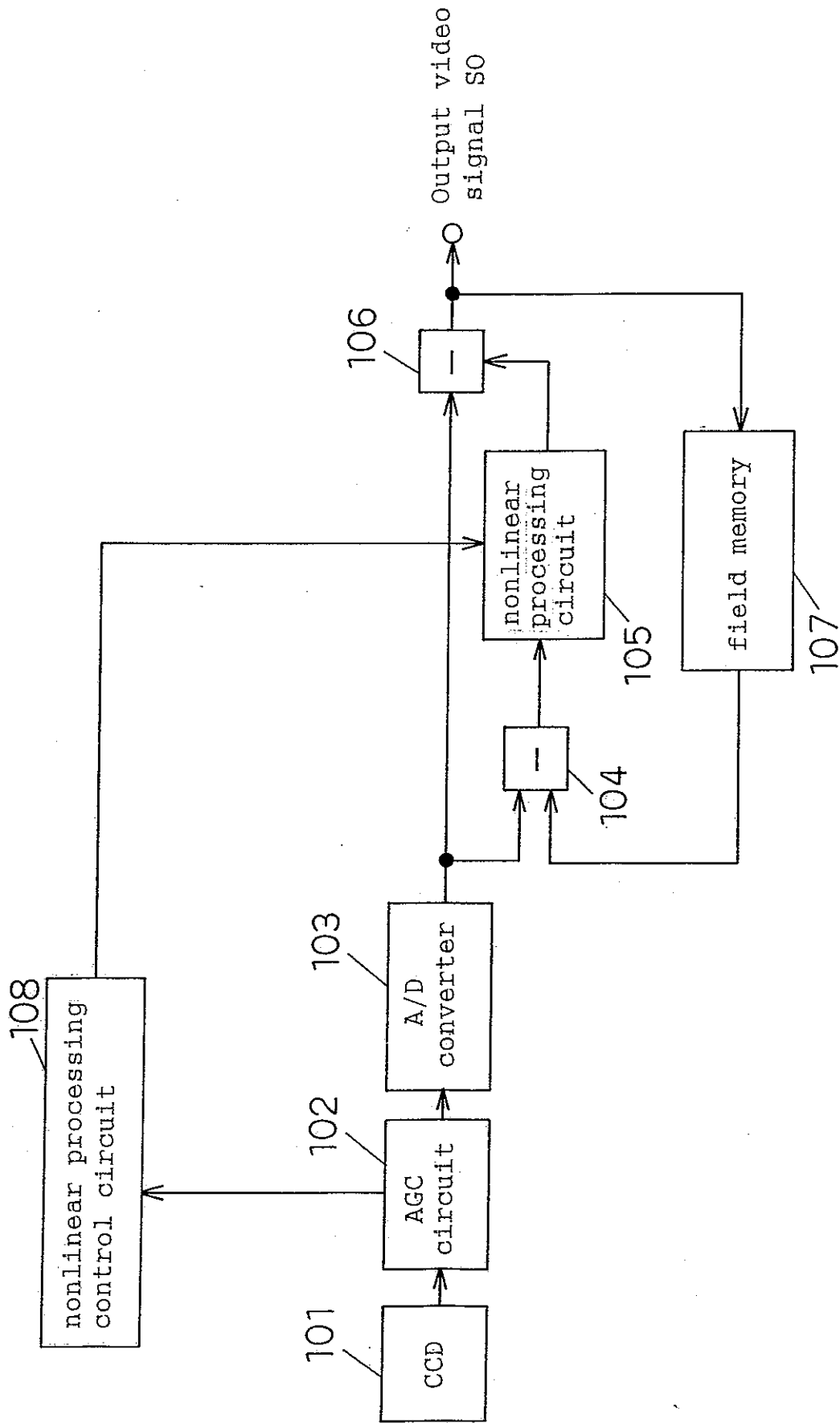


Fig. 10

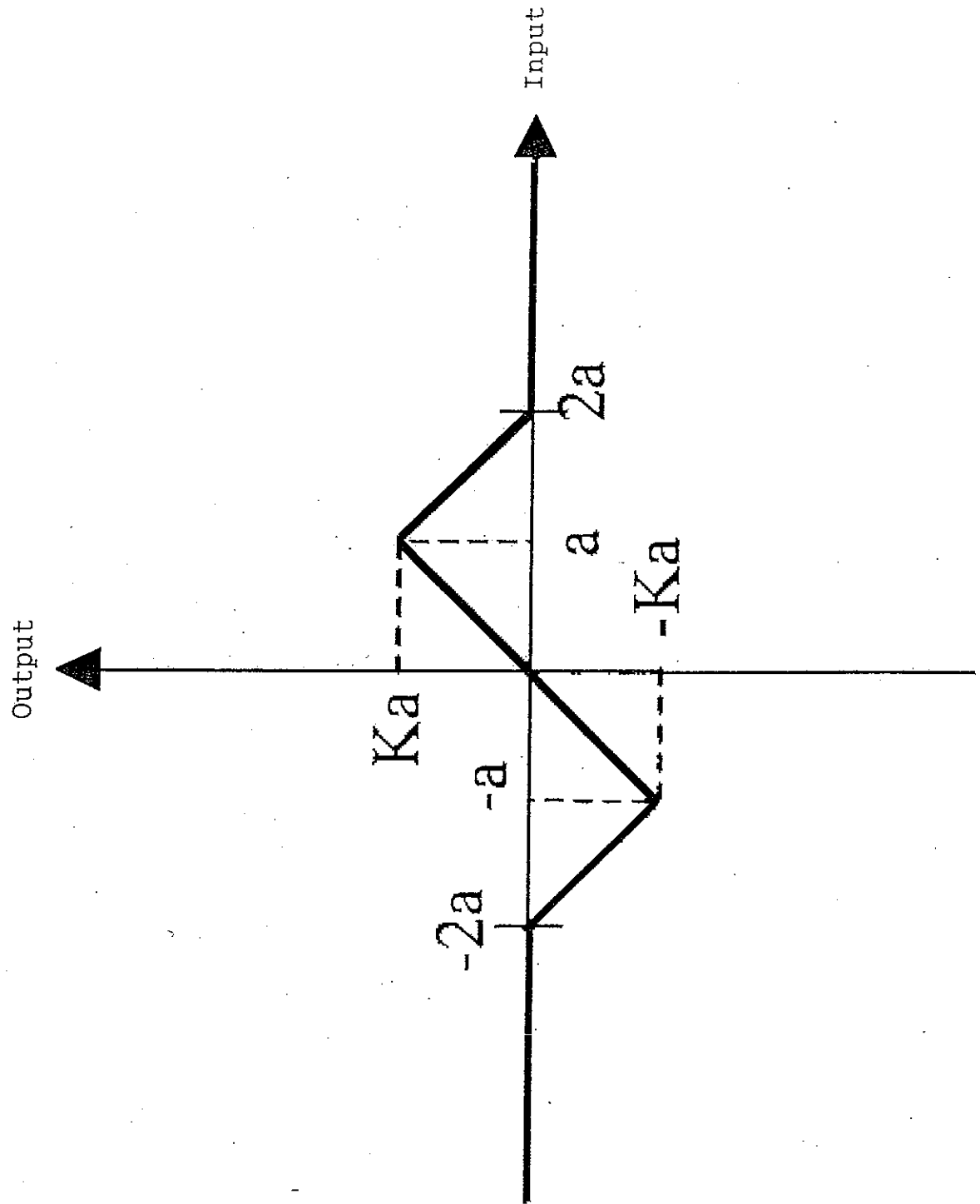


Fig. 11 (a)

CCD output
Signal level

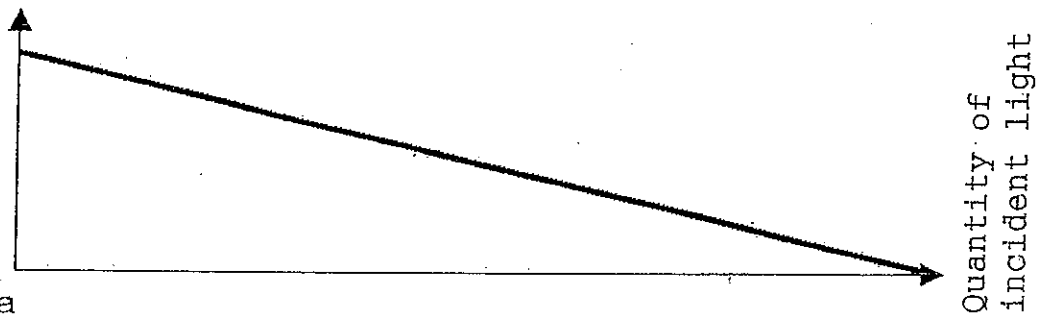


Fig. 11 (b)

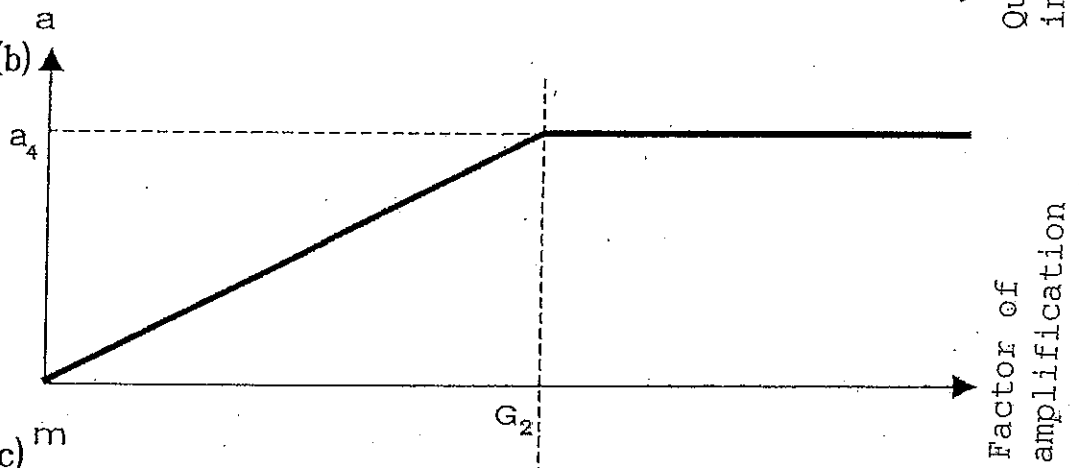


Fig. 11 (c)

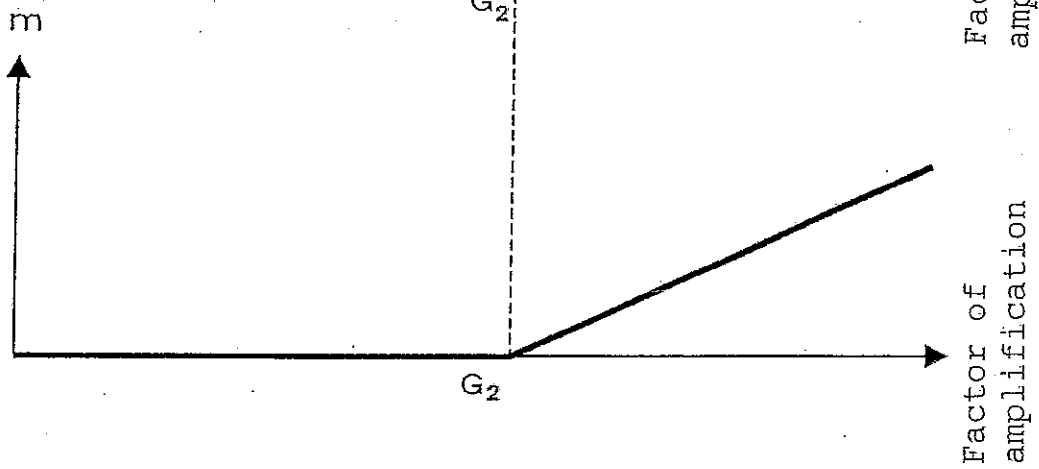


Fig. 12 (a)

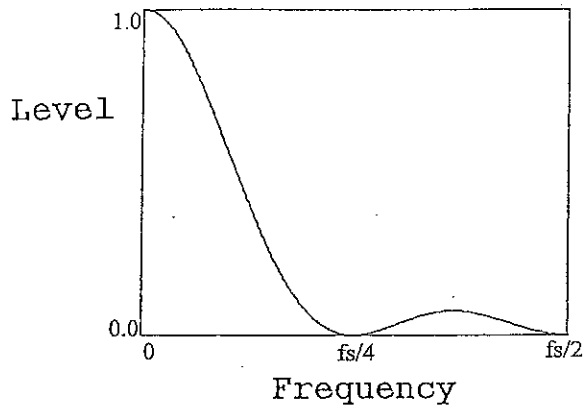


Fig. 12 (b)

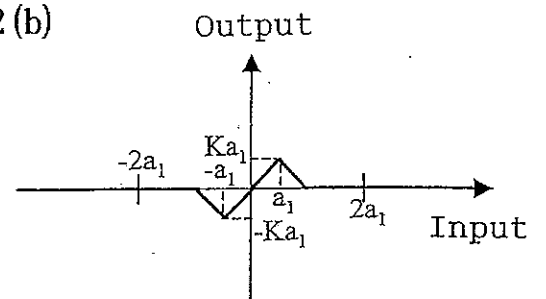


Fig. 12 (c)

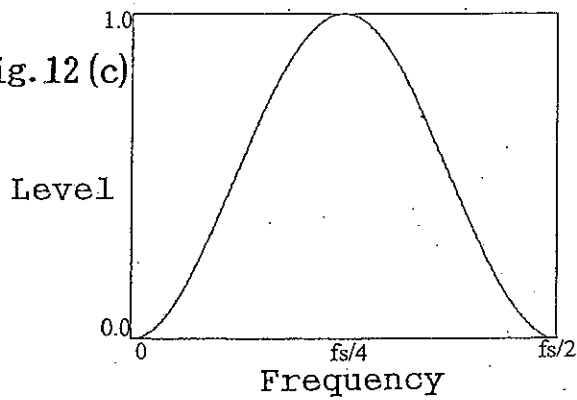


Fig. 12 (d)

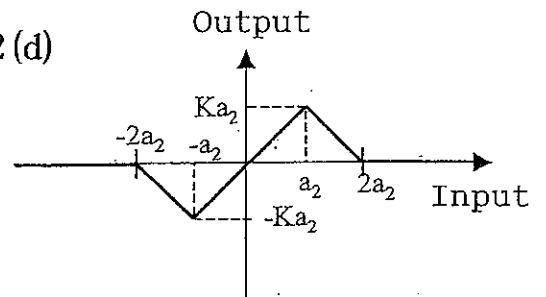


Fig. 12 (e)

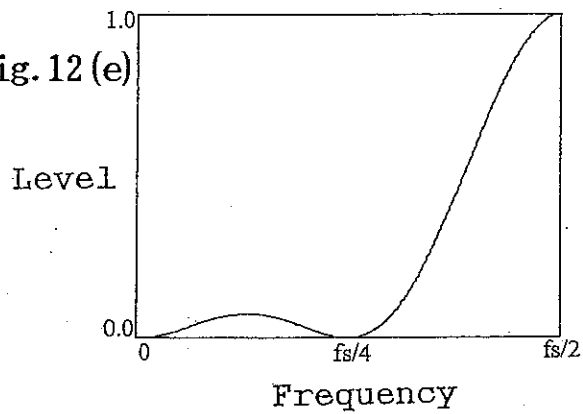


Fig. 12 (f)

