

[ABSTRACT]

The verification-processing device includes an acquisition unit that is configured to acquire a circuit logic model represented by an evaluation expression represented by a logical expression taking as variables, a relay, an element, and a connection line that
5 form a relay logic circuit to be verified, the evaluation expression including at least a logical expression for a case where a failure event occurs in the relay logic circuit; and a determination unit that is configured to determine the logical state of an output of the relay when a failure event occurs in the element or the connection line, based on the circuit logic model.