

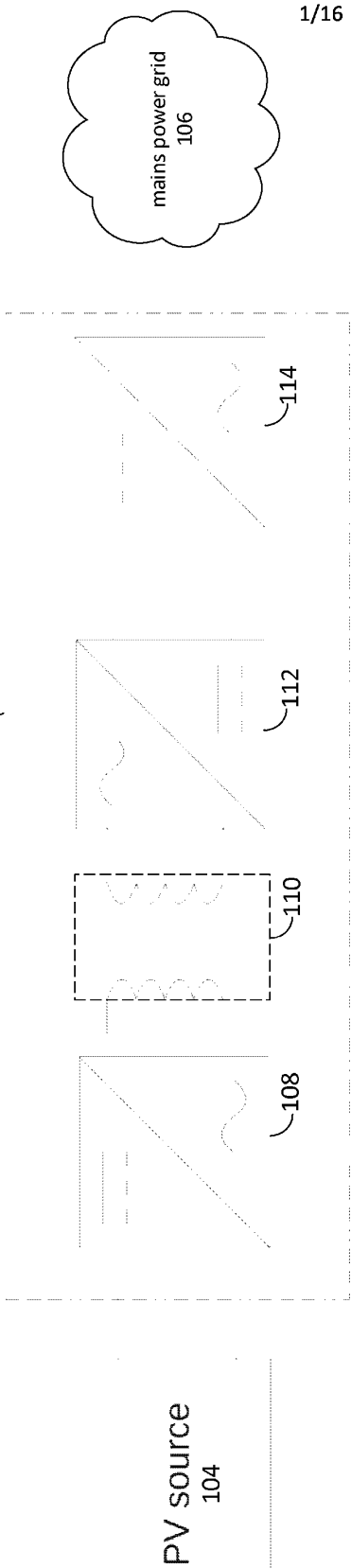


Figure 1

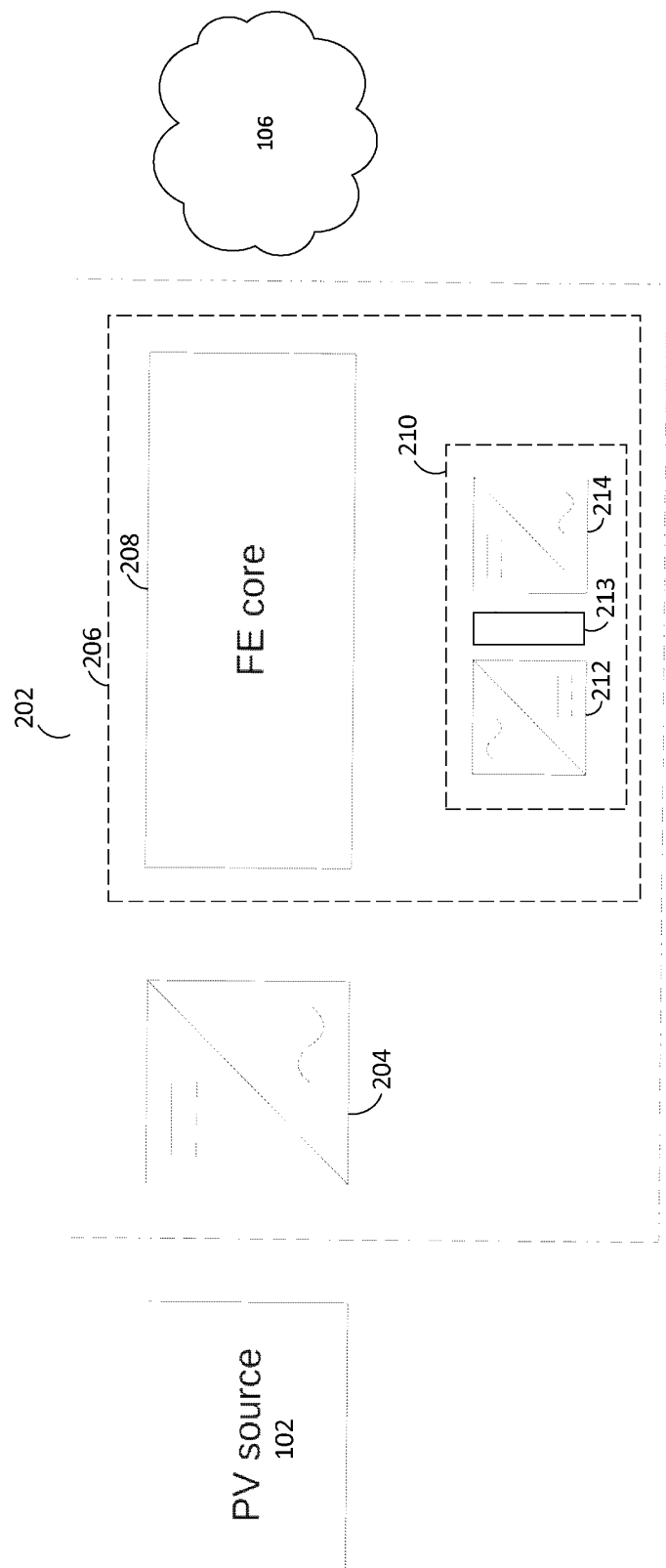


Figure 2

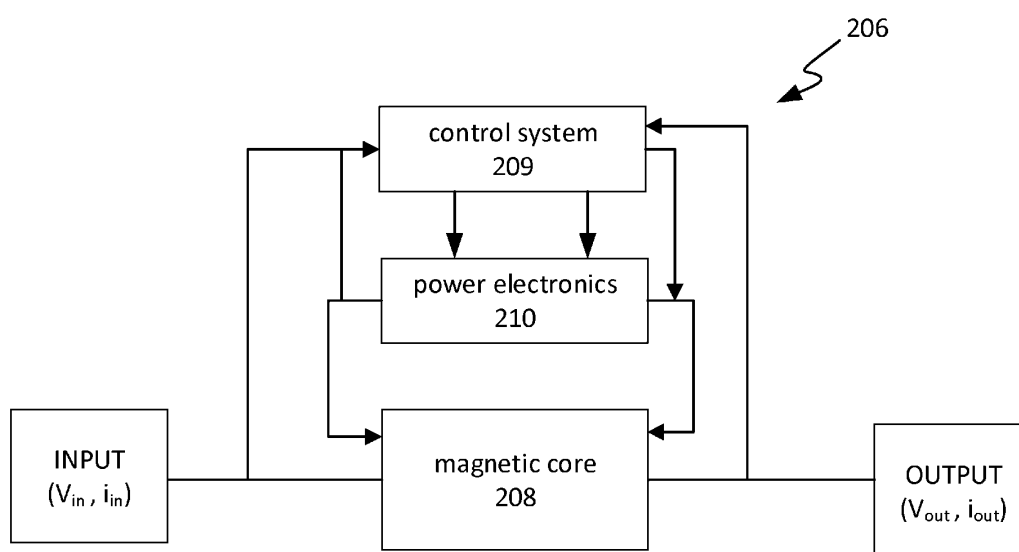


Figure 3

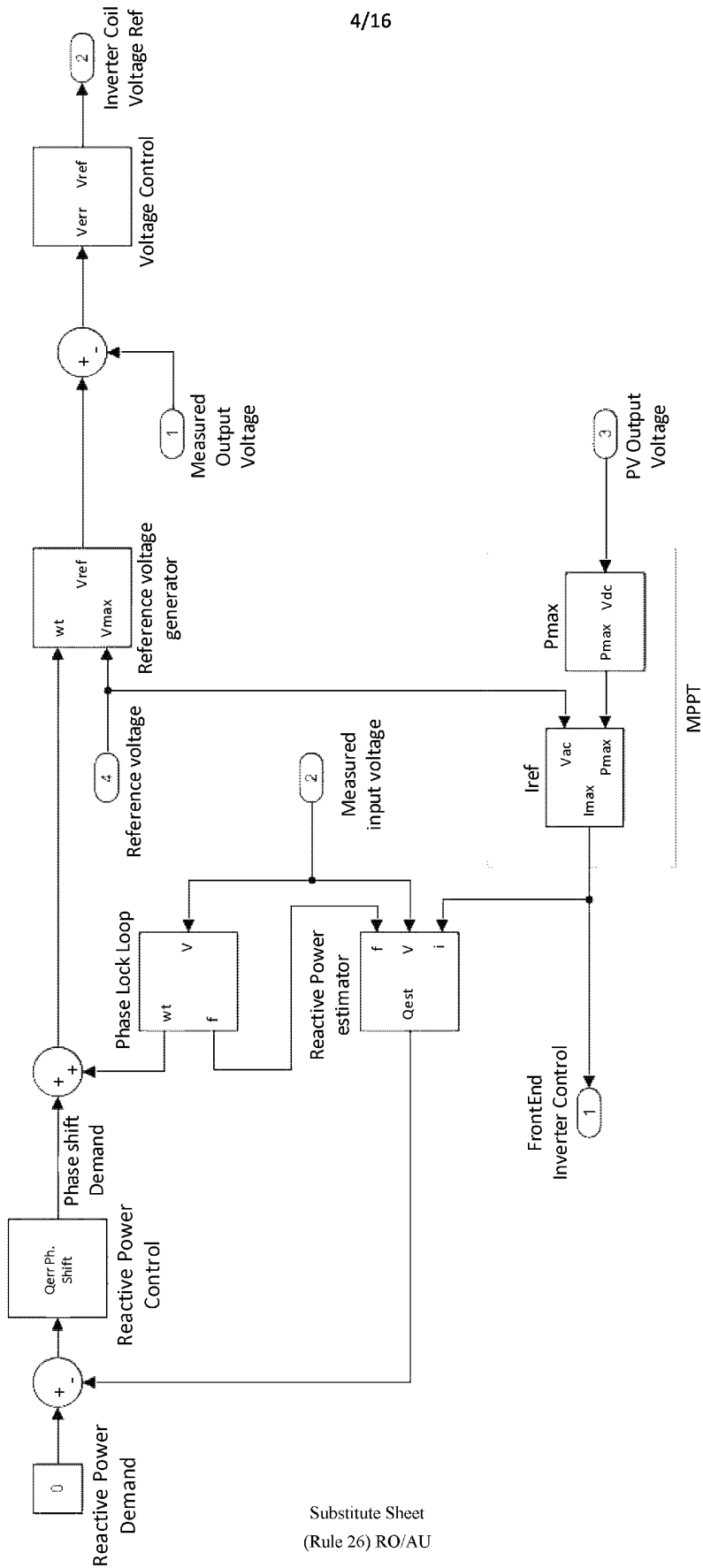


Figure 4

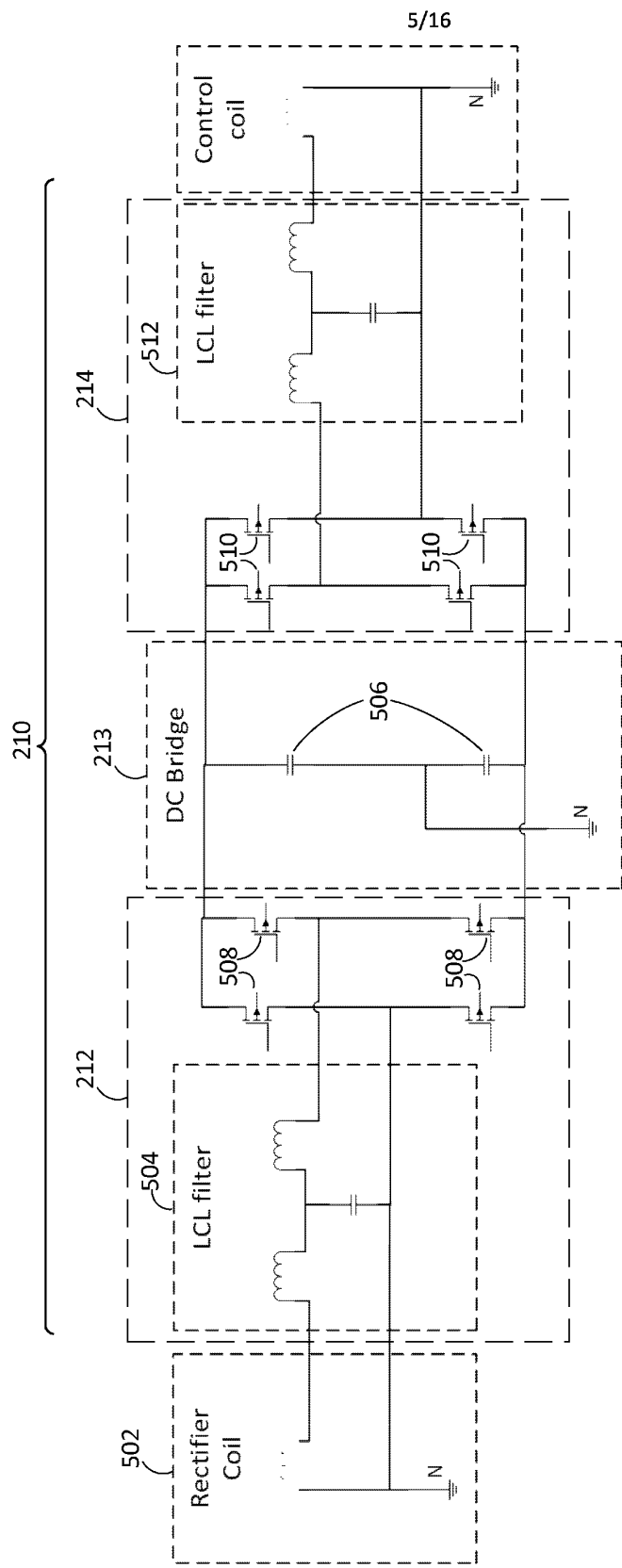


Figure 5

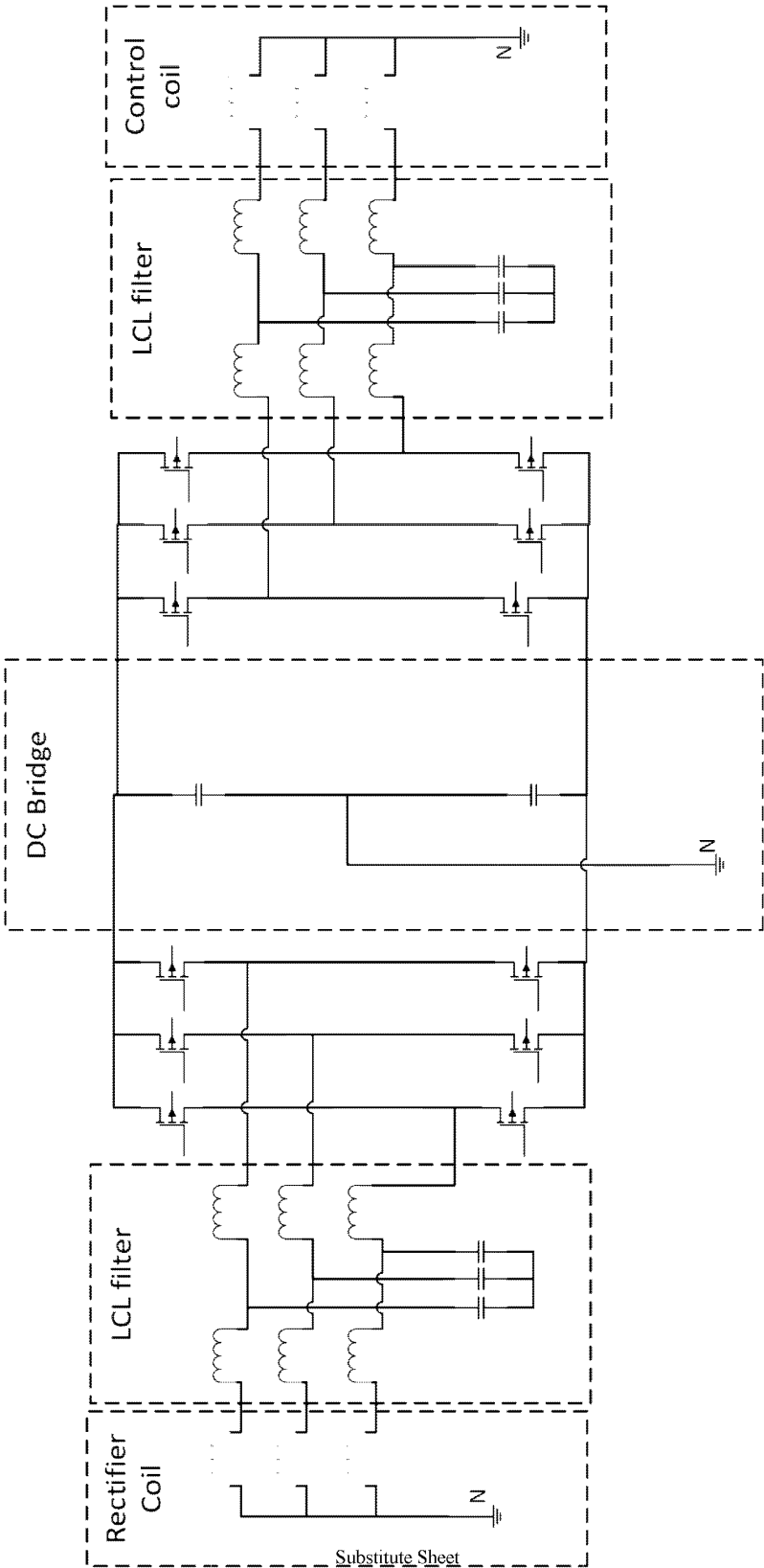


Figure 6

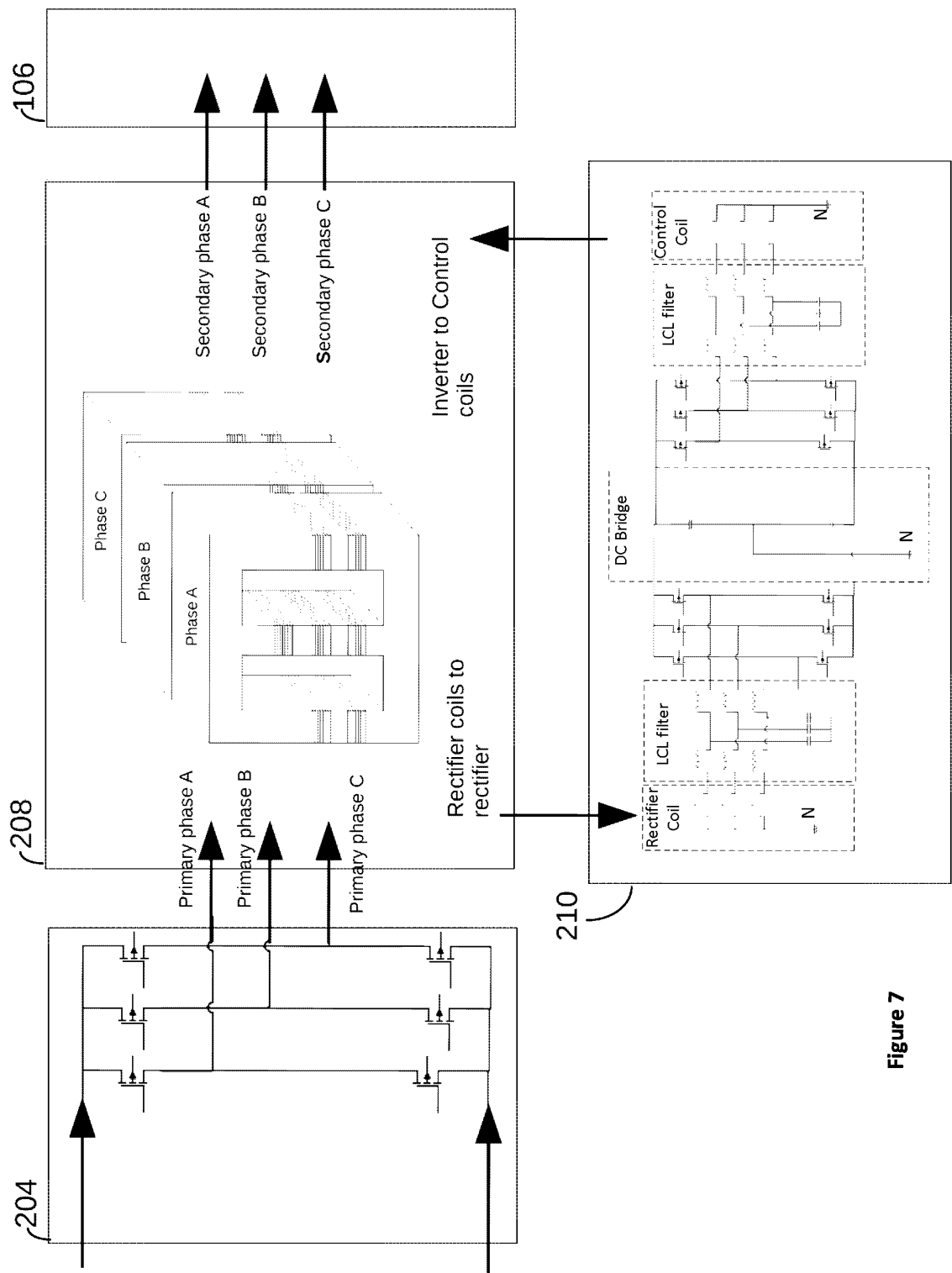


Figure 7

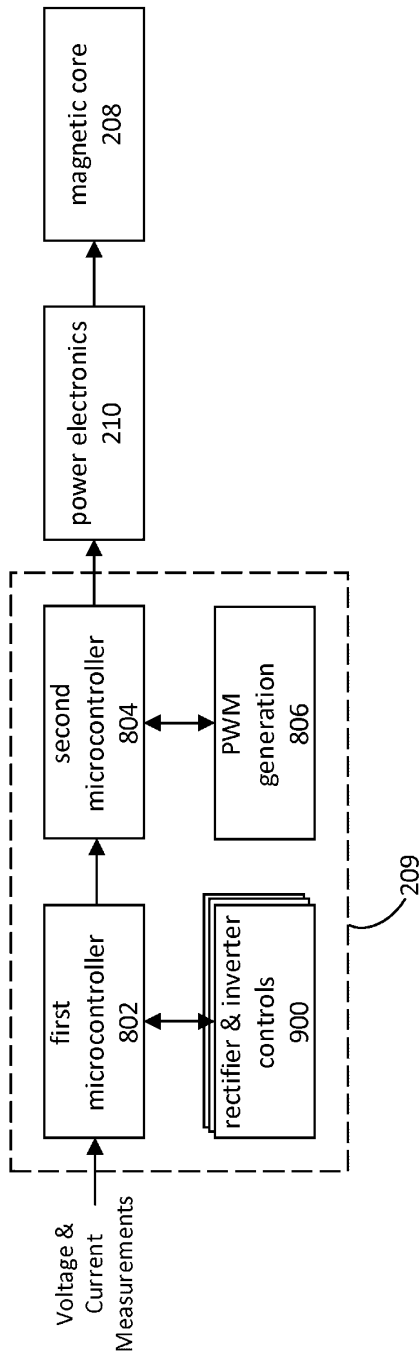


Figure 8

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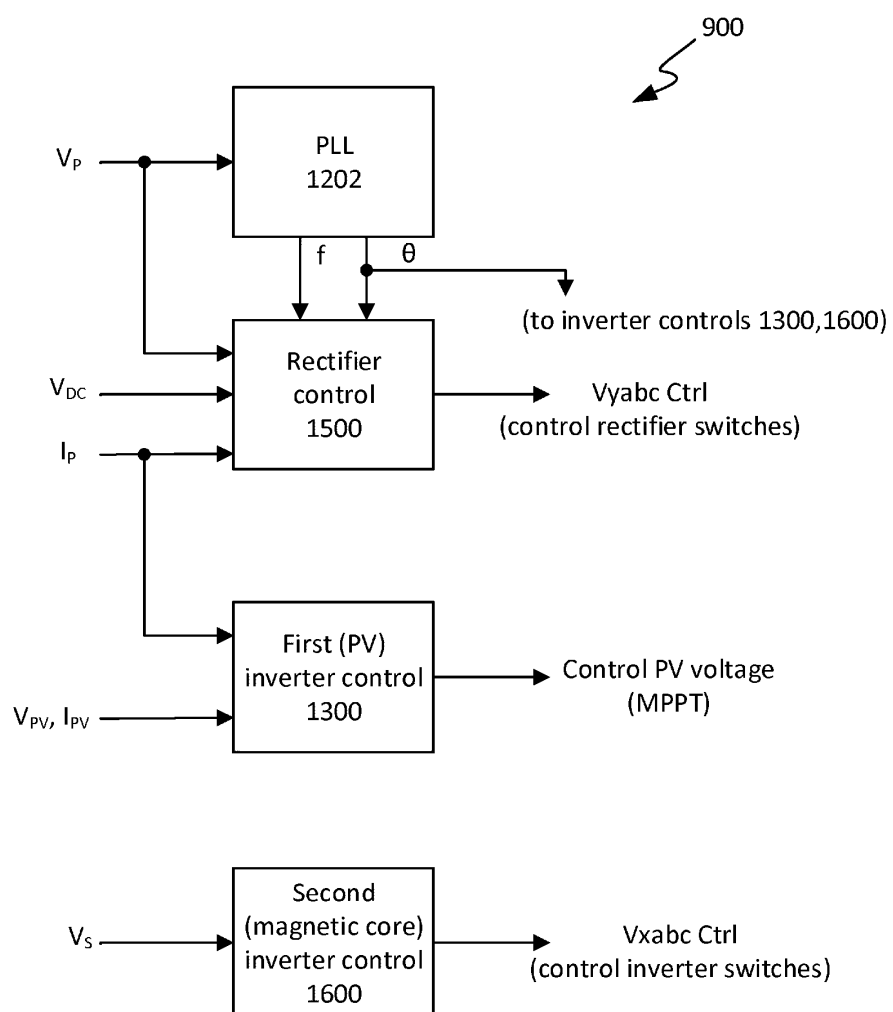


Figure 9

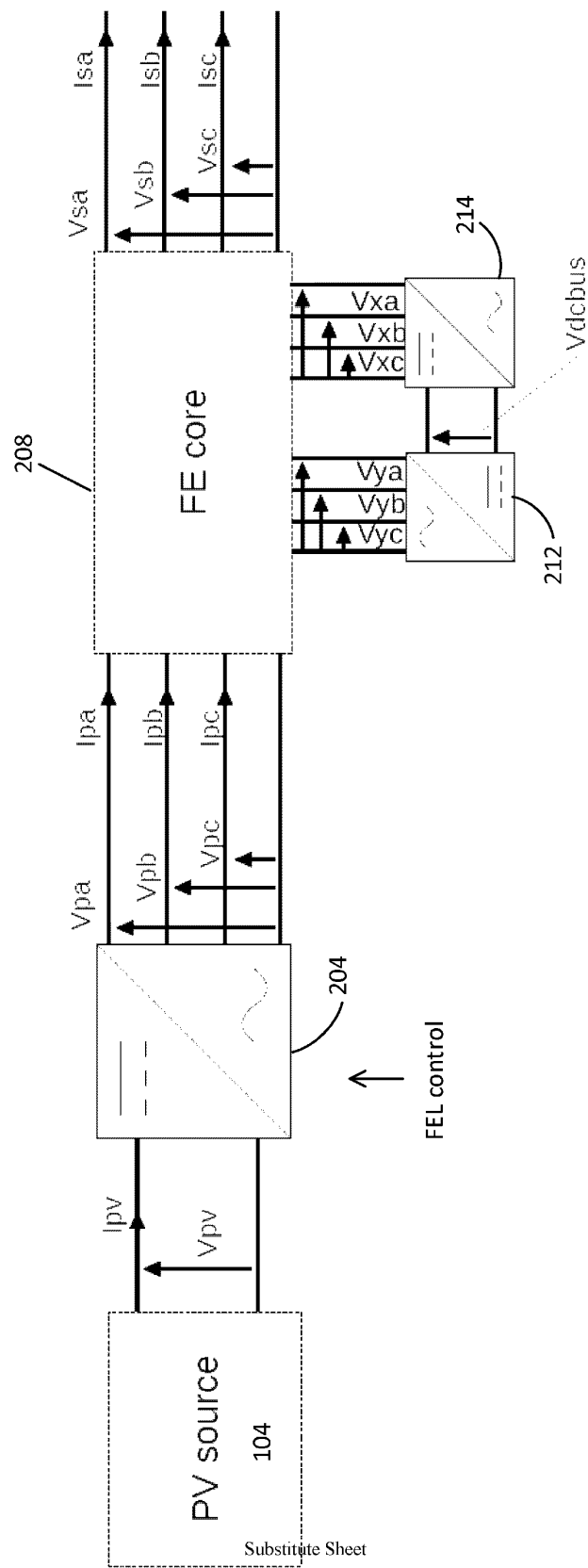


Figure 10

Primary voltage and current measures

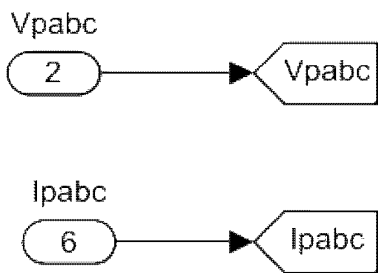
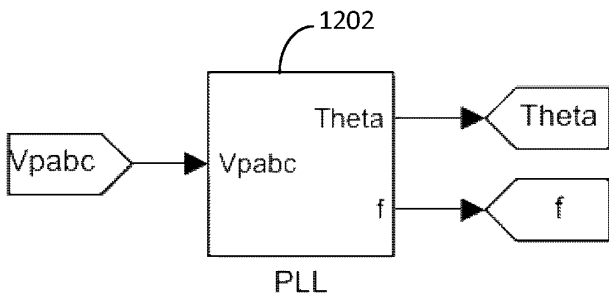


Figure 11



Phase Locked Loop

Figure 12

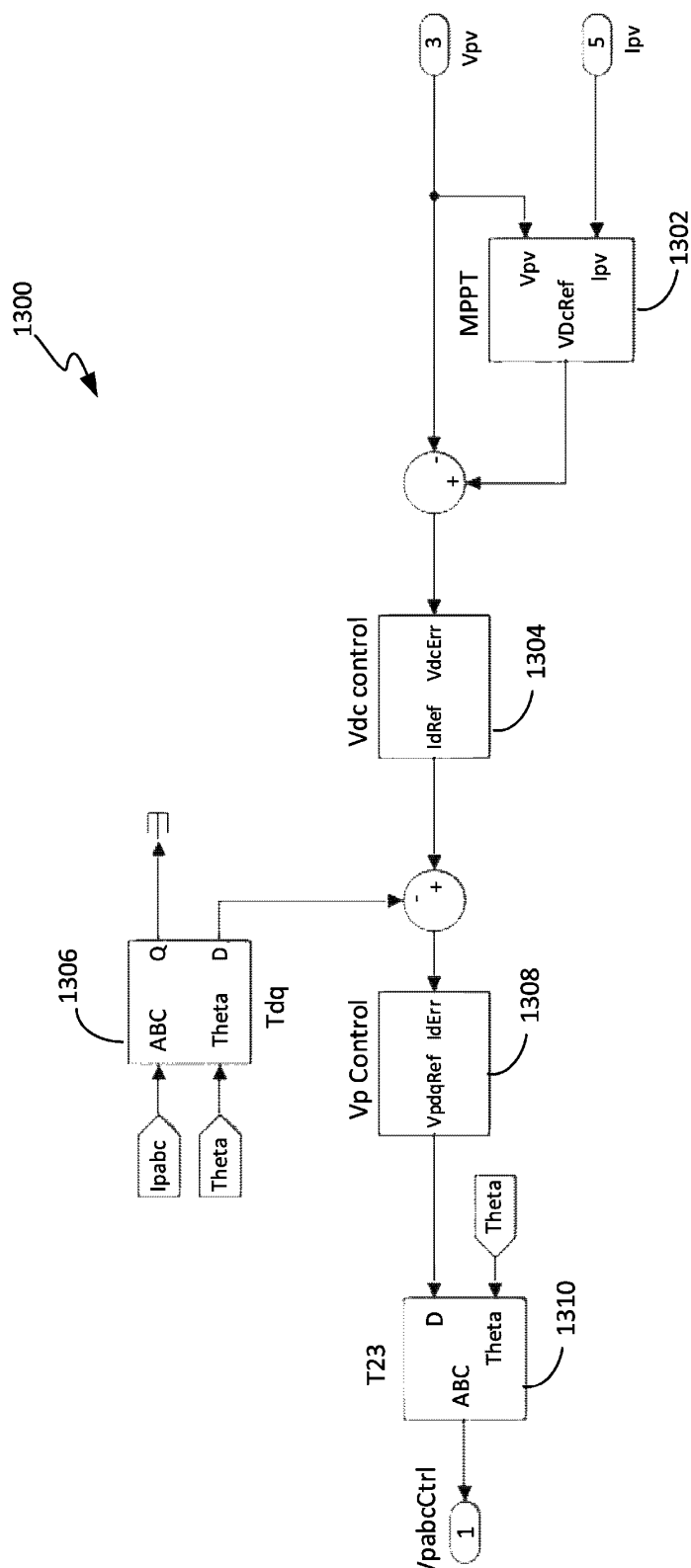


**Figure 13**

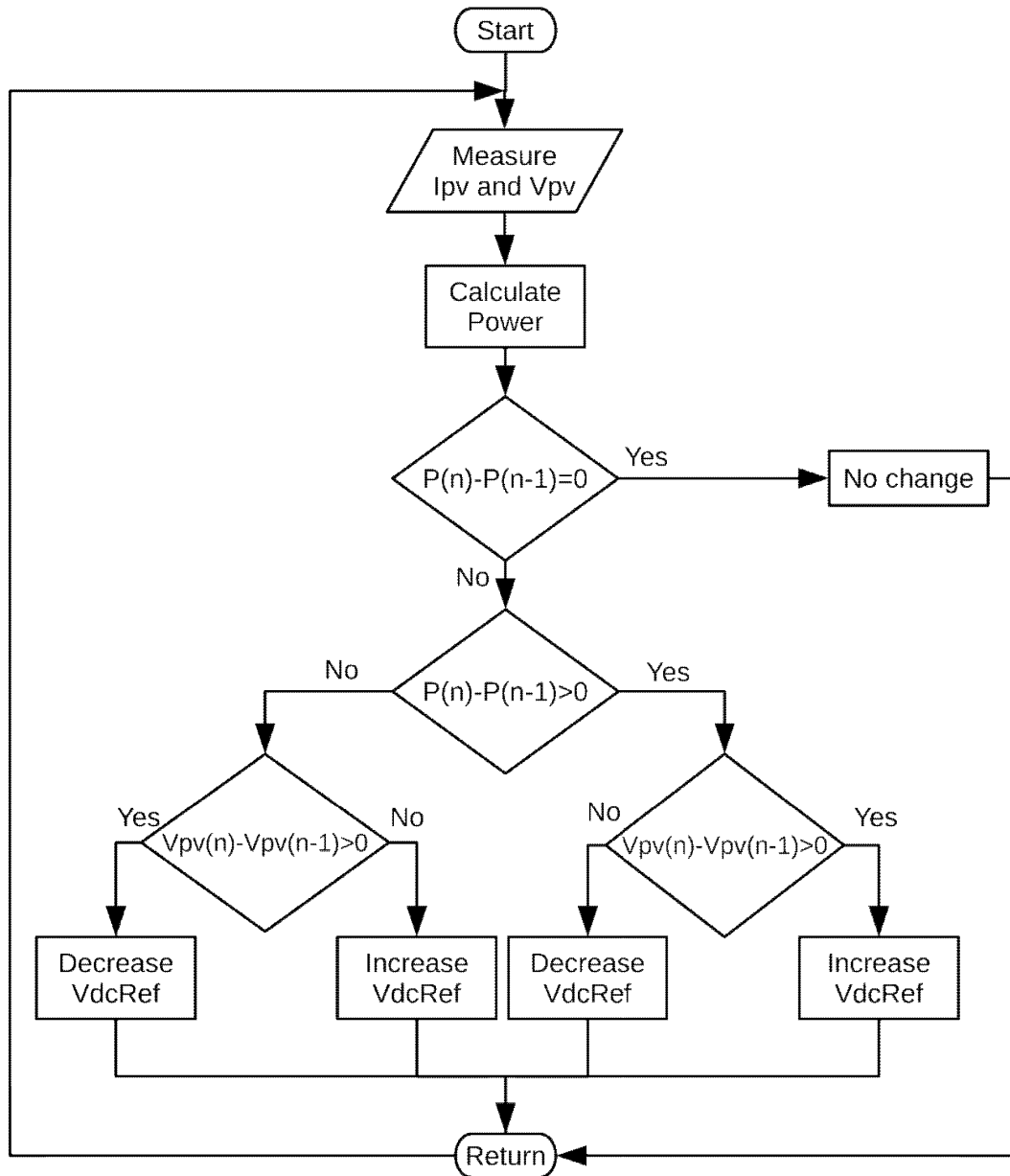


Figure 14

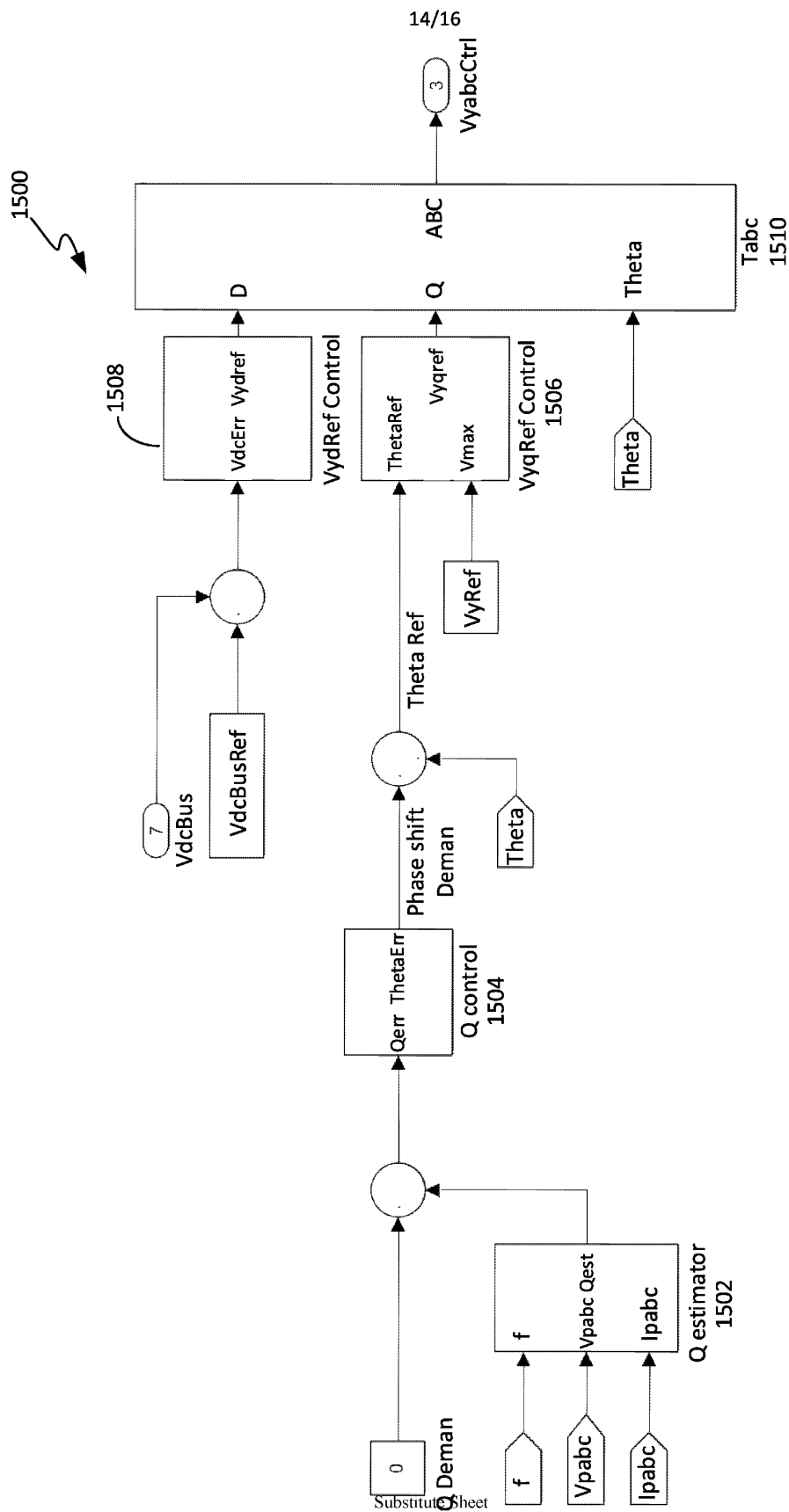


Figure 15

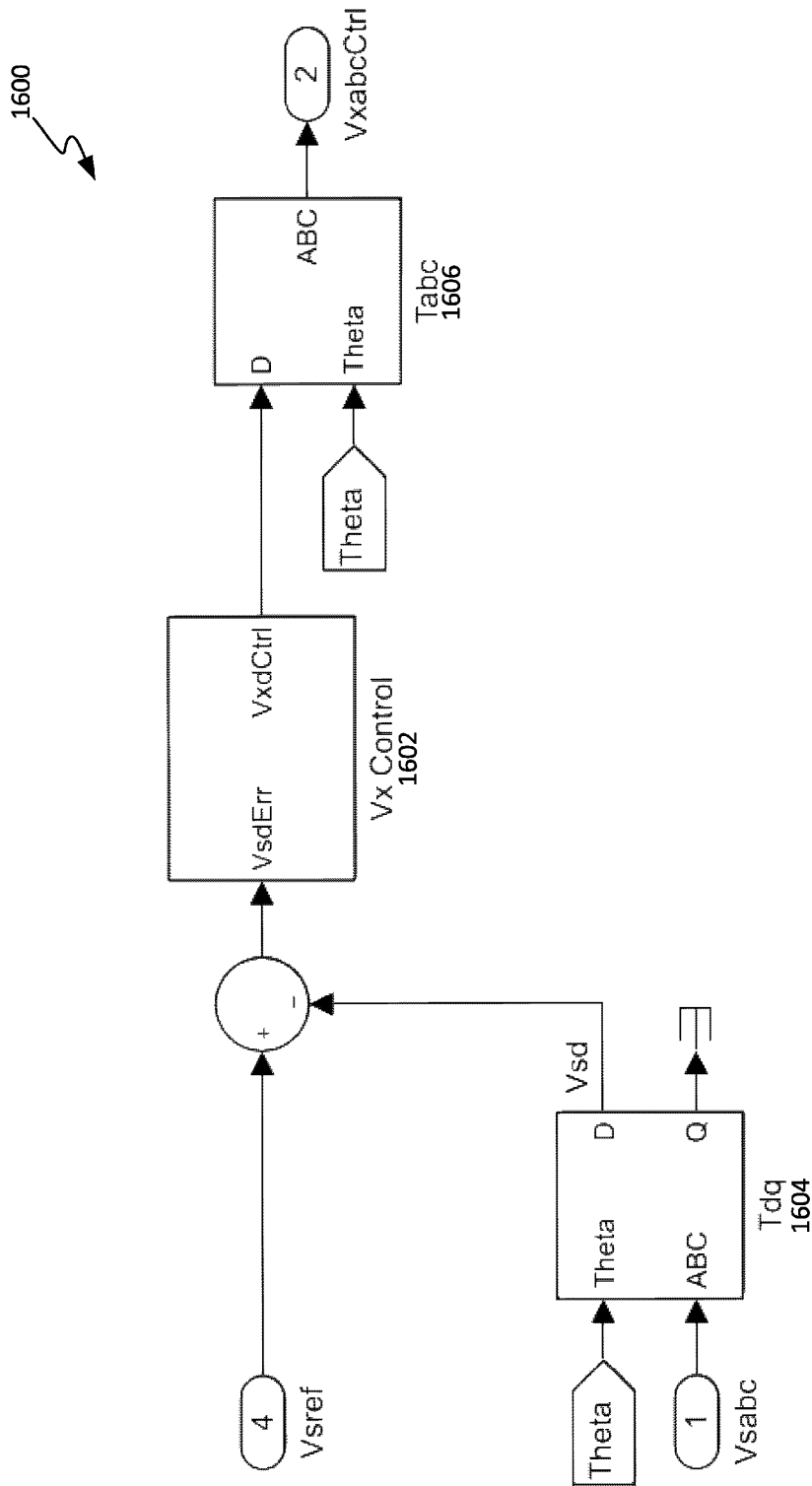


Figure 16

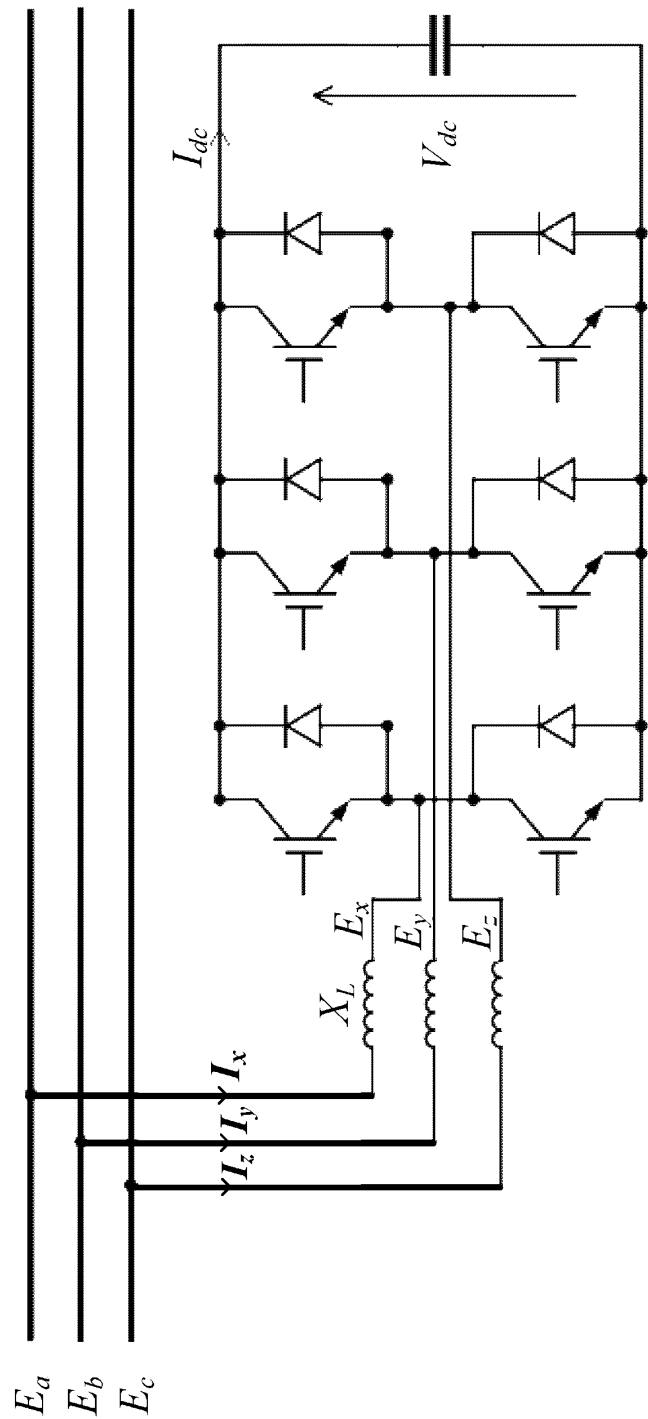


Figure 17