

VOLTAGE REGULATOR
ABSTRACT

This Sheet Has Been Replaced



5 A voltage regulator wherein the series pass transistor
(Q1) and an amplification transistor (Q2) are of complementary
types. The emitter electrode of the amplification transistor (Q2) is
coupled to a voltage which is less than the regulated DC output
voltage (V_o) so that drive requirements for the pair of transistors is
reduced, and the voltage difference between the unregulated input
voltage (V_{in}) and the regulated output voltage (V_o) is minimized
10 under maximum output voltage conditions.

FIGURE 1