

Abstract of the Disclosure (Fig. 2)

SEMICONDUCTOR DEVICE

A semiconductor device of the present invention comprises a plurality of arranged unit semiconductor devices (12) which is designed to compose a desired logic circuitry by selectively connecting among elements in the unit devices and among the unit devices. Each of the unit devices comprises at least a first insulated-gate type field-effect transistor^(11N) of a first conductivity type and a second insulated-gate type field-effect transistor^(11P) of a second conductivity type which is disposed adjacent to the first transistor and has a gate electrode^(14P) separated from a gate electrode^(14N) of the first transistor. The gate electrodes of the respective transistors have at least a gate terminal portion^(11', 12') at the side adjacent to each other, and the gate terminal portion^(11') of the first field-effect transistors has at least a first wire connecting portion and a second wire connecting portion. By using the second wire connecting portion, wiring in and among the unit devices can be carried out via the shortest aluminum wires of the first layer, so that the wiring feasibility can be enhanced.