

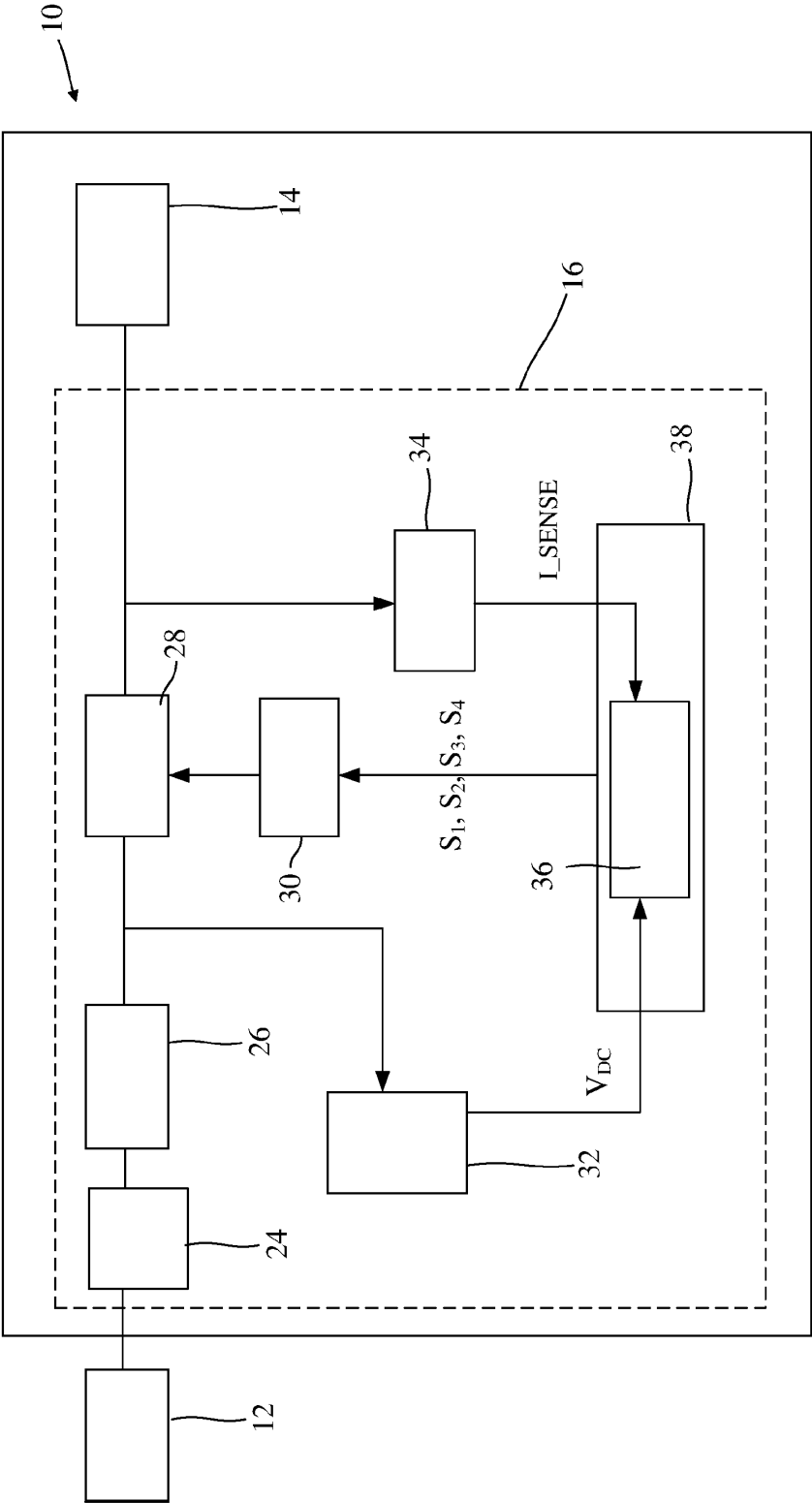


Fig. 1

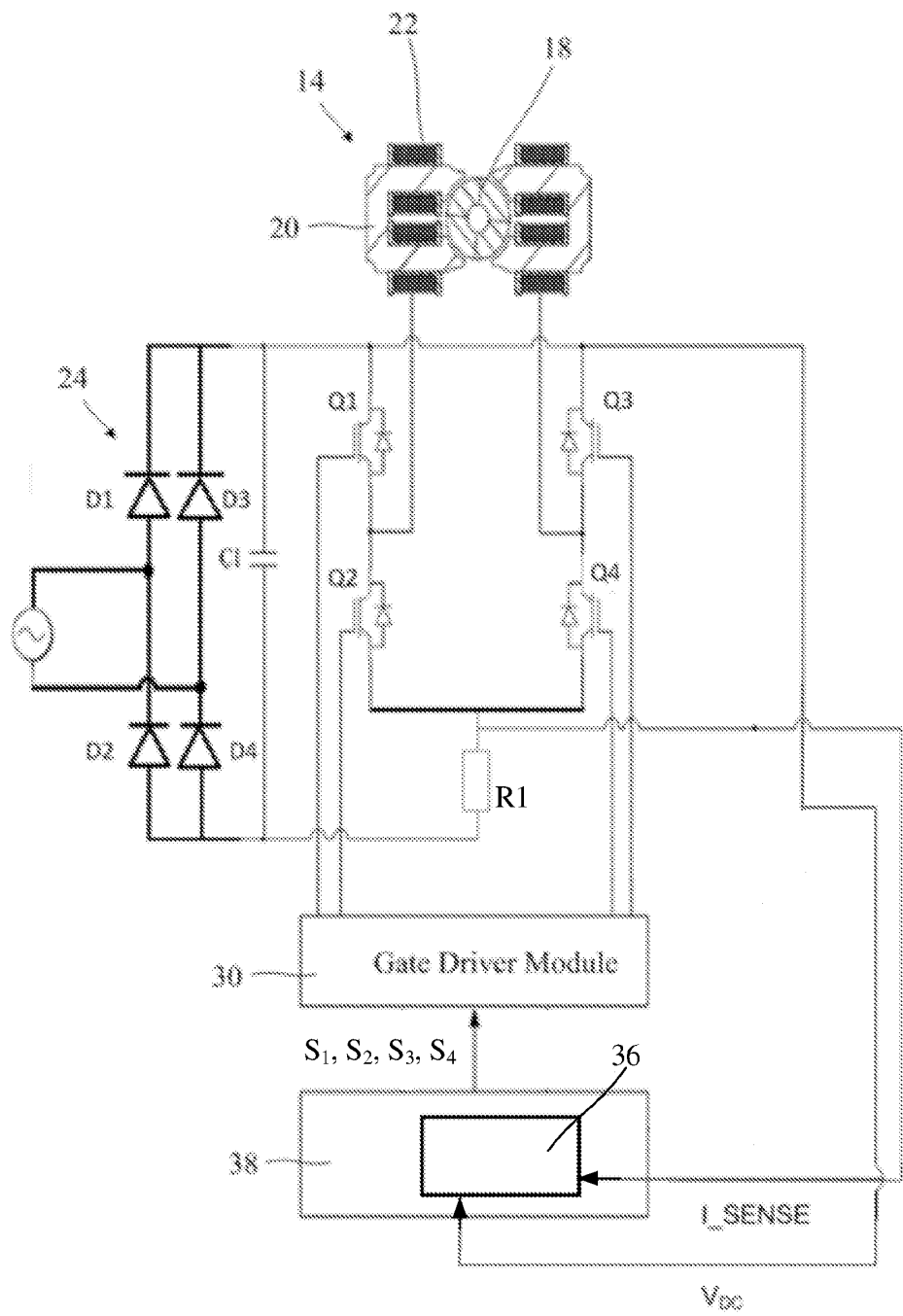


Fig. 2

Control Signals				Power Switches				Inverter Condition
S1	S2	S3	S4	Q1	Q2	Q3	Q4	
0	0	0	0	0	0	0	0	Off
1	0	0	1	1	0	0	1	Excite Left-to-Right
0	1	1	0	0	1	1	0	Excite Right-to-Left
0	1	0	1	0	1	0	1	Body Freewheel
0	0	0	1	0	0	0	1	Diode Freewheel Left-to-Right
0	1	0	0	0	1	0	0	Diode Freewheel Right-to-Left

Fig. 3

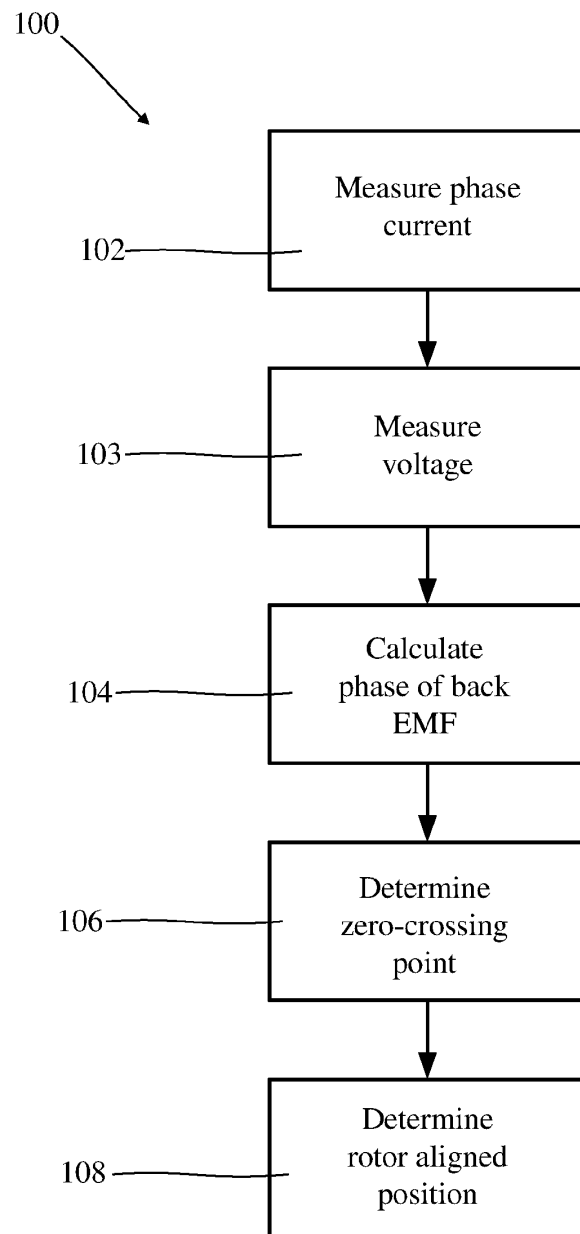


Fig. 4

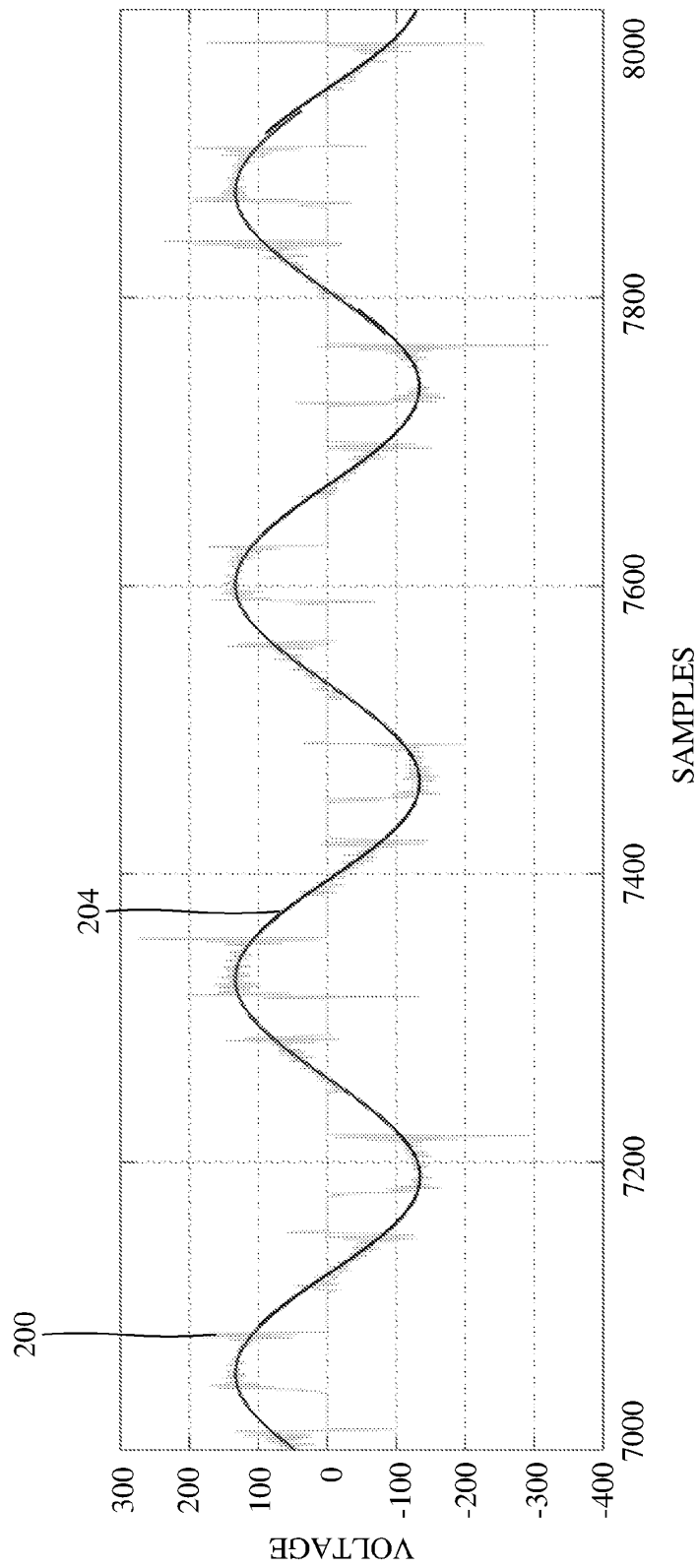


Fig. 5